Spec. No.: HSPC-K-HTS-0001 /3

Date: 2017. 1. 10

Specification

Title: ESD SUPPRESSOR; RECTANGULAR TYPE

Style: HSPC10, 16

RoHS COMPLIANCE ITEM
Halogen and Antimony Free

Product specification contained in this specification are subject to change at any time without notice If you have any questions or a Purchasing Specification for any quality Agreement is necessary, please contact our sales staff.



Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya

Note: Stock conditions

Temperature: $+5^{\circ}\text{C} \sim +35^{\circ}\text{C}$ Relative humidity: $25\% \sim 75\%$

The period of guarantee: Within 2 year from shipmen t by the company.

Solderability shall be satisfied.

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1. Scope

1.1 This specification covers the detail requirements for ESD suppressor; rectangular type, style of HSPC10,16.

2. Classification

Type designation shall be the following form.

(Example)	HSPC	16	701	B	02	TP
	1	2	3	4	5	6
	Stv	le				

1 ESD suppressor; rectangular type ——

2 Size

3 Peak voltage

Symbol	Peak voltage
601	600V
701	700V

4 Rated voltage

- 4		
I	Symbol	Rated voltage
I	Α	30V max
	В	20V max
ľ	С	50V max

5 Optional code

Symbol	Optional code
01	Capacitance: 0.1 pF max.
02	Capacitance: 0.2 pF max.

6 Packaging form

В	Bulk (loose package)	
TH	Donos toninos	
TP	Paper taping	

3. Rating

3.1 The ratings shall be in accordance with Table-1.

Table-1

	ESD capability *1			Rated voltage Capaci	Capacitance	Leakage current
Style	Peak voltage	Clamping voltage	ESD pulse withstand	(V)	(pF) *2	_
	(V)	(V)	(pulses)	(v)	(pr) 2	(μA)
HSPC10	600 max.	100 max.	100	30 max.	0.1 max.	1 max.
LICDC4C	700	100	400	20 max.	0.0	1
HSPC16	700 max.	100 max.	100	50 max.	0.2 max.	1 max.

Style	Category temperature range (°C)
HSPC10	FF 1: . 40F
HSPC16	–55 to +125

^{*1} Peak voltage: IEC61000-4-2, 15kV, Aerial discharge, The peak voltage shall be measured.

Clamping voltage: IEC61000-4-2, 15kV, Aerial discharge, The voltage value shall be measured after 30ns from the peak voltage.

ESD pulse withstand: IEC61000-4-2, 15kV, Aerial discharge, The pulse withstand.

*2 Capacitance: 25°C, 1MHz, 1Vrms

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4. Packaging form

The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	mbol Packaging form		Standard packaging quantity / units	Application
В	Bulk (loose package)		1,000 pcs.	HSPC10, 16
TH	Paper taping	8mm width, 2mm pitches	10,000 pcs.	HSPC10
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.	HSPC16

5. Dimensions

5.1 The suppressor shall be of the design and physical dimensions in accordance with Figure-1 and Table-3.

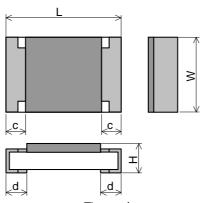


Figure-1

		Table	⊢ 3	ι	Jnit:mm
Style	L	W	Н	С	d
HSPC10	1.0±0.05	0.5±0.05	0.35±0.05	0.2±0.1	0.25±0.10
HSPC16	1.6±0.1	0.8 ^{+0.15}	0.5±0.1	0.3±0.1	0.3±0.1

5.2 Equivalent circuits



5.3 Net weight (Reference)

כ	.5 Net weight (Reference)				
	Style	Net weight(mg)			
	HSPC10	0.6			
HSPC16		2			



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6. Performance

6.1 Unless otherwise specified, the standard range of atmospheric conditions for tests is as follows;

Ambient temperature: 5 °C to 35 °C, Relative humidity: 45 % to 85 %, Air presser: 86 kPa to 106 kPa

If there is any doubt the results, measurements shall be made within the following:

Ambient temperature: 20 °C \pm 2 °C, Relative humidity: 60 % to 70 %, Air presser: 86 kPa to 106 kPa

6.2 The performance shall be satisfied in Table-4.

Table-4(1)

No.	Test items	Condition of test	Performance requirements
1	ESD capability	IEC61000-4-2	See Table-1.
	Peak voltage	The suppressor shall be mounted on the test	
		substrate as shown in Figure–2.	
		Test condition: 15kV, Aerial discharge	
		Measurement: The peak voltage shall be measured.	
2	ESD capability	IEC61000-4-2	100V max.
	Clamp voltage	The suppressor shall be mounted on the test	
		substrate as shown in Figure–2.	
		Test condition: 15kV, Aerial discharge	
		Measurement: The voltage value shall be measured	
		after 30ns from the peak voltage.	
3	ESD capability	IEC61000-4-2	10μA max.
	ESD pulse withstand	The suppressor shall be mounted on the test	·
		substrate as shown in Figure–2.	
		Test condition: 15kV, Aerial discharge	
		Applied pulses: 100 pulses	
		Measurement: After examination, the current value	
		when the rated voltage is applied is measured.	
4	Capacitance	Measurement condition:	See Table-1.
		Frequency: 1MHz±10%	
		Voltage: 1 Vrms±0.2Vrms	
		Ambient temperature:25°C±2°C	
5	Leakage current	Measurement voltage: The rated voltage	1μA max.
		Measurement: The current value when the	
		measurement voltage is applied is measured.	
6	Terminal bond strength of	JIS C 61000-2-21	Leakage current: 10µA max.
	the face plating	The suppressor shall be mounted on the test	No evidence of mechanical
		substrate as shown in Figure–2.	damage.
		Bending value: 3 mm (Among the fulcrums: 90 mm)	
		Duration: 10 s ± 1 s	
7	Resistance to soldering	JIS C 60068-2-58	Leakage current: 10µA max.
	heat	Test by a piece.	No evidence of appearance
		Temp. of solder bath: 260 °C ± 5 °C	damage
		Immersion time: 10 s ± 1 s	
		After immersion into solder, leaving the room temp.	
		for 48h or more, and then measure the leakage	
		current.	
		Reflow soldering Reflow soldering Reflow soldering	
		Pre-heating: 150 °C ~ 180 °C, 120 s max.	
		Peak: 260 °C ± 5 °C, 10 s max.	
		Reflow cycle: 2 times	
		After immersion into solder, leaving the room temp.	
		for 48h or more, and then measure the leakage	
		current.	



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Table 4(2)

No.	Test items	Condition of test	Performance requirements
8	Solderability	JIS C 60068-2-58	The surface of terminal immersed shall
		Test by a piece	be min. of 95 % covered with a new
		Flux: Rosin–Methanol	coating of solder.
		Temp. of solder: bath: 235 °C ± 5 °C	
		Immersion time: $2 s \pm 0.5 s$	
9	Solvent	JIS C 60068-2-45	No evidence of appearance
		The specimen shall be cleansed at normal	damage
		temperature for 90s using Isopropyl alcohol.	
10	Rapid change temperature	JIS C 60068-2-14	Leakage current: 10µA max.
		The suppressor shall be mounted on the test	No evidence of appearance
		substrate as shown in Figure–2.	damage
		Lower temperature: -55 °C	
		Upper temperature: +125 °C	
		Duration of exposure at each temperature: 30 min.	
		Number of cycles: 100 cycles	
		After examination, leaving the room temp. for 48h or	
- 4.4	11 12	more, and then measure the leakage current.	
11	Humidity	JIS C 60068-2-78	Leakage current: 10µA max.
	(Steady state)	The suppressor shall be mounted on the test substrate	No evidence of appearance
		as shown in Figure—2.	damage
		Test temp. & relative humidity: 60±2°C & 90~95% RH.	
		Test period: 1,000 +48 h	
		After examination, leaving the room temp. for 48h or	
12	Load life in humidity	more, and then measure the leakage current. The suppressor shall be mounted on the test substrate	Lockago ourrents 10 A moss
12	Load life in Fidinially	as shown in Figure—2.	Leakage current: 10µA max. No evidence of appearance
		Test temp. & relative humidity: 60±2°C & 90~95% R.H.	damage
		Test voltage: The rated voltage shall be applied	darrage
		continuously.	
		Test period: 1,000 ⁺⁴⁸ ₀ h	
		After examination, leaving the room temp. for 48h or	
		more, and then measure the leakage current.	
13	Endurance at 85 °C	The suppressor shall be mounted on the test substrate	Leakage current: 10µA max.
.0		as shown in Figure 2.	No evidence of appearance
		Test temp.: 85±2°C	damage
		Test voltage: The rated voltage shall be applied	
		continuously.	
		Test period: 1,000 ⁺⁴⁸ ₀ h	
		After examination, leaving the room temp. for 48h or	
		more, and then measure the leakage current.	

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7. Test substrate

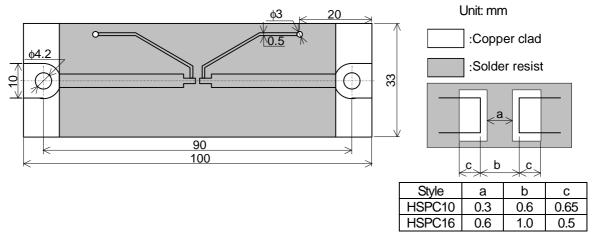


Figure-2 HSPC TEST SUBSTRATE

Remark 1). Material: Epoxide woven glass

Thickness: 1. 6mm Thickness of copper clad: 0. 035mm

8. Taping

- 8.1 Applicable documents JIS C 0806-3:2014, EIAJ ET-7200C: 2010
- 8.2 Taping dimensions
- 8.2.1 Paper taping (8mm width, 2mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.

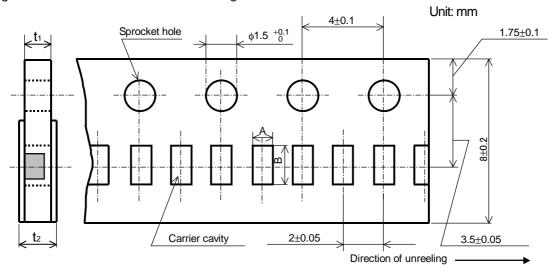


Figure-3

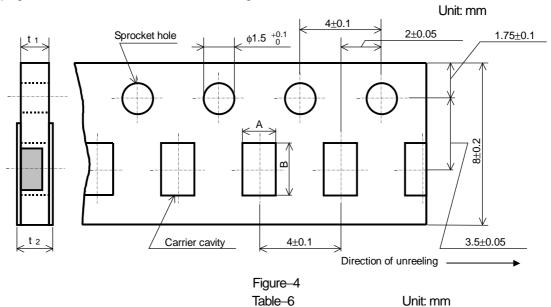
Table–5				Unit: mm
Style	Α	В	t 1	t ₂
HSPC10	0.65 ^{+0.05} _{-0.10}	1.15 +0.05 -0.10	0.4 ± 0.05	0.5max.

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8.2.2 Paper taping (8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-4 and Table-6.

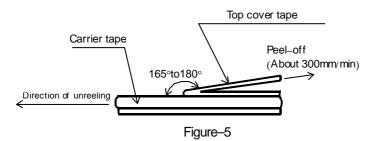


 Style
 A
 B
 t 1
 t 2

 HSPC16
 1.15±0.15
 1.9±0.2
 0.6±0.1
 0.8max

- 1). The cover tapes shall not cover the sprocket holes.
- 2). Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following Figure-5.
- 6). When the tape is bent with the minimum radius for 25 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- 7). In no case shall there be two or more consecutive components missing.

 The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The suppressors shall be faced to upward at the over coating side in the carrier cavity.

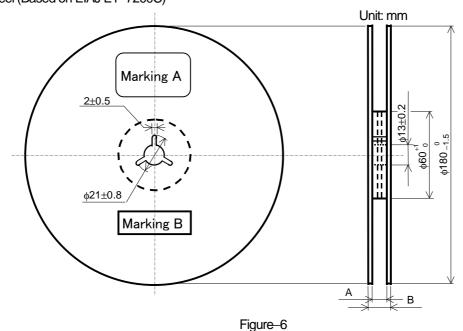


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8.3 Reel dimension

Reel dimensions shall be in accordance with the following Figure–6 and Table–7. Plastic reel (Based on EIAJ ET–7200C)



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

8.4 Leader and trailer tape.

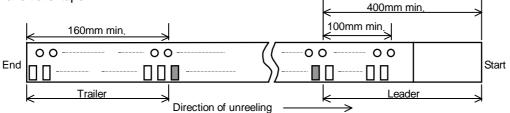


Figure-7

9. Marking on package

The label of a minimum package shall be legibly marked with follows.

9 1 Marking A

- (1) Classification (Style, Peak voltage, Rated voltage, Optional code, Packaging form) (2) Quantity (3) Lot number
- (4) Manufacturer's name or trade mark (5) Others
- 9.2 Marking B (KAMAYA Control label)

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ESD119B1W01005E6327XTSA1 ESD5V0J4-TP ESD5V0L1B02VH6327XTSA1 ESD7451N2T5G 19180-510 CPDT-5V0USP-HF
3.0SMCJ33CA-F 3.0SMCJ36A-F HSPC16701B02TP D3V3Q1B2DLP3-7 D55V0M1B2WS-7 DESD5V0U1BL-7B DRTR5V0U4SL-7
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