

CERAMIC DISC CAPACITOR SAFETY RECOGNIZED, AC SERIES

POE-D11-02-E-25

Ver: 25

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PRODUCT SPECIFICATION

PRODUCT: CERAMIC DISC CAPACITOR SAFETY RECOGNIZED

TYPE: AC SERIES

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DOC. NO.: POE-D11-02-E-25

APPROVED BY CUSTOMER

PASSIVE SYSTEM ALLIANCE

VENDOR:

■ WALSIN TECHNOLOGY CORPORATION

566-1, KAO SHI ROAD,YANG-MEI TAO-YUAN, TAIWAN

PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

NO.277,HONG MING ROAD,EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE,CHINA

□ DONGGUAN WALSIN TECHNOLOGY ELECTRONICS CO., LTD.

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MANUFACTURE SITE:

V PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

NO.277,HONG MING ROAD,EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE,CHINA





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Record of change

Date	Version	Description	page
2008.6.3	1	1. C23-00-C-01(before) → POE-C11-00-C-01(1st edition)	
2008.8.22	2	1 Complete lead code	20
		2. Add last SAP code "H" for halogen and Pb free, epoxy resin	3
2008.12.12	3	1.Complete the 13th to 17th codes of SAP P/N.	4
2000 7.16		2. Page layout adjustment.	
2009.7.16	4	1 Change PSA & POE logo to Walsin & POE logo.	0
		2.Complete Marking statement. 3. Revised standard NO. of SEV, SEMKO, FIMKO, NEMKO, DEMKO and KEMA.	9 11
		Revised recognized NO. of FIMKO, NEMKO, DEMKO, KEMA and CQC.	11
		4. Downsize:	6
2009.9.14	5	1. "Protrusion length": "+0.5 to-1.0" revised to "2.0max (Or the end of lead wire may be inside the	9
		tape.)"	
2009.12.24	6	1. Marking	10
		2. Correct recognized No	11
		3. Revised the Figure of impulse voltage test(Item 7.3.14) according to the standard IEC 60384-14 ed.3	14
2011/1/13	7	1. Review SAP P/N about diameter code:	6
		2. Delete "AT" taping type.	4,5,8,9
		 Add test item "Temperature Cycle". Add item 10 "Drawing of internal structure and material list" 	15 20
2011/4/27	8	4. Add item 10 "Drawing of internal structure and material list" 1. Add "1AC" type;	4
2011/4/27	0	2. Delete "old P/N"	6
		3. Define the marking of the type "OAC" and "1AC";	8
		4. Review the "Standard No. & Subclass & W.V. & Recognized No".	9
2012/2/7	9	1. Review the "Standard No. & Subclass & W.V. & Recognized No".	9
		2. Review the "Operating Temperature Range" from "-25 to +125°C" to "-40 to +125°C"	10
		3. Review the temperature of Step 1 from "-25+0/-3" to "-40+0/-3"	14
2012/4/6	10	1. In order to improve the traceability of the product, change the date code on capacitor body, new date code can	
		trace back to production "Lot No."	8
		1. Review the Lead diameter φ from 0.60 +0.1/-0.05mm to 0.55+/-0.05mm	5,6,7
		2. In order the customer to know the round time of manufacture, review the date code on capacitor body, new	8
2013/5/6	11	date code can know the month of manufacture.	0
		 3. Delete "No marked with " stand for Pb free". Add "epoxy resin" 4. Review the Solderability time from 2±0.5s to 5±0.5s 	8 11
			8
		 Review the "Manufactured Date" to "Products ID" on the marking page Delete "The marking can be printed on either one side or two side of coating body. "and add "for SAP 	8
2013/10/16	12	part number 10-11 digits \(\) '07' products' to two sides and "for SAP part number 11-12 digits	0
		≥ '08' products"/to one side.	
		1. Review the terminal position of the lead wire.	8
2014/11/5	13	2. Review the product of ID, add the code "D" for the products of Dongguan Walsin Technology	9
2014/11/3	13	Electronics Co., Ltd.	
		3. Review the minimum packing quantity of taping code AM.	16
2014/12/25	14	1. Add "3.1Norminal parts&3.2 special for surge parts" for "3. Part Numbering /T.C/Capacitance/	7
		Tolerance/Diameter"	
2015/5/27	15	Add the X1:440Vac/Y2:300Vac safety approval for CQC.	4,10
2015/8/4	16	Delete the H(Inside kink lead)	5,8
2015/11/12	17	1. Review the normal parts of Taping type	6,7
2015/11/12	17	2. Review Marking	9
2016/1/27	18	1. Review the Available lead code of Lead Configuration	5
2010/1/2/	10	2. Revised standard NO. of VDE, SEV, SEMKO, FIMKO, NEMKO, DEMKO and KTL.	10
0016/5/5	10	1. Delete 6 pF~10 pF for P/N CH*AC***D06 * * , 12 pF~15 pF for P/N CH*AC120J06 * * ,18 pF~24 pF	
2016/5/3	19	for P/N CH*AC***J07 * *, 27 pF~33 pF for P/N CH*AC***J08 * *, and 36 pF~39 pF for P/N	6
		CH*AC***J09**.	_
2016/11/1	20	Review the Available lead code of Lead Configuration Polyto, "CH", agrical	5
2016/11/1	20	Delete "CH" series. Revised the Marking for LAC type.	4,6,11~15,20
	1	3. Revised the Marking for 1AC type.	У



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Record of change (continue)

Date	Version	Description	
2017/6/26	21	1. Revise CQC Standard No.	10
2018/8/11	22	1. Revised standard NO. of VDE, SEV, SEMKO, FIMKO, NEMKO and DEMKO.	10
2019/2/25	23	1. Delete "3.2 Special design parts" for surge withstanding	7
2019/4/24	24	 "Protrusion length": "2.0max (Or the end of lead wire may be inside the tape.)" revised to "+0.5to-1.0 (Or the end of lead wire may be inside the tape.)" Add "Soldering Recommendation" 	7 18
2019/12/11	25	 Review the Available lead code of Lead Configuration Add "8.3 Label samples" 	5 14

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1. Part number for SAP system

(Ex.) \underline{YV} $\underline{0}$ \underline{AC} $\underline{472}$ \underline{M} $\underline{10}$ $\underline{0}$ \underline{L} $\underline{20}$ \underline{C} $\underline{7}$ \underline{H} (1) (2)-1 (2)-2 (3) (4) (5) (6) (7) (8) (9) (10) (11)

(1)Temperature characteristic (identified code)

CODE	Temperature characteristic	Cap. Change
SL	SL	-1000~+350ppm/°C (+20°C~+85°C)
YP	Y5P	±10%
YU	Y5U	-55% to +20%
YV	Y5V	-80% ~ +30%

- (2)-1 Rated voltage(identified by 1-figure code) : 0 = X1:400Vac/Y2:250Vac; 1 = X1:440Vac/Y2:300Vac
- (2)-2 Type(identified by 2-figure code): AC
- (3) Capacitance (identified by 3-figure code) : EX.221=220pF
- (4)Capacitance tolerance (identified by code) : J:±5%,K:±10%,M:±20%
- (5) Nominal body diameter dimension (identified by 2-figure code): 06--Dmax7.0mm, 07--Dmax8.0mm...
- (6)Internal code: 0--Normal, other code--Special control
- (7)Lead Style: Refer to "2. Mechanical".

(8)Packing mode and lead length (identified by 2-figure code)

Taping Code	Description
AF	Ammo box and product pitch: 15.0 mm
AM	Ammo box and product pitch: 25.4 mm

	/ ////
Bulk Code	Description
03	Lead length: 3.0mm
3E	Lead length: 3.5mm
04	Lead length := 4.0mm system A
4E	Lead length: 4.5mm
20	Lead length: 20.0mm

(9) Tolerance of lead length

Code	Description		
A	±0.5 mm	Short lead	
	(only for kink lead type)	Short lead	
В	±1.0 mm	Short lead	
C	Min.	Long lead	
D	Taping special purpose	Taping	

(10)Lead space

Code	Description		
7	7.5±1.0 mm		
M	7.5±0.5 mm		
0	10±1.0 mm		
A	10±0.5 mm		

(11)Epoxy resin code

Code	Description		
В	Halagan and Dh. free anavy regin		
Н	Halogen and Pb free, epoxy resin.		



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2. Mechanical

Encapsulation: Epoxy resin, flammability UL94 V-0

Available lead code(unit: mm)

Available lead co	ode(unit: mm)				
Lead type	SAP P/N (13-17)digits	Lead space (F)	Lead Length (L)	Packing	Lead Configuration
	L03B7	7.5 ± 1.0	3.0 ± 1.0		
	L4EB7	7.5 ± 1.0	4.5 ± 1.0		_
	L05B7	7.5 ± 1.0	5.0 ± 1.0		D max.
	L03B0	10 ± 1.0	3.0 ± 1.0	Bulk	
Lead style: L or B	L4EB0	10 ± 1.0	4.5 ± 1.0]	For
Type L or B	L05B0	10 ± 1.0	5.0± 1.0		L≧20mm
Straight lead	L20C7	7.5 ±1.0	20 min.	-	P F AT
Straight touc	L20C0	10 ± 1.0	20 min.	-	' For
	BAFD7	7.5 ±1.0			L<20mm
	BAMD7	7.5 ± 1.0	Refer to "4.	Tap. Ammo	0 0
	BAMD0	10 ± 1.0	Taping format"	1	
	G03A7	7.5 ± 1.0	3.0 ± 0.5		_
				_	D max. T max.
	G3EA7	7.5 ± 1.0	3.5 ± 0.5	-	
	G04A7	7.5 ± 1.0	4.0 ± 0.5	Bulk	
Lead style: G	G03A0	10 ± 1.0	3.0 ± 0.5		{ }
·	G3EA0	10 ± 1.0	3.5 ± 0.5	E C	, \
Type G	G04A0	√ 10 ± 1.0	-4.0 ± 0.5		- High War
Straight lead	GAFD7	7.5 ±1.0	可以目众	141	F THE
	GAMD7	7.5 ±1.0	Refer to "4. Taping format"	Tap. Ammo	Ø d-
	GAMD0	10 ± 1.0		凹	□ 2 m □ 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
	D03A7	7.5 ± 1.0	3.0 ± 0.5		
	D3EA7	7.5 ± 1.0	3.5 ± 0.5		D max. T max,
	D04A7	7.5 ± 1.0	4.0 ± 0.5	.9 意	
Lead style: D	D03A0	10 ± 1.0	3.0 ± 0.5	Bulk	
	D3EA0	10 ± 1.0	3.5 ± 0.5		I ()
T D	D04A0	10 ± 1.0	4.0 ± 0.5	10/12	ž ž ž
Type D	D20C7 D20C0	7.5 ± 1.0 10 ± 1.0	20 min. 20 min.	MIL.	4
Vertical kink lead	DAFD7	7.5 ±1.0	CA UBDLIKHIO		
	DAMD7	7.5 ± 1.0	Refer to "4.		Ø d→
	DAMD0	10 ± 1.0	Taping format"	Tap. Ammo	<u> </u>
	X03A7	7.5 ± 1.0	3.0 ± 0.5		D max. T max.
	X3EA7	7.5 ± 1.0	3.5 ± 0.5		D max. T max.
	X04A7	7.5 ± 1.0	4.0 ± 0.5]	
Lead style: X	X05B7	7.5 ± 1.0	5.0 ± 1.0	Bulk	V \ \
	X03A0	10 ± 1.0	3.0 ± 0.5	Duik	
Type X	X3EA0	10 ± 1.0	3.5 ± 0.5		1 /2 1/2
	X04A0	10 ± 1.0	4.0 ± 0.5	-	i i i i i i i i i i i i i i i i i i i
Outside kink lead	X05B0	10 ± 1.0	5.0 ± 1.0		- <u>- </u>
	XAFD7 XAMD7	7.5 ± 1.0 7.5 ± 1.0	Refer to "4.	Tom A	"↑ F →
	XAMD/ XAMD0	7.3 ± 1.0 10 ± 1.0	Taping format"	Tap. Ammo	[ø d+[+ []t
	AMINIDU	10 ± 1.0			

^{*} Lead diameter Φd: 0.55+/-0.05mm

 $[\]hbox{* C (Coating $\textbf{extension}$ on leads): 3.0mmMax $for straight lead style; Not exceed the kink for kink lead.}$



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3. Part numbering/T.C/Capacitance/ Tolerance/Diameter:

3. I alt humbering	1.0/0	apacitance/ Toler	ance/Diam		Di	mension	ns (unit: mm)	
SAP Part. No.	T.C.	Capacitance	Tolerance	D	Т		F	
SAF Fait. No.	1.C.	•	Tolerance	(max)	(max)	Bulk type	Taping type	φd
SL*AC***J060*		10,12,15,18,20,22,2 4,27,30,33, 36,39,47,50,51(pF)	±5%	7.0				
SL*AC***J070*	SL	56,62, 68,75(pF)	±5%	8.0				
SL*AC820J080*		82pF	±5%	9.0				
SL*AC101J090*		100pF	±5%	10.0				
YP*AC101K060*		100 pF	±10%	7.0				
YP*AC151K060*		150 pF	±10%	7.0			7.5±1	
YP*AC221K060*		220 pF	±10%	7.0			(AFD7)	
YP*AC331K060*	1	330 pF	±10%	7.0				
YP*AC471K060*	Y5P	470 pF	±10%	7.0				
YP*AC561K070*		560pF	±10%	8.0				
YP*AC681K070*	1	680 pF	±10%	8.0				
YP*AC821K080*		820 pF	±10%	9.0				
YP*AC102K080*		1000 pF	±10%	9.0				
YU*AC102M060*		1000 pF	±20%	7.0			7.5±1	
YU*AC152M080*	1	1500 pF	±20%	9.0	5.0	7.5±1,	(AFD7) Or	0.55+/-0.05
YU*AC222M080*	1	2200 pF	±20%	9.0		10±1	10±1 (AMD0)	0.0017 0.00
YU*AC332M100*	Y5U	3300 pF	±20%	11.0				
YU*AC392M120*		3900 pF	±20%	13.0			7.5±1 (AMD7) Or	
YU*AC472M120*		4700 pF	±20%	13.0	2		10±1 (AMD0)	
YV*AC102M060*		1000 pF	±20%	7.0	150			
YV*AC152M060*		1500 pF	±20%	7.0	5/7/7		7.5±1	
YV*AC222M060*		2200 pF	±20%	7.0	الت		(AFD7) Or	
YV*AC332M080*		3300 pF	±20%	<u>9.0</u>			10±1	
YV*AC392M100*	Y5V	3900 pF	ту∈±20% ∈м	11.0		-	(AMD0)	
YV*AC472M100*] 13 V	4700 pF	±20%	11.0	5 6			
YV*AC682M120*		6800 pF	±20%	13.0			7.5±1 (AMD7) Or	
YV*AC103M140*		10000 pF	±20%	15.0	THE STATE OF THE S		10±1 (AMD0)	

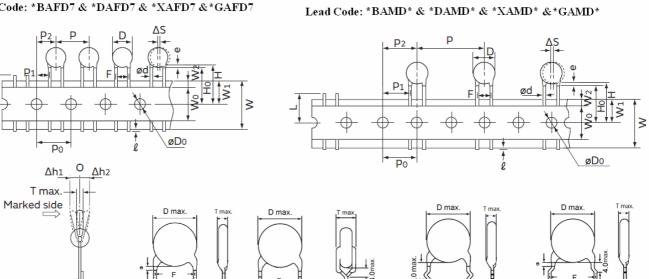


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4. Taping Format

• 15 mm pitch/lead spacing 7.5mm taping Lead Code: *BAFD7 & *DAFD7 & *XAFD7 &*GAFD7



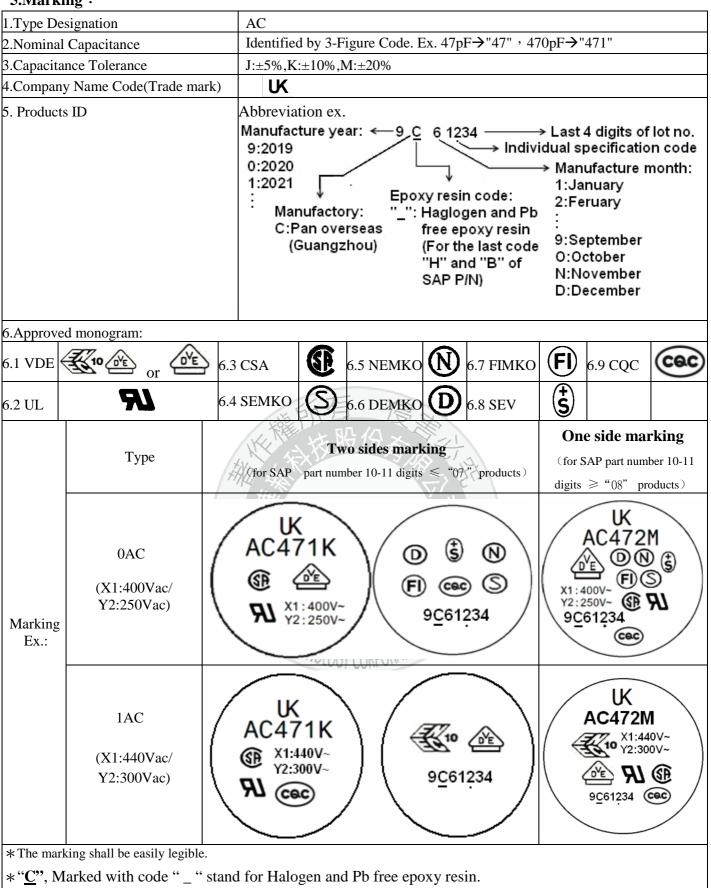
• 25.4mm pitch/lead spacing 7.5mm & 10.0mm taping

POE Part Number		*BAFD7 / *DAFD7 *XAFD7 / *GAFD7	*BAMD7 / *DAMD7 *XAMD7 / *GAMD7	*BAMD0 / *DAMD0 *XAMD0 / *GAMD0	
Item	Symbol	Dimensions (mm)	Dimensions (mm)	Dimensions (mm)	
Pitch of component	PET	15.0±1.0	25.4±2.0	25.4±2.0	
Pitch of sprocket	P0	15.0±0.3	12.7±0.3	12.7±0.3	
Lead spacing	F_\	7.5±1.0	7.5±1.0	10.0±1.0	
Length from hole center to component center	P2	7.5±1.5	12.7±1.5	12.7 ± 1.5	
Length from hole center to lead	P1	3.75±1.0	8.95±1.0	7.7±1.5	
Body diameter	#/D	See the "3. Part num	mbering/T.C/Capacitance/	Tolerance/Diameter"	
Deviation along tape, left or right	\triangle S		0±2.0		
Carrier tape width	Westv	E SYSTEM ALLIANCE	18.0 +1/-0.5		
Position of sprocket hole	W1		9.0±0.5		
Lead distance between the kink and center of	(C):	18.0+2.0/-0	18.0+2.0/-0	18.0+2.0/-0	
sprocket hole	НО	(For: *DAFD7 / *XAFD7/ *GAFD7)	(For: *DAMD7 / *XAMD7 / *GAMD7)	(For: *DAMD0 / *XAMD0 / *GAMD0)	
Lead distance between the bottom of body	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	20.0+1.5/-1.0	20.0+1.5/-1.0	20.0+1.5/-1.0	
and the center of sprocket hole	H.CHNI	(For: *BAFD7)	(For: *BAMD7)	(For: *BAMD0)	
Length from the terminal of the lead wire to the edge of carrier tape	ℓ	+0.5 to -1.0 (Or the en	nd of lead wire may be inside	e the hole-down tape.)	
Diameter of sprocket hole	D0	4.0±0.2			
Lead diameter	φd	0.55±0.05			
Total tape thickness	t1	0.6±0.3			
Total thickness, tape and lead wire	t2	1.5 max.			
Deviation across tape	△ h1/△ h2	2.0 max.			
Portion to cut in case of defect	L	11.0 max.			
Hole-down tape width	W0	8.0 min			
Hole-down tape distortion	W2	1.5±1.5			
Coating extension on leads	e	3.0 max for straight lead style; Not exceed the kink leads for kink lead.			
Body thickness	T	See the "3. Part num	mbering/T.C/Capacitance/	Tolerance/Diameter"	



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5.Marking:



*" • ": Individual specification code, it is added under the lot no.



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6. Scope

THIS SPECIFICATION APPLIES TO CERAMIC INSULATED CAPACITORS DISK TYPE USED IN ELECTRONIC EQUIPMENT.

1. VDE/SEV/SEMKO/FIMKO/NEMKO/DEMKO/ UL/CSA recognized capacitor for Antenna coupling and AC line-by-pass.X1, Y2 Capacitor based on IEC 60384-14 "UL, CSA recognized for across-the-line, line-by-pass" and antenna-isolation.

2. Approval Standard and Recognized No.

Safety Standard	Standard No.	Subclass	w.v.	Recognized No.	
UL	ANSI/UL 60384-14:2013	X1	400VAC or 440VAC	E146544	
UL	ANSI/UL 60364-14.2013	Y2	250VAC or 300VAC	Е140344	
CSA	CAN/CSA E60384-14:2009	X1	400VAC or 440VAC	2347969	
CSA	CAIVCSA E00384-14.2009	Y2	250VAC or 300VAC	2347909	
VDE	EN 60384-14:2013/A1:2016 IEC 6.384-14:2013	X1	400VAC or 440VAC	40001829	
(ENEC)	IEC 0.364-14;2013 IEC 6.384-14;2013/AMD1;2016	Y2	250VAC or 300VAC	40001829	
CEV	EN 00004 44:0040 - 44:40	X1	400VAC or 440VAC	10.0652	
SEV	EN 60384-14:2013 + A1:16	Y2	250VAC or 300VAC	18.0653	
SEMKO	EN 60384-14:2013+A1	右X1	400VAC or 440VAC	1811994	
SEMIKO		Y2	250VAC or 300VAC	1811994	
FIMKO	EN 60384-14:2013 + A1:16	X17	400VAC or 440VAC	NSC FI 30460	
FIMIKO	EN 00384-14.2013 # A1.10	Y2	250VAC or 300VAC	NSC F1 30400	
NEMKO	EN 60384-14:2013;A1	X1	400VAC or 440VAC	P18222947	
NEWIKO	EN 00384-14.2013,A1	Y2	250VAC or 300VAC	F1022294/	
DEMKO	EN 60384-14:2013/A1:2016	X1	400VAC or 440VAC	D-07617	
DEMIKO	EN 60384-14:2013 PASSIV	= SYEY2M AL =A250VAC or 300VAC		D-0/61/	
GOG	GB/T6346.14-2015	X1:40	00VAC /Y2:250VAC	CQC08001026519	
CQC	IEC60384-14:2013	X1: 44	40VAC/Y2:300VAC	CQC15001121984	
	ASM TO	noxig	400VAC or 440VAC	SU03065-14001A	
KTL	K60384-14 2006	Incv¥2ppnR	250VAC	SU03065-14002A	
		Y2 Y2	300VAC	SU03065-14003A	



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7. Specification and test method

7.1 Operating Temperature Range :

-40 to +125°C

7.2 Test condition:

Test and measurement shall be made at the standard condition. (temperature $15\sim35^{\circ}$ C, relative humidity $45\sim75\%$ and atmospheric pressure $860\sim1060$ hpa). Unless otherwise specified herein.

If doubt occurred on the value of measurement, and measurement was requested by customer capacitors shall be measured at the reference condition. (temperature $20\pm2^{\circ}\text{C}$ or $25\pm2^{\circ}\text{C}$, relative humidity $60\sim70\%$ and atmospheric pressure $860\sim1060$ hpa.)

7.3 Performance:

	Item		Specification	Testing Method			
1	Appearance and D	Dimension	No visible defect, and dimensions are within specified range.	The capacitor should be visually inspected for evidence of defect. Dimensions should be measured with slide calipers.			
2	Marking		To be easily legible.	The capacitor should be visually inspected.			
		Between lead wires	No failure.	The capacitors shall not be damage when AC2600V(rms.) are applied between the lead wires for 60 sec. (Charge/Discharge current \leq 50mA.)			
3	Dielectric Strength	Body Insulation	No failure.	First the terminal of capacitor shall be connected together. Then a metal foil shall be closely wrapped around the body of the capacitor distance of about 3 to 6 mm from each terminal. Then the capacitor shall be inserted into a container filled with metal balls of about 1 mm diameter. Finally. AC2600V(rms.) is applied for 60 sec. between the capacitor lead wires and metal balls. (Charge/Discharge current ≤ 50mA.)			
4	Insulation Resist	tance(I.R.)	10000MΩ min.	The insulation resistance shall be measured with 500±50VDC with 60±5sec. of charging.			
5	Capacitance		Within specified tolerance	Y5P&Y5U&Y5V: The capacitance shall be measured at 20±2°C with			
6	Dissipation Fact Q	or(D.F.) or	$ \begin{array}{c c} Char. & Specification \\ \hline Y5P \\ Y5U & D.F \leq 2.5\% \\ \hline Y5V & D.F \leq 5.0\% \\ \hline SL & Q: \geq 1000 \ (\ C \geq 30 pF) \\ Q: \geq 400 + 20 \times C \ (\ C < 30 Pf) \\ \end{array} $	IkHz±20% and 5V(rms.) or less. SL: The capacitance shall be measured at 25°C with 1MHz±20% and 1.0±0.2Vrms			
7	Temperature C	haracteristic	Char. Capacitance Change Y5P Within ± 10% Y5U Within ± ½ 0 % Y5V Within −80 ~ +30% -1000~+350 ppm/°C SL (+20°C ~+85°C)	The capacitance measurement shall be made at each step specified in table. Step 1 2 3 4 5 Temp.(°C) +20±2 -25±2 +20±2 +85±2 +20±2 Pr-treatment: Capacitor shall be stored at 125±2°C for 1 hour. Then placed at room condition for 1(*) 24±2 hours before measurement			
capacitor and apply a tensile weigh			As shown in the figure at right, fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec.				
		Bending	Lead wire shall not cut off capacitor shall not be broken.	W Each lead wire should be subjected to 5N of weight and bent 90° at the point of egress, in one direction, then returned to its original position and bent 90° in the opposite direction at the rate of one bend in 2 to 3 sec.			

^{* &}quot;room condition" temperature : 15~35°C, humidity : 45~75%, atmospheric pressure : 86~106kPa

[&]quot;C" expresses nominal capacitance value (pF).



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	Item		Specification	Testing Method
9	Solderability of leads		Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of capacitor should be dipped into molten solder for 5 ± 0.5 sec. The depth of immersion is up to about 1.5 to 2.0 mm from the root of lead wires. Temp. of solder : Lead free solder (Sn-3Ag -0.5 Cu)
		Appearance I.R. Dielectric	No marked defect $1000 \text{M}\Omega$ min.	As shown in figure, the lead wires should be immersed in solder of $350 \pm 10 ^{\circ}\text{C}$ or $260 \pm 5 ^{\circ}\text{C}$ up to 1.5 to 2.0mm from the root of Terminal for $3.5 \pm 0.5 \text{sec} (10 \pm 1 \text{sec} \text{for} 260 \pm 5 ^{\circ}\text{C})$
	Soldering Effect (Non-Preheat)	Strength Capacitance Change	Y5P,Y5U,Y5V: Within ±10% SL: Within±2.5% or ±0.25pF,Whichever is large.	Thermal Capacitor Screen 1.5 1.5 Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour then placed at * 1 room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at * 1 room condition.
10		Appearance I.R.	No marked defect. 1000MΩ min. PASSIVE SYSTEM	First the capacitor should be stored at $120 + 0 / -5$ °C for $60 + 0 / -5$ sec. Then, as in figure, the lead wires should be immersed solder of $260 + / -5$ °C up to 1.5 to 2.0 mm from the root of terminal for 7.5 $+0 / -1$ sec.
	Soldering Effect (On-Preheat)	Dielectric Strength	Per Item 3.	Thermal Screen. 1.5 to 2 0mm Molten Solder
		Capacitance Change	Y5P,Y5U,Y5V: Within ±10% SL: Within±2.5% or ±0.25pF,Whichever is large.	Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at * 1 room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at * 1 room condition.

 $[\]mbox{\%}$ "room condition" temperature $\mbox{:}\ 15\mbox{-}35\mbox{°C}$, humidity $\mbox{:}\ 45\mbox{-}75\mbox{\%}$,atmospheric pressure $\mbox{:}\ 86\mbox{-}106\mbox{kPa}$

[&]quot;C" expresses nominal capacitance value (pF).



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	Itom		Specification	Tosting Mothed
	Item		Specification No marked defect.	Testing Method
11	Humidity (Under Steady State)	Appearance Capacitance Change	Y5P: Within ±10% Y5U: Within ±20% Y5V: Within ±30% SL: Within±2.5% or ±0.25pF,Whichever is large.	Set the capacitor for 500±12 hours at 40±2°C, in 90 to 95% humidity. Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at *1room condition for 24±2hours before initial measurements. Post-treatment:
		D.F.	Y5P,Y5U: 5.0% max. Y5V: 7.5% max.	Capacitor shall be stored for 1 to 2hours at *1room condition. Apply the rated voltage for 500±12 hours at 40±2°C, in 90 to 95% humidity.
12	Humidity Loading	Q	SL: $Q \ge 200 (C \ge 30 pF)$ $Q \ge 100 + 10 \times C/3 (C < 30 pF)$	Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at *1room
	Louding	I.R.	Y5P,Y5U,Y5V : 3000M Ω min. SL : 1000M Ω min.	condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at * 1 room condition.
		Appearance	No marked defect.	Impulse Voltage: Each individual capacitor shall be subjected to a 5kv impulses for three times.
		Capacitance Change I.R.	Y5P,Y5U,Y5V: Within $\pm 20\%$ SL: Within $\pm 3\%$ or ± 0.3 pF,Whichever is large. Y5P,Y5U,Y5V: 3000 M Ω min. SL: 1000 M Ω min.	After the capacitors are applied to life test. 100 (%)
13	Life	Dielectric Strength	Per Item 3.	Fig. T2 The specimen capacitors are placed in a circulating air oven for a period of 1000 hrs. The air in the oven is maintained at a temperature of 125±2°C. Throughout the test. The capacitors are subjected to an AC425Vrms.(for 0AC type) or AC510Vrms.(for 1AC type) alternating voltage of mains frequency. Except that once each hour the voltage id increased to 1000Vrms for 0.1sec. Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour then placed at *1room condition for 24±2hours before initial measurements. Post-treatment:
14	Passive Flammability	_	time shall not be exceeded sec. The tissue paper shall not	Capacitor shall be stored for 1 to 2hours at *1 room condition. The capacitor under test shall be held in the flame in the position, which best promotes burning. Each specimen shall only be exposed once to the flame. Time of exposure to flame: 30 sec Length of flame: 12±1 mm Gas burner: Length 35 mm min. Inside Dia.: 0.5±0.1 mm Outside Dia.: 0.9 mm max. Gas: Butane gas Purity 95% min.

[&]quot;room condition" temperature : 15~35°C , humidity : 45~75%, atmospheric pressure : 86~106kPa

[&]quot;C" expresses nominal capacitance value (pF).



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	Item	Specification	Testing Method		
15	Active Flammability	The cheesecloth shall not be on fire.	The specimens shall be individually wrapped in at least one but more the two complete layers of cheesecloth. The specimens shall be subjected to 20 discharges. The interval between successive discharges shall be 5sec. The Uac shall be maintained for 2 min. after the last discharge. Fig. FL1 L2 C3 R FL1 L2 C3 R Oacilloscpoe C1,2: 1Mf±10% C3: 0.03Mf±5% 10KV L1-4: 1.5Mh±20% 16A Rod core choke R: 100Ω±2% Ct: 3Mf±5% 10KV Uac: Ur±5% Ur: Rated working voltage Cx: Capacitor F: Fuse, Rated 10A Ut: Voltage applied to Ct		
16	Temperature Cycle		The capacitor should be subjected to 5 temperature cycles, Capacitor should be subjected to 5 temperature cycles, Step Temperature(°C) Time(min) -40+0/-3		

[&]quot;room condition" temperature : 15~35 $^\circ \! C$, humidity : 45~75%,atmospheric pressure : 86~106kPa

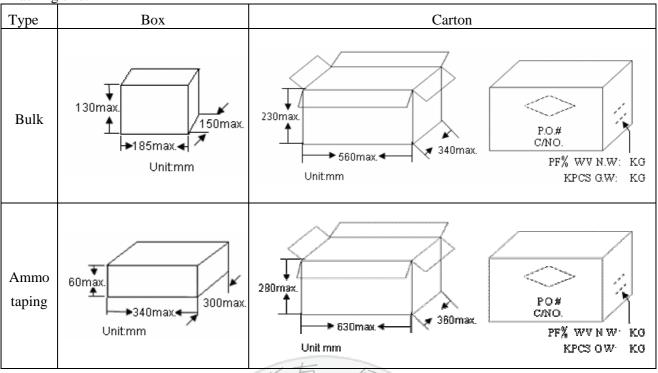
[&]quot;C" expresses nominal capacitance value (pF).



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8. Packing specification:

8.1 Packing size:



8.2 Packing quantity:

Packing type	The code of 14th to15th in SAP P/N	MPQ(Kpcs/Box)
	AF	1
Taping	AM (The size code ≤ 11)	1
	AM (The size code≥12)	0.5

PASSIVE SYSTEM ALLIANCE						
Packing type Lead length		Size code of 10th to 11th in SAP P/N	MPQ (Kpcs/Bag)	Kpcs/Box		
	Long lead	06~12	0.5	1.5		
	(L≧20mm)	13-15	0.5	1		
Bulk	Short lead	06~14 ₀ C	0.5	2		
	(L < 20mm)	ECHNOLIS CORRORATION.	0.2	1		
	All	16 CURPONIA	0.2	1		

8.3 Label samples:





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9. Notices:

9.1 Caution(Rating):

(1). Operating Voltage

Be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage
Positional measurement	V0-p	V0-p	Vp-p

(2). Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

(3). Test condition for withstanding Voltage

I. Test Equipment

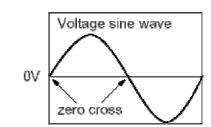
Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

II. Voltage Applied Method

When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall be taken off the output of the withstanding voltage test equipment.



If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

ZERO CROSS is the point where voltage sine wave pass 0V.- See the right figure.



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(4). Fail-Safe

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9.2 Caution (Storage and operating condition):

Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed –10 to 40 degrees centigrade and 15 to 85 % for 6 months maximum and use within the period after receiving the capacitors.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

9.3 Caution (Soldering and Mounting):

9.3.1 Vibration and impact:

Do not expose a capacitor or its leads to excessive shock or vibration during use.

9.3.2 Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

9.3.3 Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

*PULU*GY LIKY

9.4 Caution (Handling):

Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

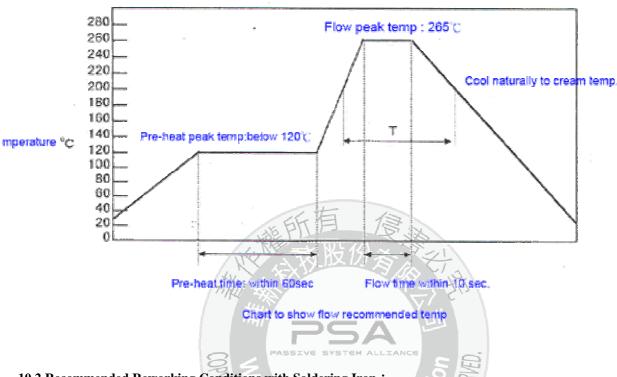


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10. Soldering Recommendation:

10.1 Wave Soldering Profile:

- Temperature conditions of the flow is recommended as shown in the chart
- Must implement the pre-heat
- Maximum peak flow temperature is recommended 265°C
- \bullet Time "T" implement in the chart recommended within 20 sec. it temperature exceed 200°C
- Take care with the flow solder not to touch the capacitor body directly at mounting



${\bf 10.2\ Recommended\ Reworking\ Conditions\ with\ Soldering\ Iron:}$

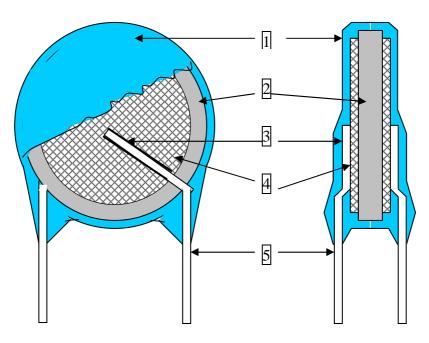
- Temperature of iron-tip: 400 degrees C. max.
- Soldering iron wattage: 50W max.
- Soldering time: 3.5 sec. max.
- Distance from coating body: 2 mm (min.)

10.3 Reflow-Soldering: Lead Ceramic Cap. should not be soldered by reflow-soldering.



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11. Drawing of internal structure and material list:



Remarks:

No.	Part name	Material	Model/Type	Component
1	Insulation Coating	Epoxy polymer	EF-150 PCE-300 ECP-357	Epoxy resin、Pigment (Blue / UL 94 V-0)
2	Dielectric Element	Ceramic	SL/Y5P/Y5U/Y5V	BaTiO ₃
3	Solder	Tin-silver	Sn96.5-Ag3-Cu0.5	Sn96.5-Ag3-Cu0.5
4	Electrodes	OAg Nais	SP-160PL SP-260PL	Silver · Glass frit
5	Leads wire	Tinned copper clad	0.55±0.05 mm	Substrate metal: Fe & Cu Surface plating: Sn 100%(3~7µm)

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46KI3150NDM2M PHE840MD6220MD13R30 PHE840MY6470MD14R06 PHE845VD5470MR06 R463N4100ZAM1K MKPX2R
1/400/10P27 YP500101K040B20C2P YU0AH222M090DAMD0B LS1808N102K302NX080TM CY1471KE1IEB46X2A2

CY1222ME5IEE48O2A2 MPX474K31DTEV158G0 Y2560K-D1I-B4-AC250V HMF222MG3BW CY1471ME19EE45W2A2

MPX104K31D2KN158HF MPX224K31D2KN158G0 PX104K2W1502 MP2224K32C5J6LC H102M050FQ55250L750A

MP2474K32D6R8LC MP2224K32C3J6LC MP2104K32C3J6LC PX334K2C1006 YU0AC222M080L20C7B MP2473K27B2X6LC

MP2474K32D4J8LC MP2684K32D6T8LC ST3Y1Y5U332M500VAC ST3Y1Y5V472M500VAC MP2474K32D4X8LC

MP2474K32D4J8LC YU0AH332M110L4EB0B CY1681ME1IEE45S2A2 Y1220J-E1I-B4-AC400V Y1120K-E1I-B4-AC400V

MP2154K32D2R8LC