WM02DN110C

Dual N-Channel Enhancement Mode MOSFET

Description

WM02DN110C uses advanced power trench technology that has been especially tailored to minimize the on-state resistance This device is suitable for un-directional or bidirectional load switch, facilitated by its common-drain configuration

$V_{(BR)DSS}$(V)	I_D(A)	$R_{DS(on)}$TYP (mΩ)
20	11	6.0 @VGS=4.5V
		6.2 @VGS=4.0V
		6.5 @VGS=3.7V
		7.0 @VGS=3.1V
		8.2 @VGS=2.5V

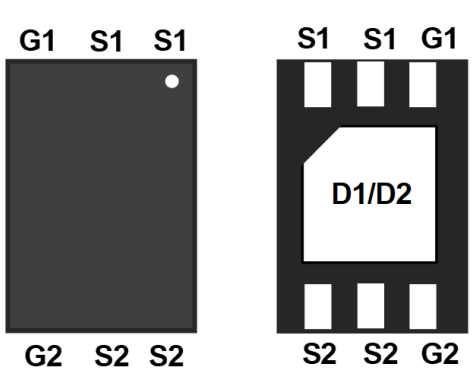
Features

- Super high dense cell for low $R_{DS(ON)}$
- RoHS Compliant and Halogen-Free
- ESD protected: Class 2

Applications

- Battery protection
- Load switch

Schematic & PIN Configuration



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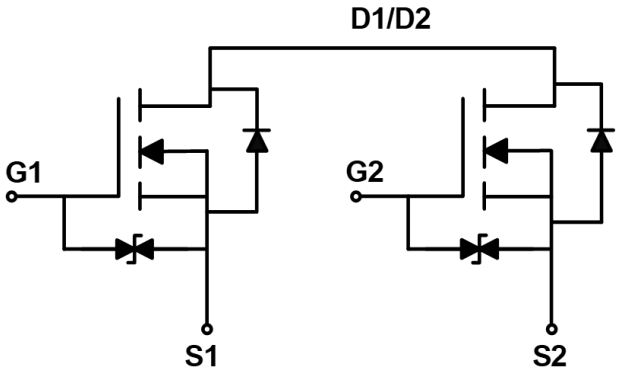


Device symbol

Absolute Maximum Rating

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current	T_A=25℃	I_D	11	A
	T_A=70℃		8.8	A
Pulsed Drain Current[1]		I_{DM}	70	A
Single Pulse Avalanche Energy[5]		EAS	39	mJ
Avalanche Current		I_{AS}	28	A
Total Power Dissipation	T_A=25℃	P_D	1.56	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C
Maximum Junction-to-Ambient[2]		$R_{\theta JA}$	80	°C/W

Electrical Characteristics (T_{amb}=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 20V, V_{GS} = 0V			1	μA
Gate-body Leakage current	I_{GSS}	V_{DS} = 0V, V_{GS} = ±8V	-	-	±10	μA
Gate-Threshold Voltage[3]	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	0.45	-	1.5	V
Drain-Source on-Resistance[3]	$R_{DS(on)}$	V_{GS} = 4.5V, I_D = 5.5A	4.5	6.0	7.5	mΩ
		V_{GS} = 4.0V, I_D = 5.5A	4.8	6.2	7.8	
		V_{GS} = 3.7V, I_D = 5.5A	5.0	6.5	8.5	
		V_{GS} = 3.1V, I_D = 5.5A	5.3	7.0	9.3	
		V_{GS} = 2.5 V, I_D = 5.5A	6	8.2	10.5	
Forward Transconductance[3]	g_{fs}	V_{DS} = 5V, I_D = 5.5A	-	45	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	V_{DS} = 10V, V_{GS} =0V, f =1MHz	-	1767	-	pF
Output Capacitance	C_{oss}		-	184	-	
Reverse Transfer Capacitance	C_{rss}		-	155	-	
Switching Characteristics						
Total Gate Charge[4]	Q_g	V_{GS} =4.5V, V_{DS} = 16V, I_D= 10A	-	23	-	nC
Gate-Source Charge[4]	Q_{gs}		-	3.5	-	
Gate-Drain Charge[4]	Q_{gd}		-	8.4	-	
Turn-on Delay Time[4]	$t_{d(on)}$	V_{GS} =4.5V, V_{DD} = 16V, R_G = 6Ω, I_D= 5.5A	-	10.2	-	nS
Rise Time[4]	t_r		-	41	-	
Turn-off Delay Time[4]	$t_{d(off)}$		-	67	-	
Fall Time[4]	t_f		-	31	-	
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	I_S = 1A, V_{GS} = 0V	-	-	1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface mounted on FR4 board using 1 square inch pad size,1oz single-side copper.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. Guaranteed by design, not subject to product
5. The EAS data shows Max. rating . The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=28A.

Typical Characteristics

Figure 1. Output Characteristics

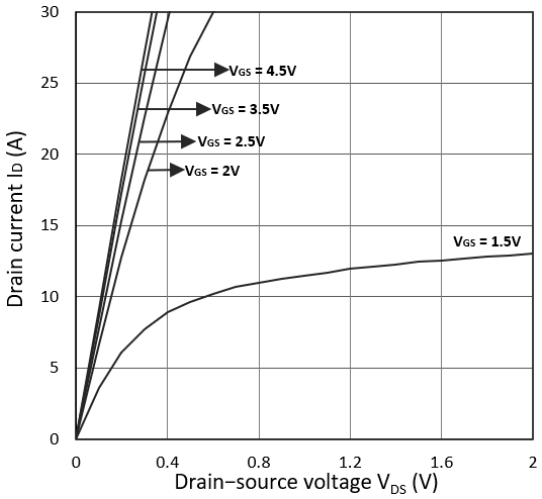


Figure 2. Transfer Characteristics

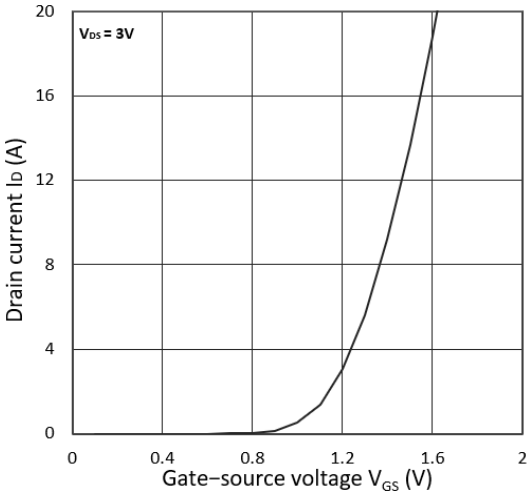


Figure 3. $R_{DS(ON)}$ vs. I_D

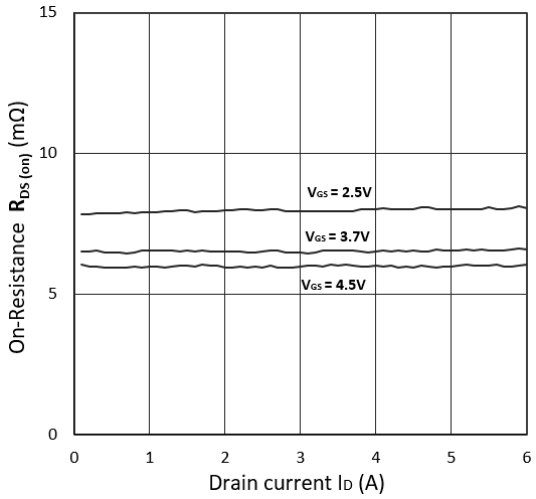


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

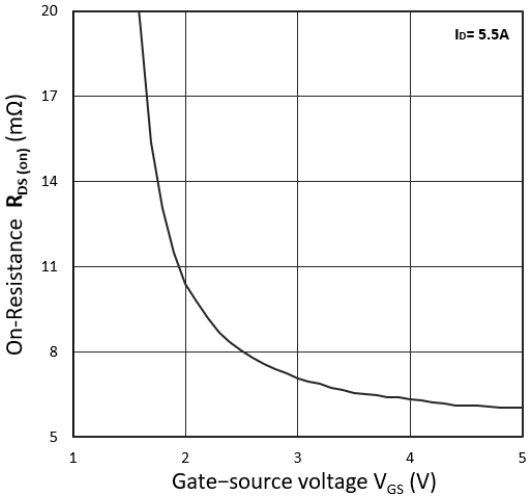


Figure 5. I_S vs. V_{SD}

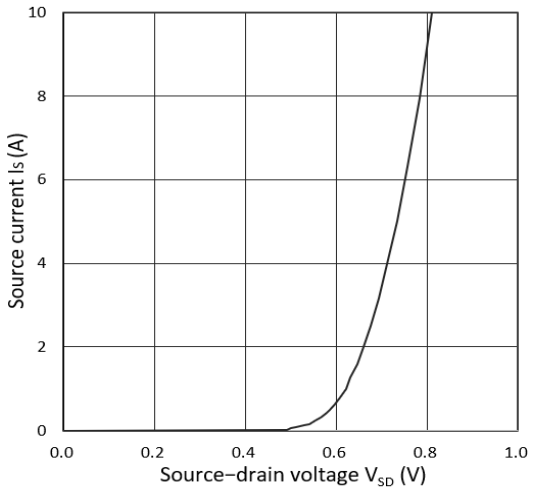
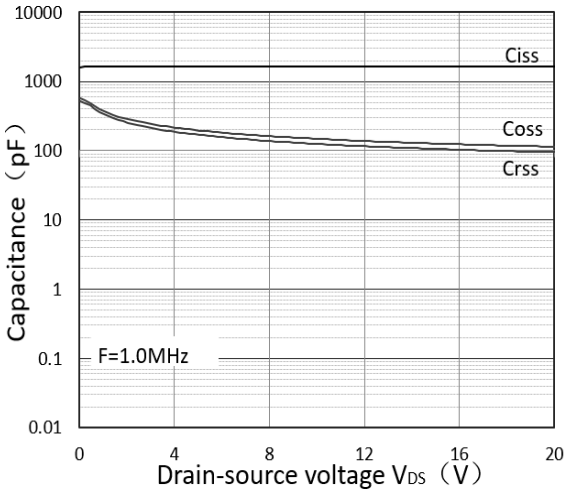
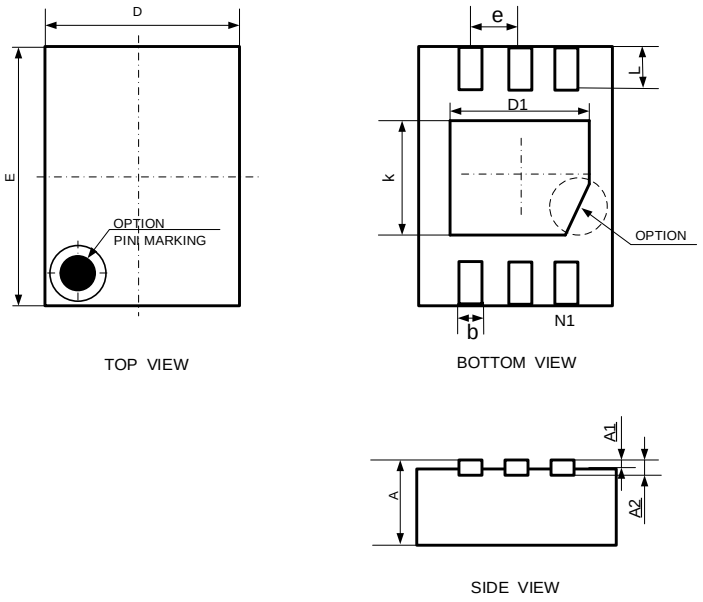


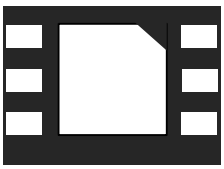
Figure 6. Capacitance Characteristics



Outline Drawing –DFN2030-6L

PACKAGE OUTLINE





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DIMENSIONS

SYMBOL	MILLIMETER		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.80	0.028	0.031
A1	0.00	0.05	0.000	0.002
A2	0.203		0.008	
D	1.95	2.05	0.077	0.081
E	2.95	3.05	0.116	0.120
k	1.65	1.75	0.057	0.061
D1	1.45	1.55	0.065	0.069
b	0.20	0.30	0.008	0.012
e	0.50 BSC		0.020BSC	
L	0.30	0.40	0.012	0.016

Marking Codes

Part Number	WM02DN110C
Marking Code	C11N02 WWXX XXX C11N02 = Device Code WWXX XXX= Date Code

Package Information

Qty： 3k/Reel

CONTACT INFORMATION

No.1001, Shiwan (7) Road, Pudong District, Shanghai, P.R.China.201207
Tel: 86-21-68969993 Fax: 86-21-50757680 Email: market@way-on.com
WAYON website: **http://www.way-on.com**
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