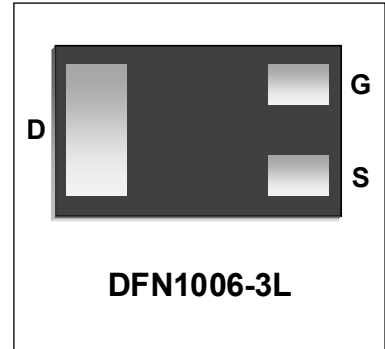


N-Channel Enhancement MOSFET

Features

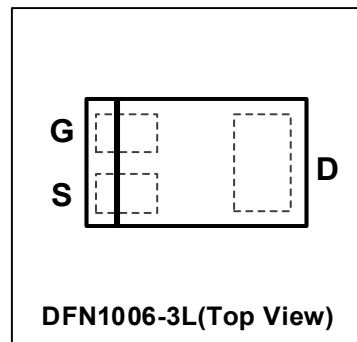
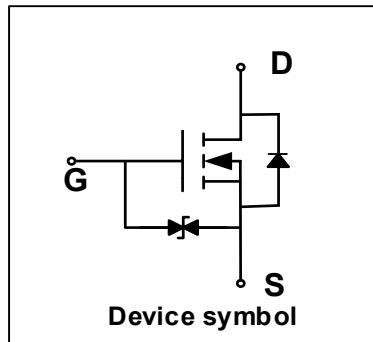
- Way-on Small Signal MOSFETs
- $V_{DS} = 20V$, $I_D = 0.75A$
 $R_{DS(on)} < 0.38\Omega @ V_{GS} = 4.5V$
 $R_{DS(on)} < 0.45\Omega @ V_{GS} = 2.5V$
- Trench LV MOSFET Technology
- ESD Protected



Mechanical Characteristics

- DFN1006-3L Package
- Marking : Making Code
- RoHS Compliant

Schematic & PIN Configuration



Absolute Maximum Rating ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 10	V
Continuous Drain Current	I_D	0.75	A
Pulsed Drain Current ¹	I_{DM}	3	A
Power Dissipation	P_D	150	mW
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction to Ambient ²	$R_{\theta JA}$	833	$^\circ C/W$

Electrical Characteristics ($T_{amb}=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20V, V_{GS} = 0V$	-	-	1	μA
Gate-body Leakage Current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 10V$	-	-	± 20	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.35	0.75	1.1	V
Drain-Source on-State Resistance ³	$R_{DS(on)}$	$V_{GS} = 4.5V, I_D = 0.65A$	-	0.19	0.38	Ω
		$V_{GS} = 2.5V, I_D = 0.55A$	-	0.26	0.45	
		$V_{GS} = 1.8V, I_D = 0.45A$	-	0.70	-	
Dynamic Characteristics⁴						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 10V,$ $f = 1MHz$	-	26.5	-	pF
Output Capacitance	C_{oss}		-	11	-	
Reverse Transfer Capacitance	C_{rss}		-	5.2	-	
Switching Characteristics⁴						
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 4.5V, V_{DD} = 10V,$ $I_D = 0.65A, R_G = 3\Omega$	-	6.7	-	ns
Turn-on Rise Time	t_r		-	4.8	-	
Turn-off Delay Time	$t_{d(off)}$		-	17.3	-	
Turn- off Fall Time	t_f		-	7.4	-	
Source-Drain Diode Characteristics						
Body Diode Voltage ³	V_{SD}	$I_S = 0.15A, V_{GS} = 0V$	-	-	1.2	V
Continuous Source Current	I_S		-	-	0.75	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^{\circ}C$.
2. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

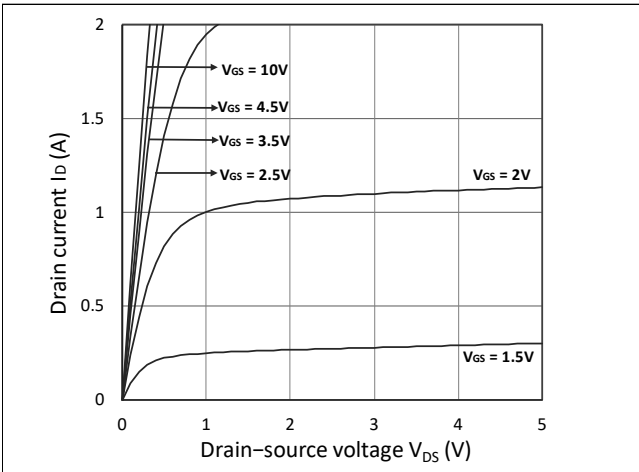


Figure 1. Output Characteristics

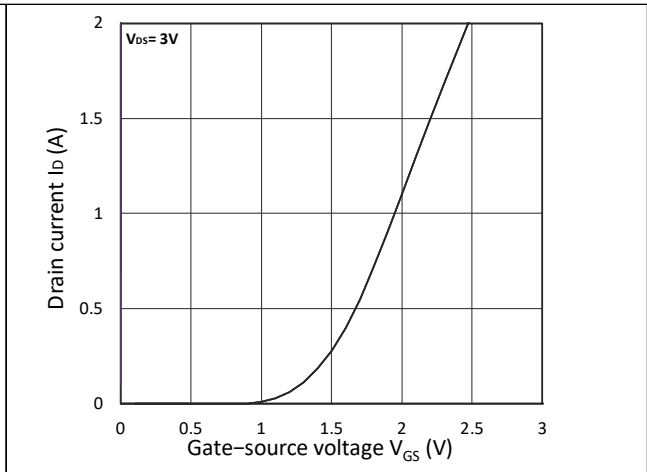


Figure 2. Transfer Characteristics

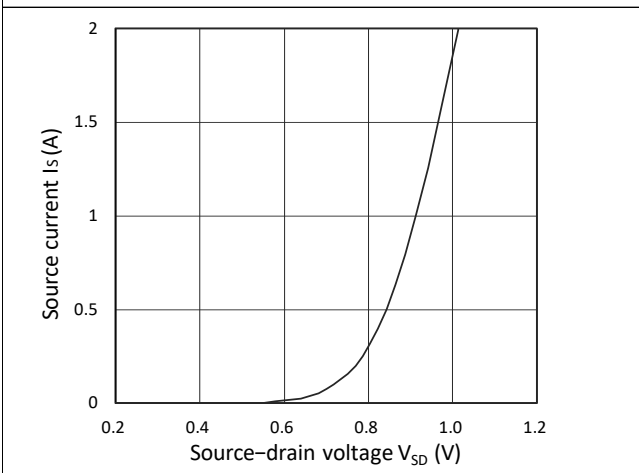


Figure 3. Forward Characteristics of Reverse

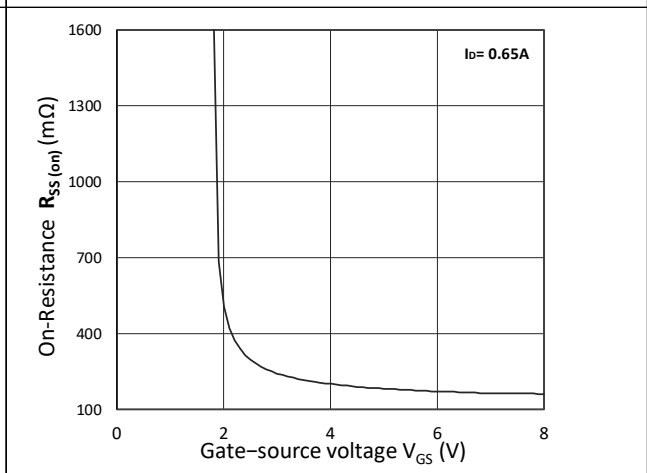


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

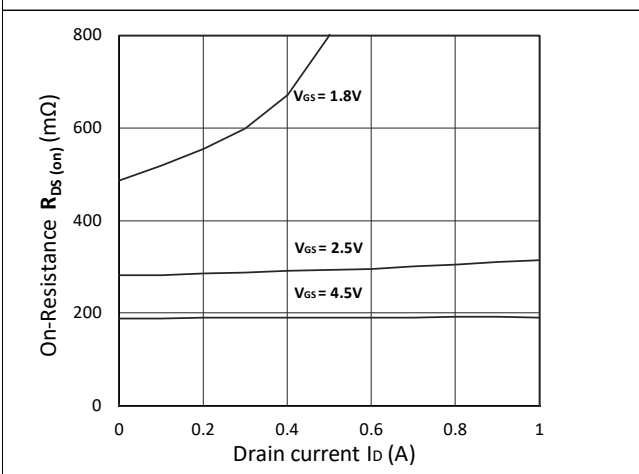


Figure 5. $R_{DS(ON)}$ vs. I_D

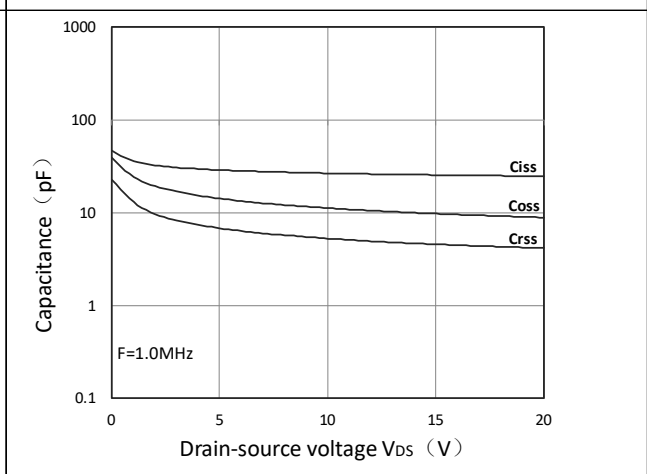
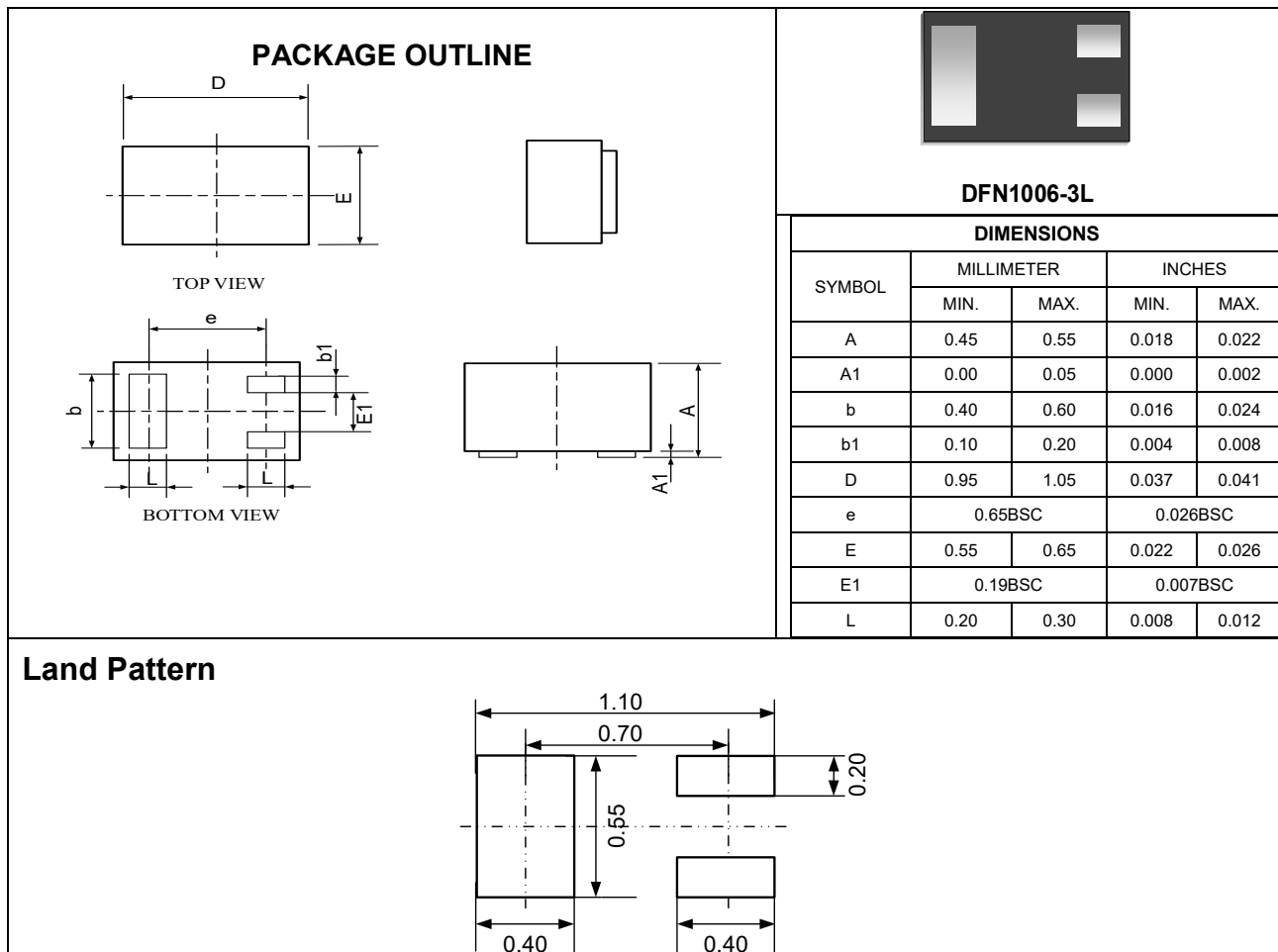
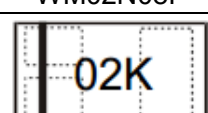


Figure 6. Capacitance Characteristics

Outline Drawing – DFN1006-3L



Marking Codes

Part Number	WM02N08F
Marking Code	

Package Information

Qty: 10k/Reel

CONTACT INFORMATION

No.1001, Shiwan (7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-68969993 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

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Users should verify actual device performance in their specific applications.*

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