

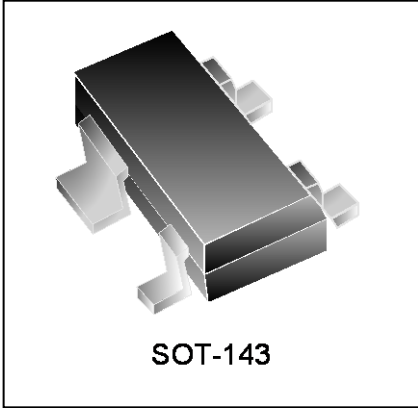


WS05RXLC

Transient Voltage Suppressor

Features

- Small Body Outline Dimensions
- Protects two I/O lines
- Low Clamping Voltage
- Low Capacitance
- Working Voltage: 5 V
- Low Leakage Current
- Response Time is Typically < 1 ns



SOT-143

IEC COMPATIBILITY (EN61000-4)

- IEC 61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 9A (8/20µs)

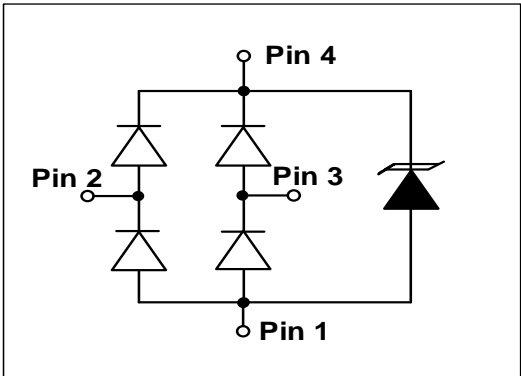
Mechanical Characteristics

- SOT-143 package
- Marking: Marking Code
- Packaging: Tape and Reel per EIA 481
- RoHS Compliant

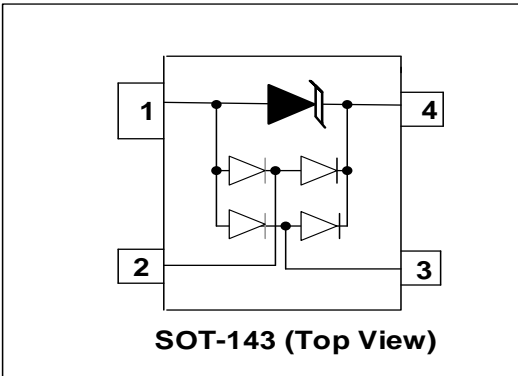
Applications

- Laptop Computers
- RoHS Compliant
- Cellular Phones
- Digital Cameras
- Personal Digital Assistants (PDAs)

Circuit Diagram



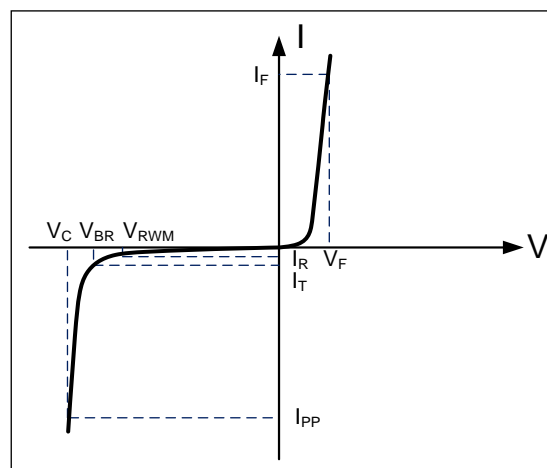
Schematic & PIN Configuration



Absolute Maximum Rating			
Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P _{PP}	135	W
Peak Pulse Current ($t_p = 8/20\mu s$)	I _{PP}	9	A
Operating Temperature	T _J	-55 to + 125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Parameters (T=25°C)

Symbol	Parameter
I _{PP}	Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Reverse Stand-Off Voltage
I _R	Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
I _T	Test Current
I _F	Forward Current
V _F	Forward Voltage @ I _F



Electrical Characteristics

WS05RXLC						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				5	V
Reverse Breakdown Voltage	V _{BR}	I _T =1mA	6			V
Reverse Leakage Current	I _R	V _{RWM} =5V, T=25°C			500	nA
Clamping Voltage	V _C	I _{PP} =9A, t _p =8/20μs I/O pin to GND		12	15	V
		I _{PP} =25A, t _p =8/20μs VCC pin to GND		12	15	
Dynamic Resistance ^{1,2}	R _{DYN}	TLP=0.2/100ns I/O pin to GND		0.3		Ω
ESD Clamping Voltage ¹	V _C	I _{PP} = 4A, t _p = 0.2/100ns (TLP)		9		V
ESD Clamping Voltage ¹	V _C	I _{PP} = 16A, t _p = 0.2/100ns (TLP)		12		V
Junction Capacitance	C _j	V _R = 0V, f = 1MHz I/O pin to GND		2.5	3.5	pF
		V _R = 0V, f = 1MHz Between I/O pins		1.2	1.5	

Notes : 1、 TLP Setting : t_p=100ns, t_r=0.2ns, I_{TLP} and V_{TLP} sample window:t₁=70ns to t₂=90ns.

2、 Dynamic resistance calculated from I_{PP}=4A to I_{PP}=16A using "Best Fit".

Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

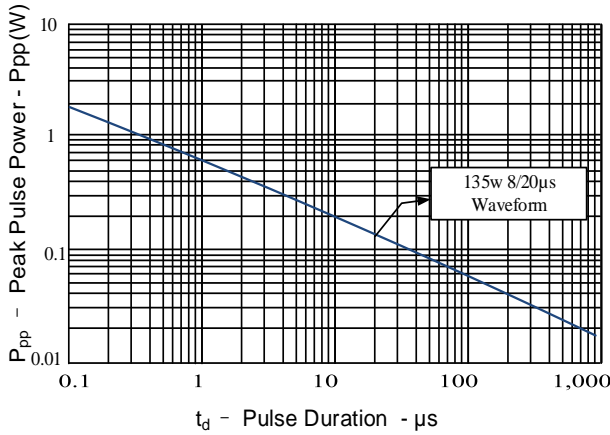


Figure 2: Power Derating Curve

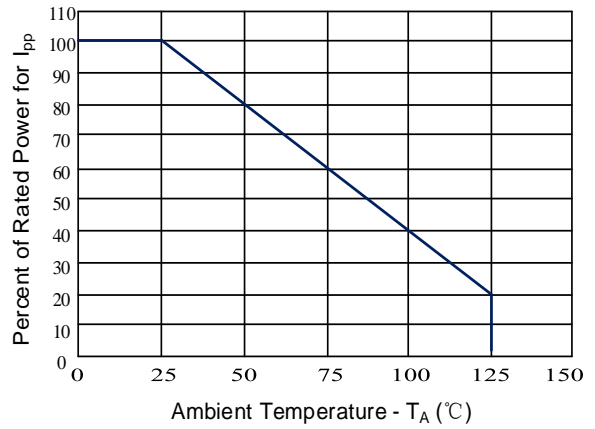


Figure 3: Clamping Voltage vs. Peak Pulse Current

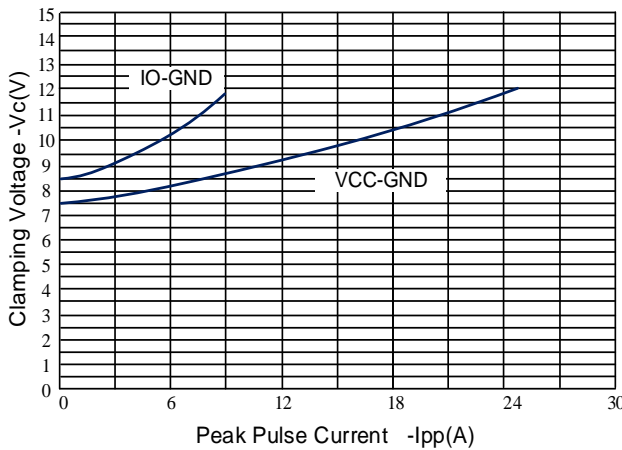


Figure 4: Normalized Junction Capacitance vs. Reverse Voltage

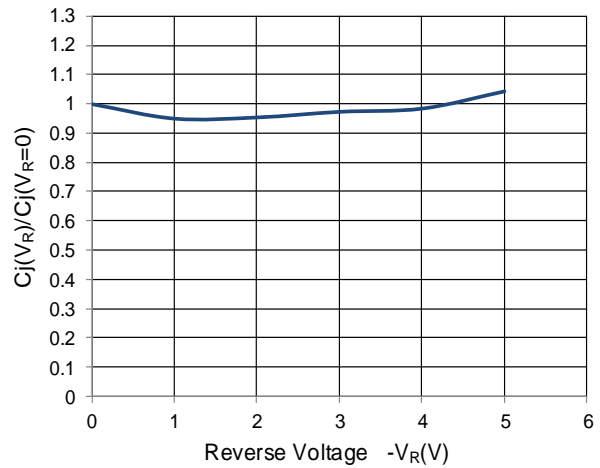


Figure 5: 8/20μs Pulse Waveform

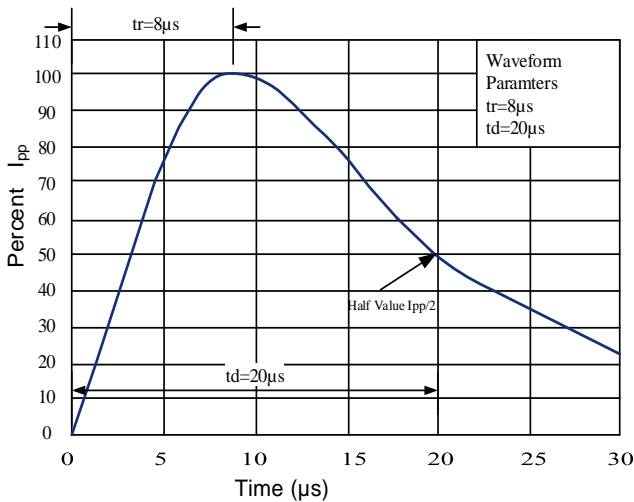
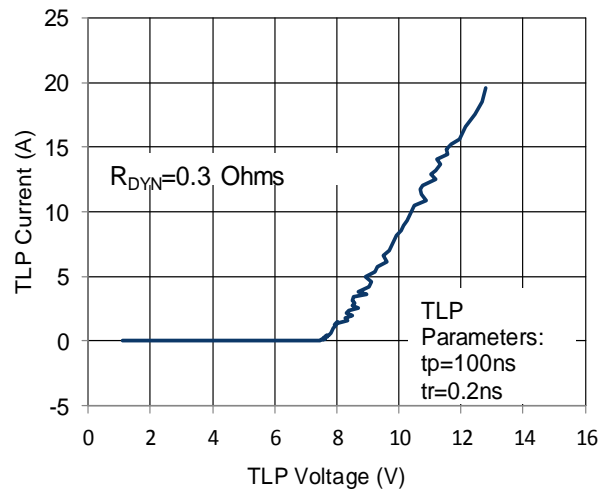
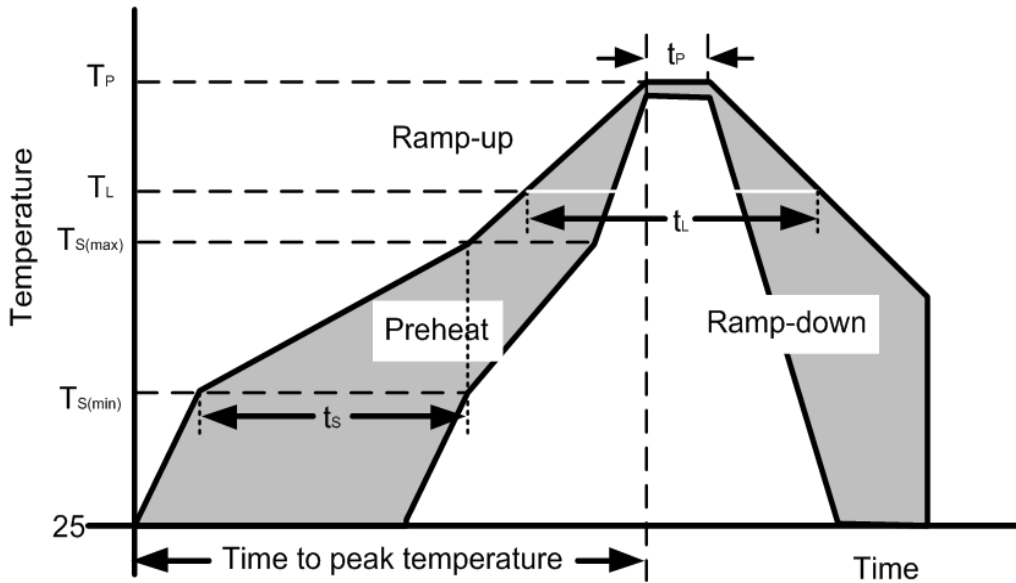


Figure 6: TLP I-V Curve



Soldering Parameters

Reflow Condition		Pb – Free assembly
Pre Heat	Temperature Min ($T_{s(min)}$)	150°C
	Temperature Max ($T_{s(max)}$)	200°C
	Time (min to max) (t_s)	60 – 190 secs
Average ramp up rate (Liquidus Temp) (T_L) to peak		5°C/second max
$T_{s(max)}$ to T_L —Ramp-up Rate		5°C/second max
Reflow	Temperature (T_L) (Liquidus)	217°C
	Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_P)		260+0/-5 °C
Time within actual peak Temperature (t_p)		20 – 40 seconds
Ramp-down Rate		5°C/second max
Time 25°C to peak Temperature (T_P)		8 minutes Max.
Do not exceed		280°C



Outline Drawing –SOT-143

PACKAGE OUTLINE

SOT-143

SYMBOL	MILLIMETER		INCHES	
	MIN	MAX	MIN	MAX
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
b1	0.750	0.900	0.030	0.035
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
e	1.800	2.000	0.071	0.079
e1	0.200TYP		0.008TYP	
E	2.250	2.550	0.089	0.100
E1	1.200	1.400	0.047	0.055
L1	0.550REF		0.022REF	
L	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

DIMENSIONS		
DIM	INCHES	MILLIMETERS
C	.087	2.20
E1	.076	1.92
E2	.068	1.72
G	.031	0.80
X1	.039	1.00
X2	.047	1.20
Y	.055	1.40
Z	.141	3.60

Notes:
Controlling Dimension: Millimeter.

Marking Codes

Part Number	Marking Code
WS05RXLC	R05

Package Information

Qty: 3k/Reel

CONTACT INFORMATION

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The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
Users should verify actual device performance in their specific applications.

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