Image: State in the state i		۵. ۳		C B	>
ran max subscription subscri		Ordering 6320D-XXX NoofPins Contact PI SI=3u"Go S2=5u"Go		Withstand Voltage: Insulation Resistar Operation Tempera Contact Material:P Contact Plating:Au Insulator Material:f	NOTES: Rated Current:0.54 Contact Resistance
Image: Control of the second state st	Change PCB Layout Dimension,Add Change Plastic Structure ADD With Board Lock Pi NEW MODIFICATION DESCR	formation S1 PX T Type Par N:Without Board Lock Pin T=		500V AC/DC ce:500M& min iture:-40°C to +125°C hosphor Bronze and Sn Over Ni 9BT+30%G.F UL94V-0	2 5m2 max
1 0 1					
A   Y   N   Recommended P.C.B Loyout (lop Side) Solution Solution (22010004)   PCB BOARD TOLERANCE LO.S) (2201004)     1.122.0006   5.80 (2201004)   1.122.0006   0.00000   0.00000   0.00000	RATION ±0.40 ±0.25 ±0.15 TOL				
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	DATE 2012. 04. 28 DATE DATE 6	9.80±0.15 [.386±0.00	06 [.075±0.004] 5.60±0.10 [.220±0.004] 7.00±0.15	3.10±0.15 [.122±0.006]	_
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9 10   P.C.B Layout (Top Side)   QD TOLERANCE ± 0.05)   O O O O O O O O O O O O O O O O O O O	0.80				Recomme (PCB
10.05 10	5	26.38   22.93     28.92   25.47     32.73   29.28     39.08   35.63     45.43   41.98     50.51   47.06     58.13   54.68     70.83   67.38     83.53   80.08	Q O O O C −1.27 D (26, 30, 34, 50P) (20,03 16.58 13.2: 20,03 16.58 13.2: 20,03 17.00	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	
	0NKunshan 512 57468408 512 57468468 Sälesks@wcon.com	24.39 24.89 26.93 27.43 30.74 31.24 43.44 43.94 48.52 49.02 56.14 56.64 68.84 69.34 81.54 82.04	15 24 21 85 22 35 1 15 24 21 85 4 1.905 7 1.905 7 1.9	1.905 1.905	

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