**Product data sheet** 

## 1. General description

Planar passivated Silicon Controlled Rectifier in a TO247 plastic package intended for use in applications requiring very high inrush current capability and high thermal cycling performance.

### 2. Features and benefits

- High thermal cycling performance
- Planar passivated for voltage ruggedness and reliability
- · High voltage capacity
- · Very high current surge capability

## 3. Applications

- Line rectifying 50/60 Hz
- Softstart AC motor control
- · DC Motor control
- Power converter
- AC power control
- Lighting and temperature control
- Uninterruptible Power Supply (UPS)
- Solid State Relay (SSR)
- Traction battery charging

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Values	Unit				
Absolute maximum rating								
$V_{DRM}$	repetitive peak off-state voltage		1200	V				
$V_{RRM}$	repetitive peak reverse voltage		1200	V				
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; $T_{mb} \le 131 ^{\circ}\text{C}$ ; Fig. 1; Fig. 2; Fig. 3	79	А				
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)} = 25  ^{\circ}C$ ; $t_p = 10  \text{ms}$ ; Fig. 4; Fig. 5	650	А				
		half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 8.3 ms	715	Α				
T <sub>j</sub>	junction temperature		150	°C				

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static ch	Static characteristics						
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 7; Fig. 8		-	-	50	mA
Dynamic	characteristics						
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 804 V; $T_j$ = 125 °C; $R_{GK}$ = 100 Ω; $(V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform		1500	-	-	V/µs

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		A <del>                                    </del>
2	А	anode		G G
3	G	gate		sym037
mb	A	mounting base; connected to anode	1 2 3	

# 6. Ordering information

#### Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
BT155W-1200T	TO247	BT155W-1200TQ	Tube	30	TO247N	20-July-2016

# 7. Limiting values

**Table 4. Limiting values** 

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Values	Unit
$V_{DRM}$	repetitive peak off-state voltage		1200	V
$V_{RRM}$	repetitive peak reverse voltage		1200	V
I <sub>T(AV)</sub>	average on-state current	half sine wave; T <sub>mb</sub> ≤ 131 °C	50	А
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; $T_{mb} \le 131 ^{\circ}\text{C}$ ; Fig 1; Fig 2; Fig 3	79	А
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)} = 25 \text{ °C}$ ; $t_p = 10 \text{ ms}$ ; Fig 4; Fig 5	650	А
		half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 8.3 ms	715	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse	2113	A <sup>2</sup> s
dl <sub>⊤</sub> /dt	rate of rise of on-state current	I <sub>G</sub> = 200mA	150	A/µs
I <sub>GM</sub>	peak gate current		8	А
$V_{RGM}$	peak reverse gate voltage		5	V
$P_{GM}$	peak gate power		20	W
$P_{G(AV)}$	average gate power	over any 20 ms period	1	W
T <sub>stg</sub>	storage temperature		-40 to 150	°C
T <sub>j</sub>	junction temperature		150	°C

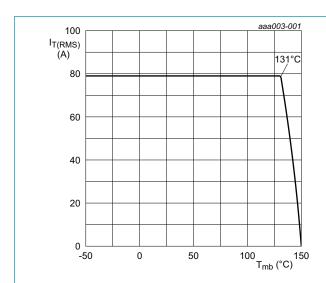
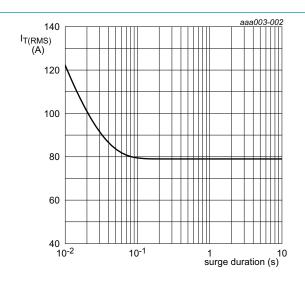
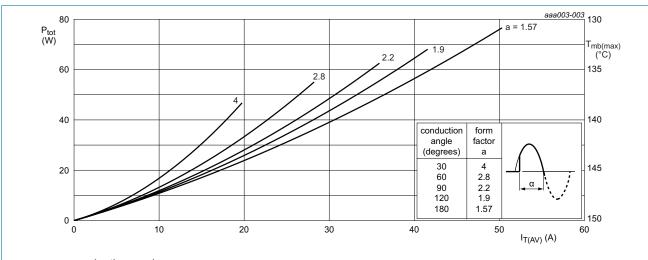


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values



f = 50 Hz; T<sub>mb</sub> = 131 °C Fig. 2. RMS on-state current as a function of surge duration; maximum values



 $\alpha$  = conduction angle a = form factor =  $I_{T(RMS)} / I_{T(AV)}$ 

Fig. 3. Total power dissipation as a function of average on-state current; maximum values

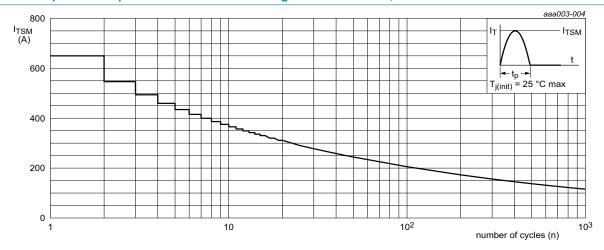
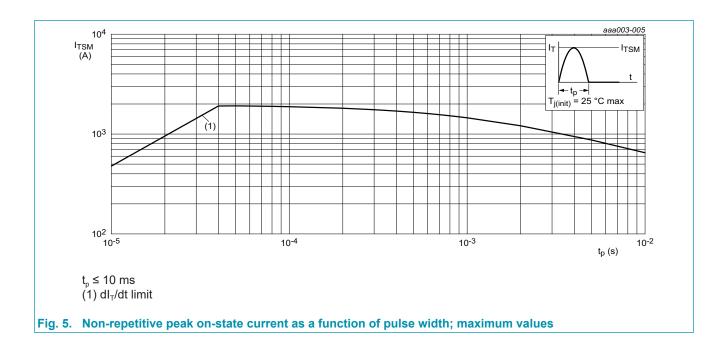


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum

f = 50 Hz

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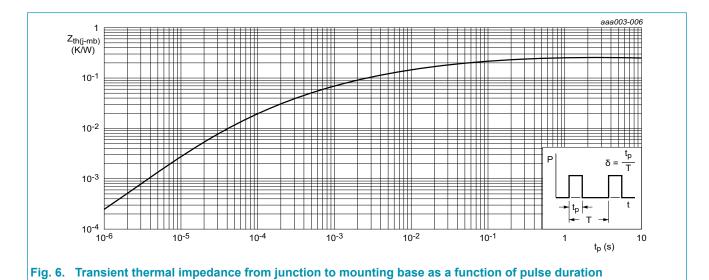


## 8. Thermal & Mechanical characteristics

Table 5. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	Fig 6	-	-	0.25	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	in free air	-	50	-	K/W
	Mounting torque	M3 screw mounting	0.55	-	0.8	Nm

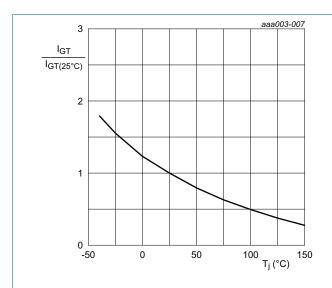
Note: It is recommended that a metal washer is inserted between screw head and mounting tab. Do not use self-tapping screws.



## 9. Characteristics

**Table 6. Characteristics** 

<b>Symbol</b>	Parameter	Conditions	Min	Тур	Max	Unit
Static ch	aracteristics					
l <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 7; Fig. 8	-	-	50	mA
I <sub>L</sub>	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 9	-	-	300	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	-	200	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 50 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	-	1.3	V
		I <sub>T</sub> = 90 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	-	1.5	V
V <sub>GT</sub>	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 12	-	0.7	1	V
		$V_D = 800 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 ^{\circ}\text{C}$	0.25	0.4	-	V
$I_D$	off-state current	V <sub>D</sub> = 1200 V; T <sub>j</sub> = 125 °C	-	-	3	mA
I <sub>R</sub>	reverse current	V <sub>D</sub> = 1200 V; T <sub>j</sub> = 125 °C	-	-	3	mA
Dynamic	characteristics			_		
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 804 V; $T_j$ = 125 °C; $R_{GK}$ = 100 Ω; $(V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform	1500	-	-	V/µs
		$V_{DM}$ = 804 V; $T_j$ = 150 °C; $R_{GK}$ = 100 Ω; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform	1000	-	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM} = 40 \text{ A}; V_D = 800 \text{ V}; I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A/}\mu\text{s}; T_j = 25 °C$	-	2	-	μs
t <sub>q</sub>	commutated turn-off time	$V_{DM} = 804 \text{ V; } T_j = 125 \text{ °C; } I_{TM} = 20 \text{ A; } V_R = 25 \text{ V; } (dI_T/dt)_M = 30 \text{ A/}\mu\text{s; } dV_D/dt = 50 \text{ V/}\mu\text{s; } R_{GK(ext)} = 100 \text{ k}\Omega\text{; } (V_{DM} = 67\% \text{ of } V_{DRM})$	-	150	-	μs



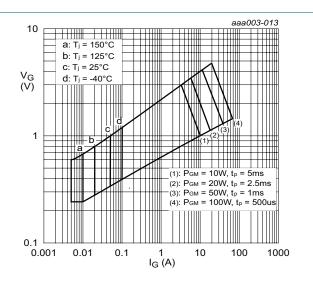
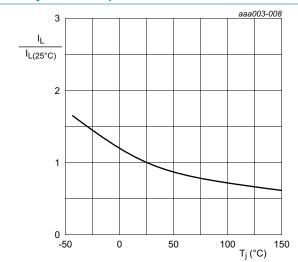


Fig. 7. Normalized gate trigger current as a function of junction temperature





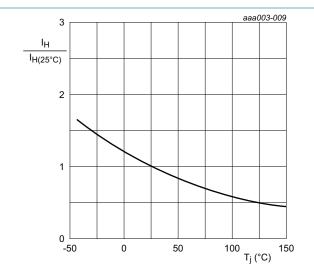
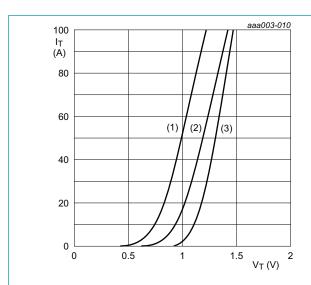


Fig. 9. Normalized latching current as a function of junction temperature

Fig. 10. Normalized holding current as a function of junction temperature

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 $V_o = 0.975 \text{ V}; R_s = 0.0044 \Omega$ 

(1) T<sub>j</sub> = 150 °C; typical values (2) T<sub>j</sub> = 150 °C; maximum values

(3)  $T_i = 25$  °C; maximum values

Fig. 11. On-state current as a function of on-state voltage

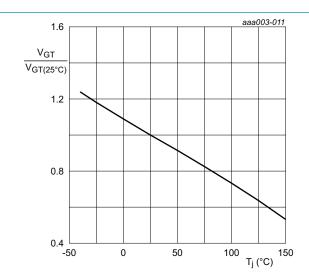
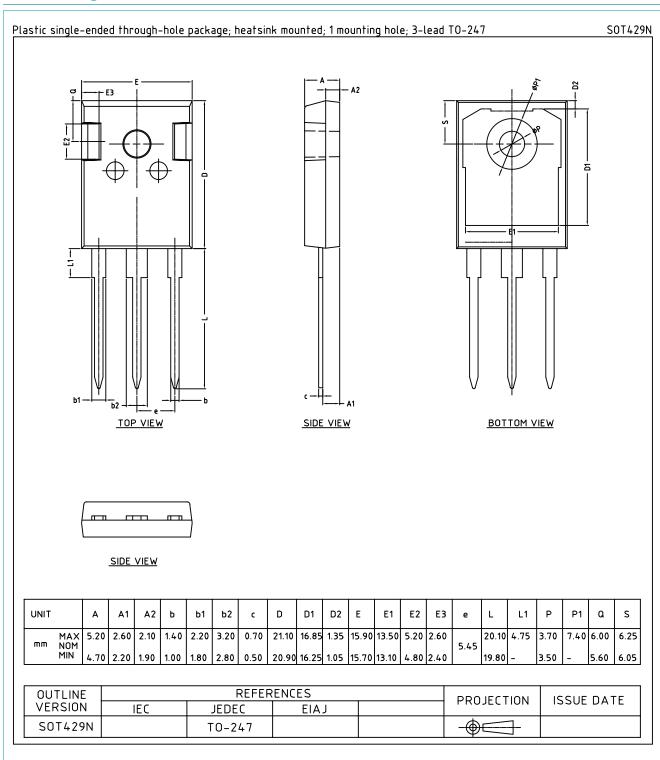


Fig. 12. Normalized gate trigger voltage as a function of junction temperature

## 10. Package outline



### 11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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11 / 13

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### 12. Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Limiting values	3
8. Thermal characteristics	6
9. Characteristics	7
10. Package outline	10
11. Legal information	
12 Contents	

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