



IMPORTANT NOTICE

10 December 2015

1. Global joint venture starts operations as WeEn Semiconductors

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As from November 9th, 2015 NXP Semiconductors N.V. and Beijing JianGuang Asset Management Co. Ltd established Bipolar Power joint venture (JV), **WeEn Semiconductors**, which will be used in future Bipolar Power documents together with new contact details.

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Thank you for your cooperation and understanding,

WeEn Semiconductors





BTA330Y-800CT

3Q Hi-Com Triac

27 July 2015

Product data sheet

1. General description

Planar passivated high commutation three quadrant triac in a SOT78D (TO-220AB) internally insulated plastic package intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. This triac will commute the full RMS current at the maximum rated junction temperature ($T_{j(max)} = 150\text{ }^{\circ}\text{C}$) without the aid of a snubber. It is used in applications where "high junction operating temperature capability" is required.

2. Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High junction operating temperature capability ($T_{j(max)} = 150\text{ }^{\circ}\text{C}$)
- High voltage capability
- High current capability
- Less sensitive gate for highest noise immunity
- Internally insulated package
- Internally isolated mounting base
- Triggering in three quadrants only
- Very high immunity to false turn-on by dv/dt and IEC 61000-4-4 fast transient
- Package is RoHS compliant
- Package meets UL94V0 flammability requirement
- Package meets UL1557 isolation test requirement rated at 2500V RMS

3. Applications

- Heating controls
- High power motor control
- High power switching
- Applications subject to high temperature ($T_{j(max)} = 150\text{ }^{\circ}\text{C}$)

4. Quick reference data

Table 1. Quick reference data

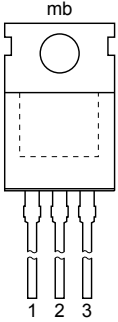

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 86\text{ }^{\circ}\text{C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	-	30	A



Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{TSM}	non-repetitive peak on-state current	full sine wave; T _{j(init)} = 25 °C; t _p = 20 ms; Fig. 4 ; Fig. 5	-	-	270	A
		full sine wave; T _{j(init)} = 25 °C; t _p = 16.7 ms	-	-	297	A
T _j	junction temperature		-	-	150	°C
Static characteristics						
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; Fig. 7	-	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; Fig. 7	-	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; Fig. 7	-	-	35	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; Fig. 9	-	-	50	mA
V _T	on-state voltage	I _T = 42 A; T _j = 25 °C; Fig. 10	-	1.2	1.55	V
Dynamic characteristics						
dV _D /dt	rate of rise of off-state voltage	V _{DM} = 536 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	2000	-	-	V/μs
		V _{DM} = 536 V; T _j = 150 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	1000	-	-	V/μs
dI _{com} /dt	rate of change of commutating current	V _D = 400 V; T _j = 125 °C; I _{T(RMS)} = 30 A; dV _{com} /dt = 20 V/μs; (snubberless condition); gate open circuit	16	-	-	A/ms
		V _D = 400 V; T _j = 150 °C; I _{T(RMS)} = 30 A; dV _{com} /dt = 20 V/μs; (snubberless condition); gate open circuit	13	-	-	A/ms

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	 <p style="text-align: center;">TO-220AB (SOT78D)</p>	
2	T2	main terminal 2		
3	G	gate		
mb	n.c.	mounting base; isolated		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BTA330Y-800CT	TO-220AB	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220	SOT78D

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 86 \text{ }^\circ\text{C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	30	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(\text{init})} = 25 \text{ }^\circ\text{C}$; $t_p = 20 \text{ ms}$; Fig. 4 ; Fig. 5	-	270	A
		full sine wave; $T_{j(\text{init})} = 25 \text{ }^\circ\text{C}$; $t_p = 16.7 \text{ ms}$	-	297	A
I^2t	I^2t for fusing	$t_p = 10 \text{ ms}$; sine-wave pulse	-	364.5	A ² s
dl_T/dt	rate of rise of on-state current	$I_G = 70 \text{ mA}$	-	100	A/ μs
I_{GM}	peak gate current		-	2	A
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T_{stg}	storage temperature		-40	150	$^\circ\text{C}$
T_j	junction temperature		-	150	$^\circ\text{C}$

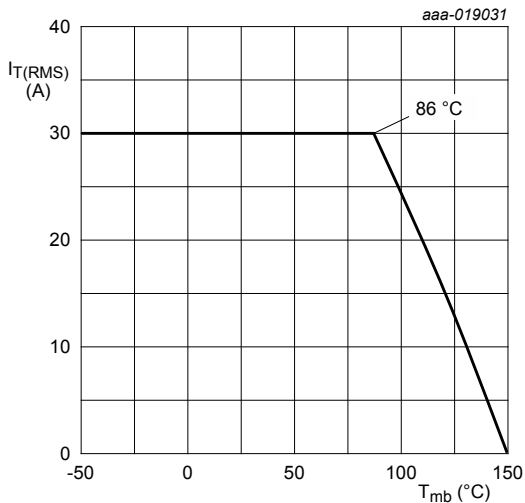
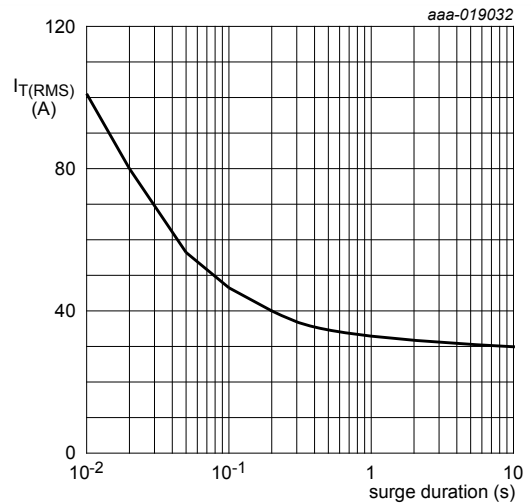


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values



$f = 50 \text{ Hz}$; $T_{mb} = 86 \text{ }^\circ\text{C}$

Fig. 2. RMS on-state current as a function of surge duration; maximum values

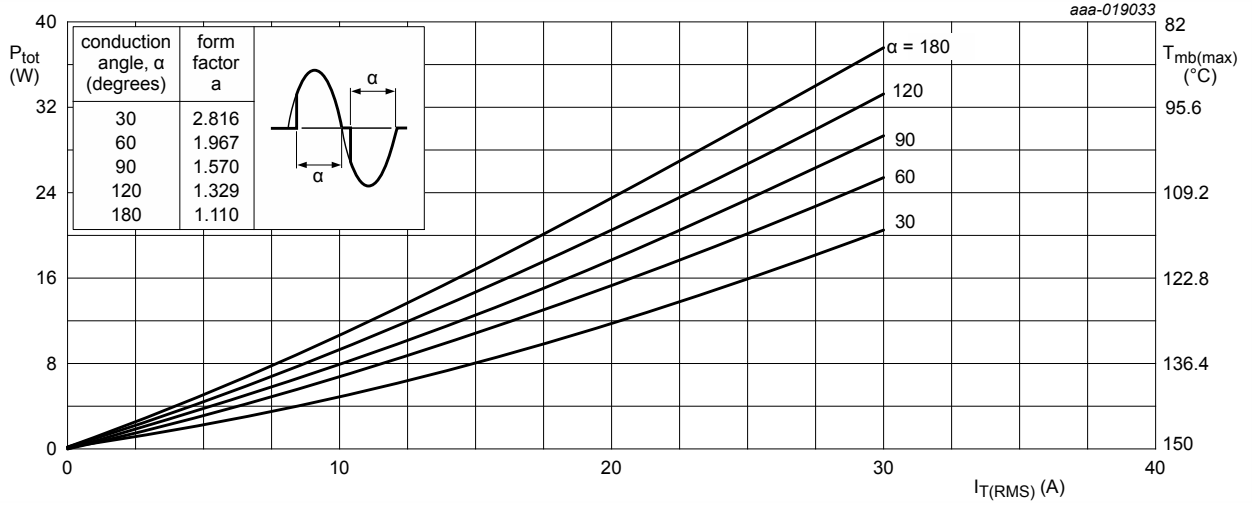


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

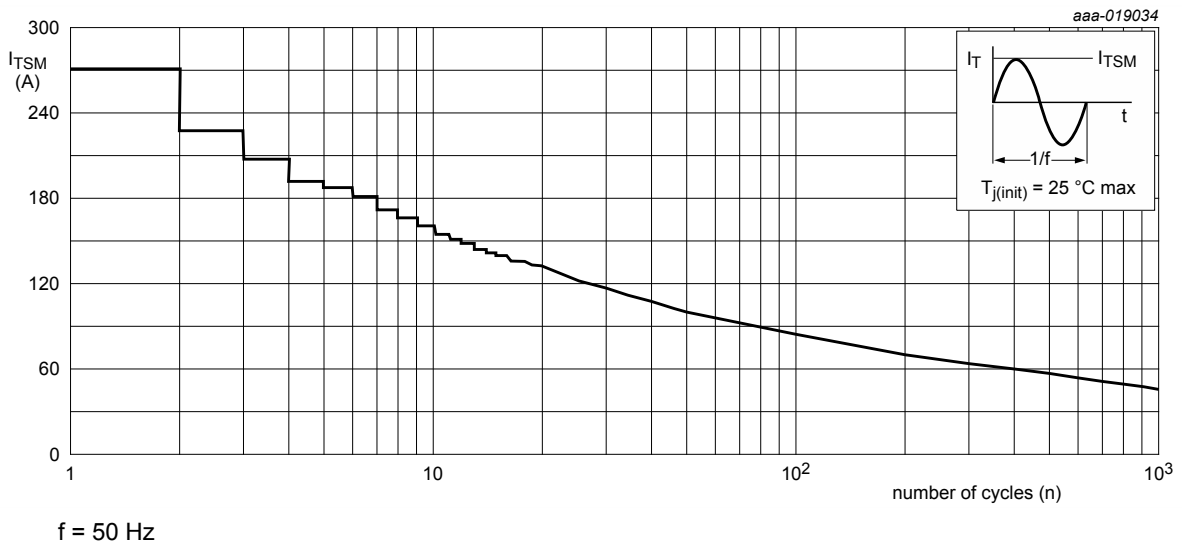
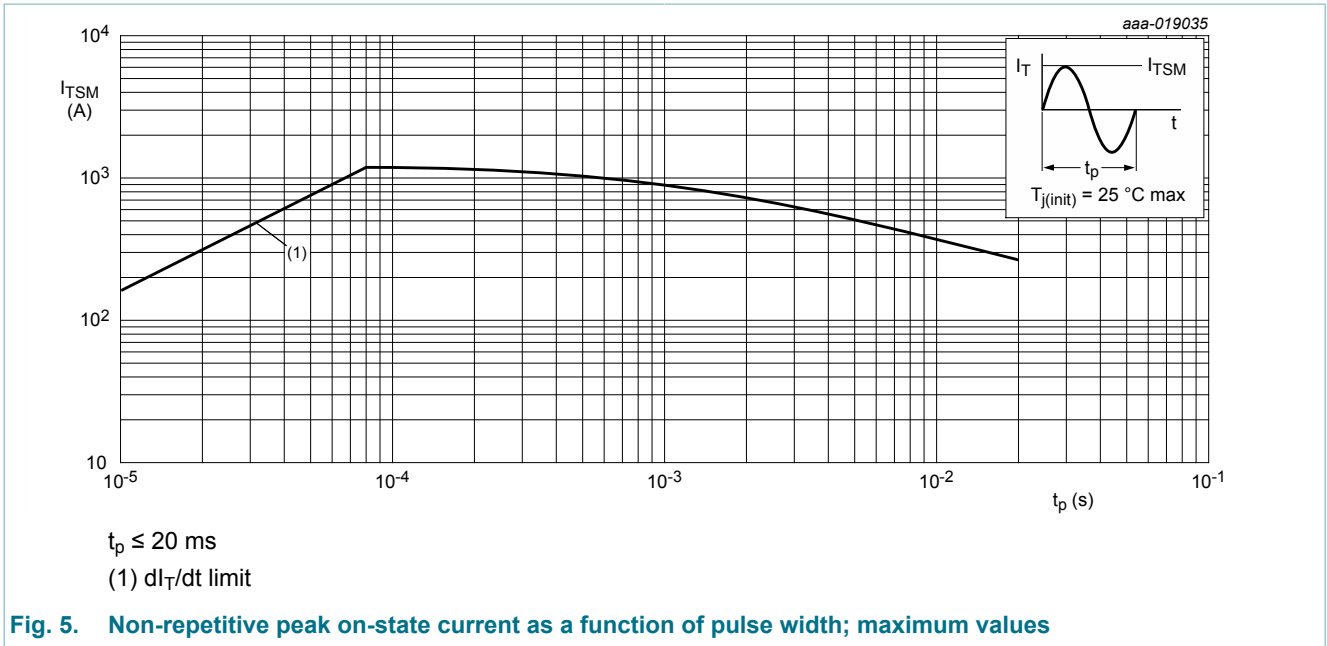


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	full cycle; Fig. 6	-	-	1.7	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	in free air	-	60	-	K/W

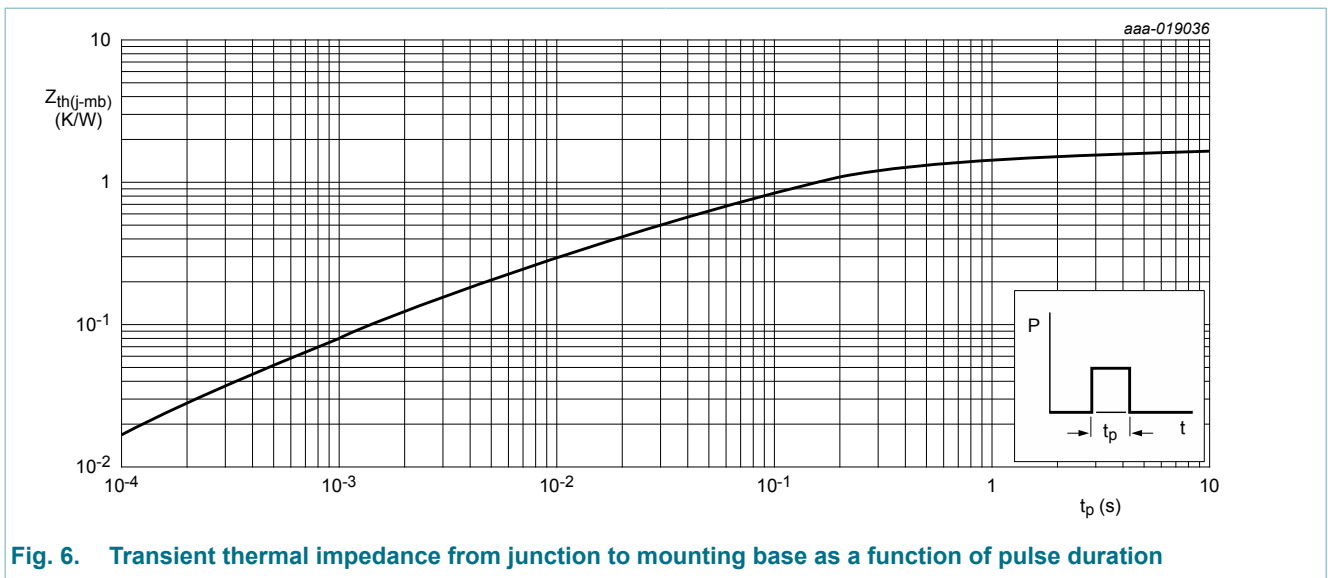


Fig. 6. Transient thermal impedance from junction to mounting base as a function of pulse duration

9. Isolation characteristics

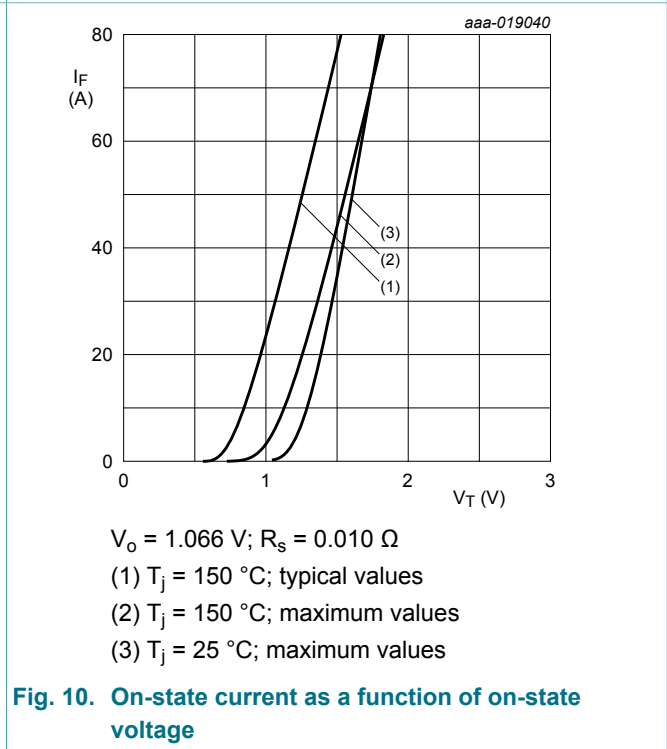
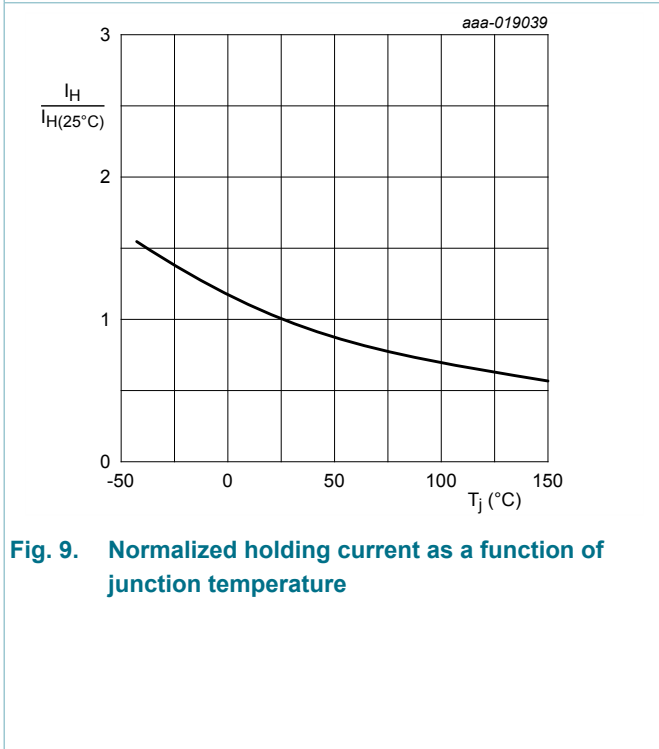
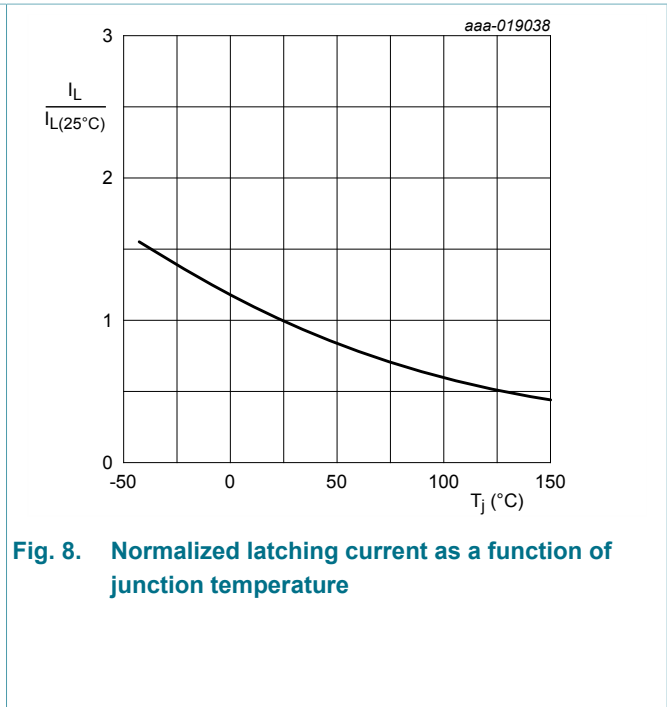
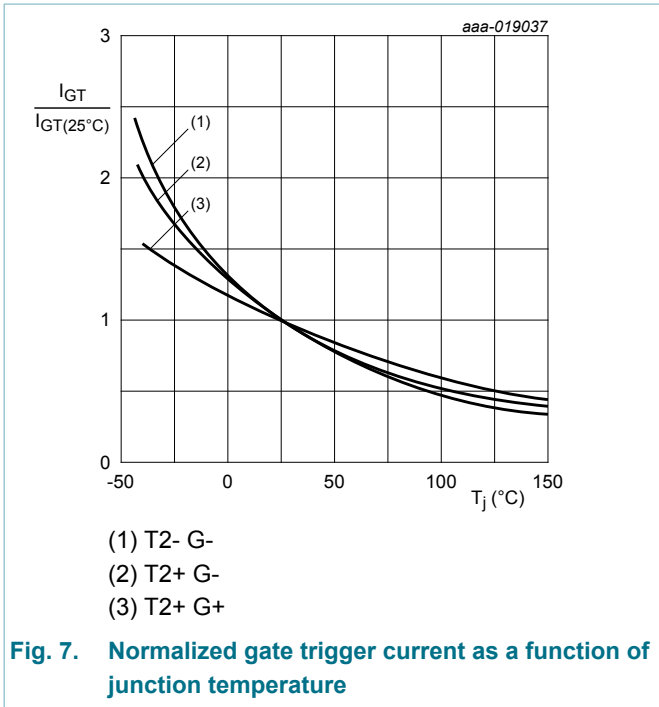
Table 6. Isolation characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{isol(RMS)}$	RMS isolation voltage	from all terminals to external heatsink; sinusoidal waveform; clean and dust free; $50\text{ Hz} \leq f \leq 60\text{ Hz}$; $RH \leq 65\%$; $T_{mb} = 25\text{ }^\circ\text{C}$	-	-	2500	V
C_{isol}	isolation capacitance	from main terminal 2 to external heatsink; $f = 1\text{ MHz}$; $T_{mb} = 25\text{ }^\circ\text{C}$	-	10	-	pF

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; Fig. 7	-	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; Fig. 7	-	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; Fig. 7	-	-	35	mA
I _L	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; Fig. 8	-	-	70	mA
		V _D = 12 V; I _G = 0.1 A; T2+ G-; T _j = 25 °C; Fig. 8	-	-	80	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _j = 25 °C; Fig. 8	-	-	70	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; Fig. 9	-	-	50	mA
V _T	on-state voltage	I _T = 42 A; T _j = 25 °C; Fig. 10	-	1.2	1.55	V
V _{GT}	gate trigger voltage	V _D = 12 V; T _j = 25 °C; Fig. 11	-	0.9	1.3	V
		V _D = 400 V; T _j = 150 °C; Fig. 11	0.2	0.45	-	V
I _D	off-state current	V _D = 800 V; T _j = 25 °C	-	-	10	µA
		V _D = 800 V; T _j = 150 °C	-	0.4	2	mA
Dynamic characteristics						
dV _D /dt	rate of rise of off-state voltage	V _{DM} = 536 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	2000	-	-	V/µs
		V _{DM} = 536 V; T _j = 150 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	1000	-	-	V/µs
dI _{com} /dt	rate of change of commutating current	V _D = 400 V; T _j = 125 °C; I _{T(RMS)} = 30 A; dV _{com} /dt = 20 V/µs; (snubberless condition); gate open circuit	16	-	-	A/ms
		V _D = 400 V; T _j = 150 °C; I _{T(RMS)} = 30 A; dV _{com} /dt = 20 V/µs; (snubberless condition); gate open circuit	13	-	-	A/ms



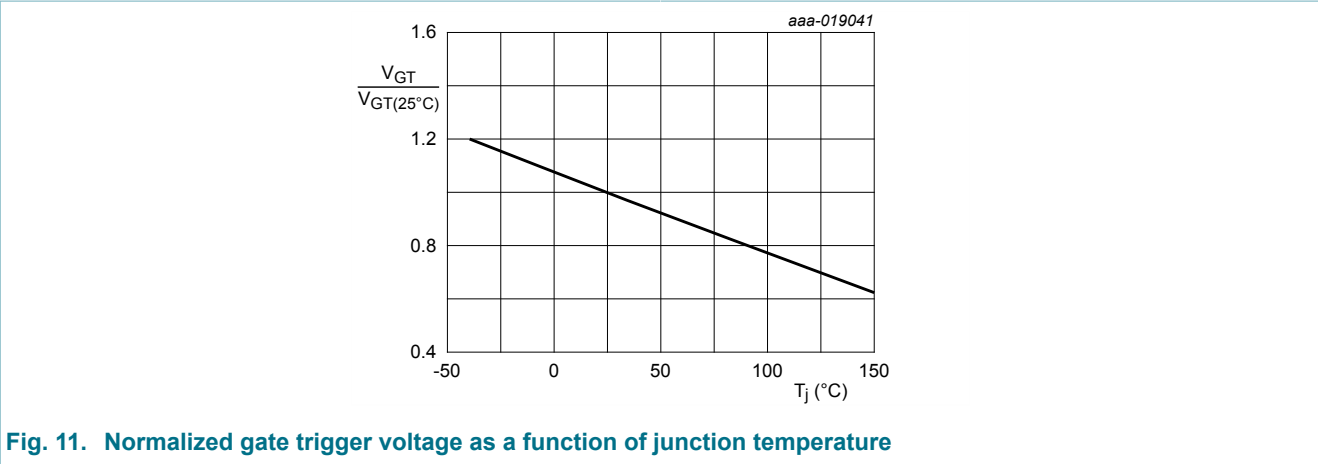
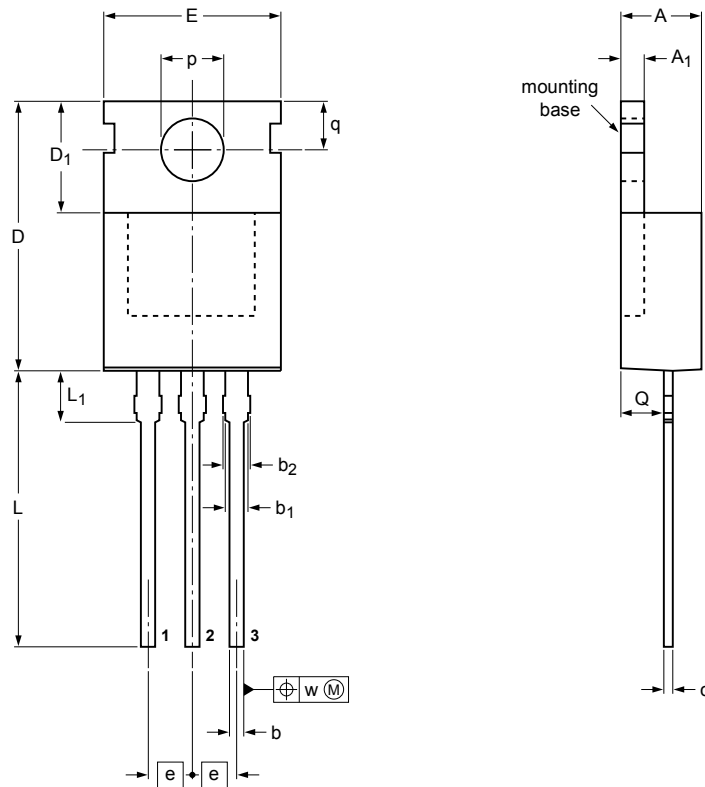


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

11. Package outline

Plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 SOT78D



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁	b	b ₁	b ₂	c	D	D ₁ ref	E	e	L	L ₁ ref	p	Q	q	w
mm	4.7 4.3	1.40 1.25	0.9 0.6	1.4 1.1	1.72 1.32	0.6 0.4	16.0 15.2	6.5	10.3 9.7	2.54	14.0 12.8	3.0	3.7 3.5	2.6 2.2	3.0 2.7	0.2

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA		
SOT78D		TO-220			07-04-04 07-07-10

Fig. 12. Package outline TO-220AB (SOT78D)

12. Legal information

12.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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