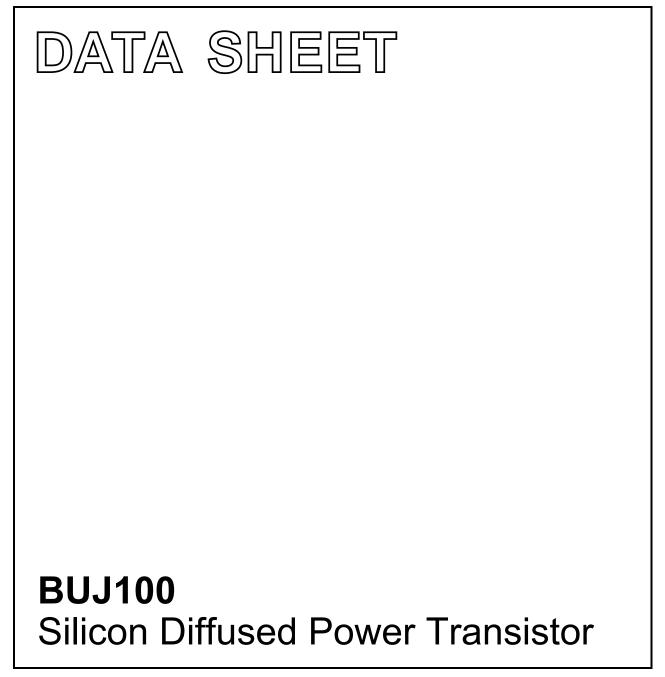
DISCRETE SEMICONDUCTORS



Product specification

September 2018



BUJ100

GENERAL DESCRIPTION

High-voltage, high-speed planar-passivated npn power switching transistor in the TO92 envelope intended for use in compact fluorescent lamps and low power electronic lighting ballasts, converters and inverters, etc.

QUICK REFERENCE DATA

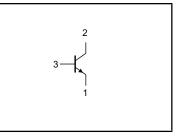
| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT |
|---|---|--|------|--------------------------------------|-----------------------|
| $V_{CESM} \\ V_{CBO} \\ V_{CEO} \\ I_C \\ I_{CM} \\ P_{tot} \\ V_{CEsat}$ | Collector-emitter voltage peak value Collector-Base voltage (open emitter) Collector-emitter voltage (open base) Collector current (DC) Collector current peak value Total power dissipation | $V_{BE} = 0 V$ $T_{lead} \le 25 \degree C$ | | 700 700 400 1.0 2.0 2 | V V A A W |
| ∣ V _{CEsat} h _{FE} | Collector-emitter saturation voltage | $I_{c} = 0.75 \text{ A}; I_{B} = 150 \text{ mA}$ $I_{c} = 0.75 \text{ A}; V_{CE} = 5 \text{ V}$ | 0.24 | 1.0 20 | |
| t _{fi} | Fall time (Inductive) | $I_{c} = 1.0 \text{ A}; I_{BON} = 200 \text{ mA}$ | 50 | 70 | ns |

PINNING - TO92

| PIN | DESCRIPTION | |
|-----|-------------|--|
| 1 | Emitter | |
| 2 | Collector | |
| 3 | Base | |
| | | |

PIN CONFIGURATION

SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|-------------------|--|---------------------------|------|------|------|
| V _{CESM} | Collector to emitter voltage | $V_{BF} = 0 V$ | - | 700 | V |
| V _{CEO} | Collector to emitter voltage (open base) | | - | 400 | V |
| V _{CBO} | Collector to base voltage (open emitter) | | - | 700 | V |
| I _c | Collector current (DC) | | - | 1.0 | A |
| I _{CM} | Collector current peak value | | - | 2.0 | A |
| I _B | Base current (DC) | | - | 0.5 | A |
| 1 1 | Base current peak value | | - | 1.0 | A |
| P _{tot} | Total power dissipation | T _{lead} ≤ 25 °C | - | 2 | W |
| T _{stg} | Storage temperature | | -65 | 150 | °C |
| $ T_i^{i} $ | Junction temperature | | - | 150 | °C |

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THERMAL RESISTANCES

| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT |
|------------------------|---|--------------------------------|------|------|------|
| R _{th j-lead} | Thermal resistance junction to lead | | - | 60 | K/W |
| R _{th j-a} | Thermal resistance Junction to ambient | pcb mounted; lead length = 4mm | 150 | - | K/W |

BUJ100

STATIC CHARACTERISTICS

 $T_{lead} = 25$ °C unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--|---|---|----------|------|------|---------|
| I _{CES} ,I _{CBO} | Collector cut-off current ¹ | | - | 0.8 | 100 | μA |
| CES | | $V_{BE} = 0 V; V_{CE} = V_{CESMmax};$ $T_i = 125 °C$ | - | 2.0 | 500 | μA |
| I _{CEO} | Collector cut-off current | $V_{CEO} = V_{CEOMmax}(400V)$ | - | - | 100 | μA |
| I _{EBO} V _{CEOsust} | Emitter cut-off current Collector-emitter sustaining voltage | $V_{EB} = 9 V; I_{C} = 0 A$ $I_{B} = 0 A; I_{C} = 10mA;$ | - 400 | 0.05 | 100 | μA V |
| ♥ CEOsust | Concetor ennitier sustaining voltage | L = 25 mH | +00 | | | v |
| V _{CEsat} | Collector-emitter saturation voltage | $I_{\rm C} = 0.75 \text{ A}; I_{\rm B} = 0.15 \text{ A}$ | - | 0.24 | 1.0 | V |
| V _{BEsat} | Base-emitter saturation voltage | l _c = 0.75 A; l _B =0.15 A | - | 0.93 | 1.3 | V |
| h _{FE} | DC current gain | I _c = 10mA; V _{ce} = 5 V | 11 | 20 | 27 | |
| h _{FE} | - | $I_{c} = 10mA; V_{cE} = 5 V$ $I_{c} = 100mA; V_{cE} = 5 V$ | 12.5 | 21 | 31 | |
| h _{FE} | | $I_{\rm C} = 0.75 \text{ A}; V_{\rm CE} = 5 \text{ V}$ | 9 | 14 | 20 | |

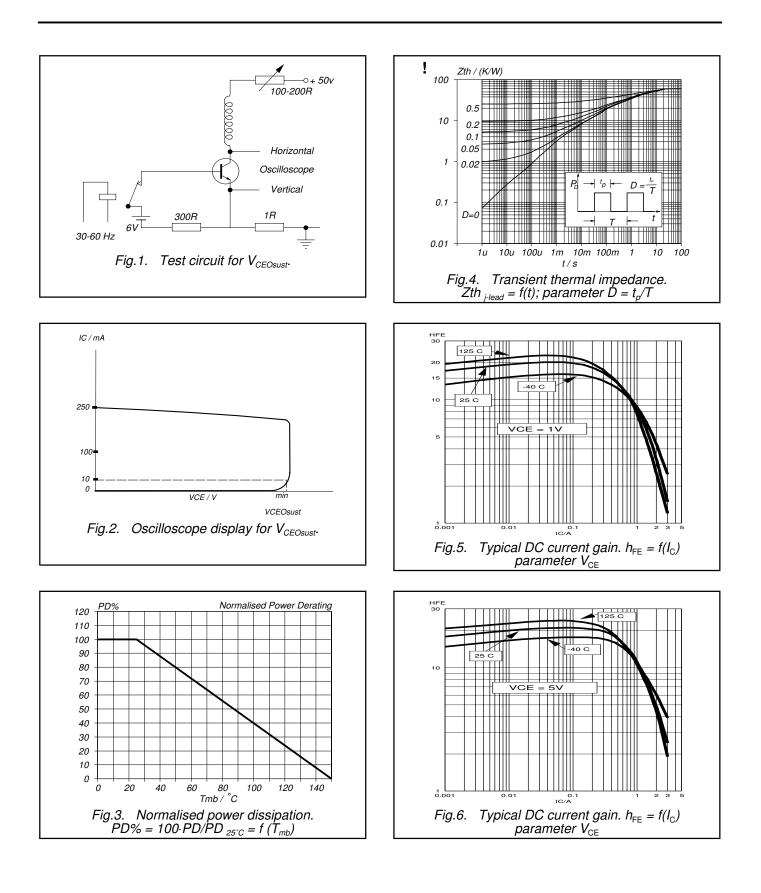
DYNAMIC CHARACTERISTICS

 $T_{\text{lead}} = 25$ °C unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT |
|---|---|--|---------------------|--------------------|----------------|
| | Switching times (resistive load) | $I_{Con} = 1.0 \text{ A}; I_{Bon} = -I_{Boff} = 200 \text{ mA};$ R ₁ = 75 ohms; V _{BB2} = 4 V; | | | |
| t _{on} t _s t _f | Turn-on time Turn-off storage time Turn-off fall time | | 0.65 0.88 250 | 0.88 1.2 338 | μs μs ns |
| | Switching times (inductive load) | $I_{Con} = 1.0 \text{ A}; I_{Bon} = 200 \text{ mA}; L_{B} = 1 \mu\text{H};$ - $V_{BB} = 5 \text{ V}$ | | | |
| t _s t _f | Turn-off storage time Turn-off fall time | | 0.51 50 | 0.7 70 | μs ns |
| | Switching times (inductive load) | $ I_{Con} = 1.0 \text{ A}; I_{Bon} = 200 \text{mA}; L_{B} = 1 \mu\text{H}; \\ -V_{BB} = 5 \text{ V}; T_{i} = 100 ^{\circ}\text{C} $ | | | |
| t _s t _f | Turn-off storage time Turn-off fall time | | - | 1.4 130 | μs ns |

¹ Measured with half sine-wave voltage (curve tracer).

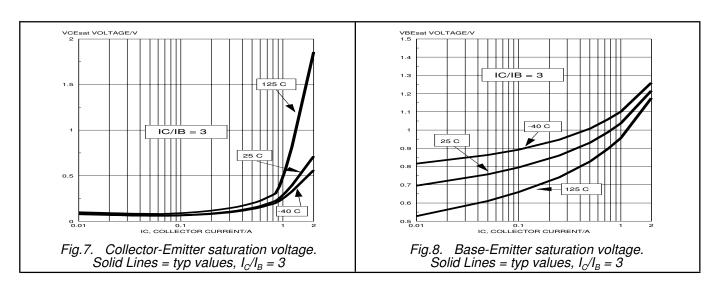
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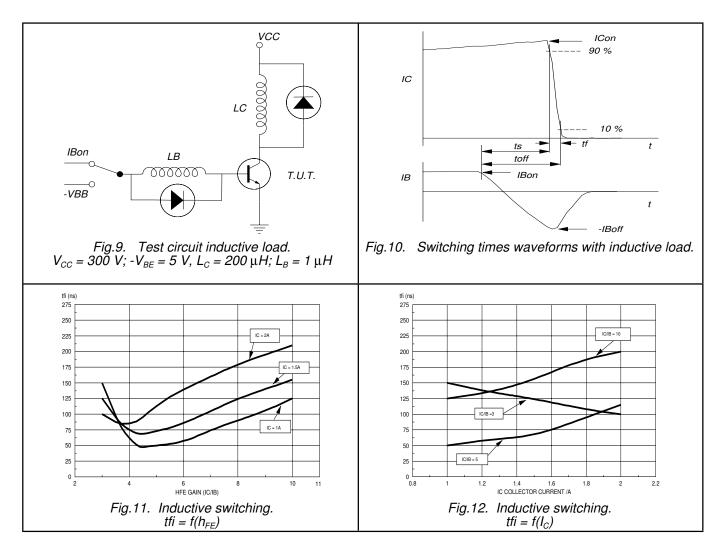
Product specification

Silicon Diffused Power Transistor

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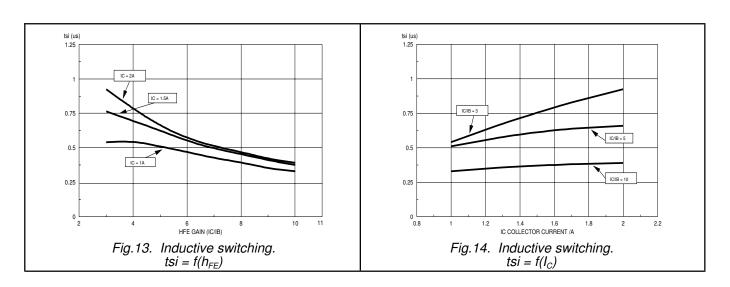
INDUCTIVE SWITCHING



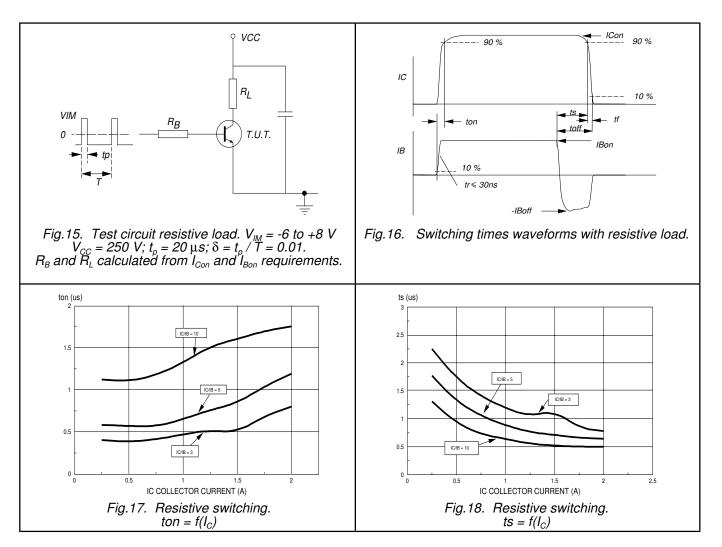
Product specification

Silicon Diffused Power Transistor

BUJ100



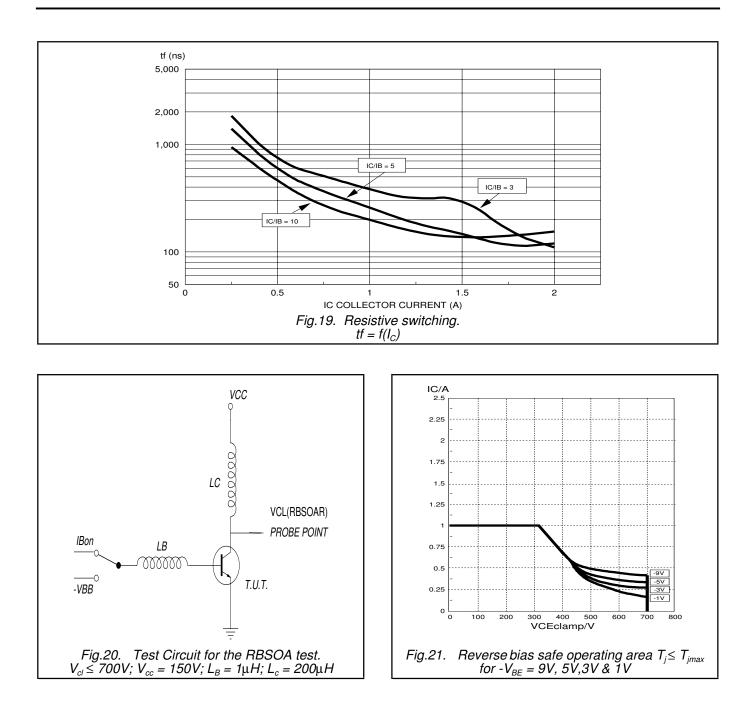
RESISTIVE SWITCHING



Product specification

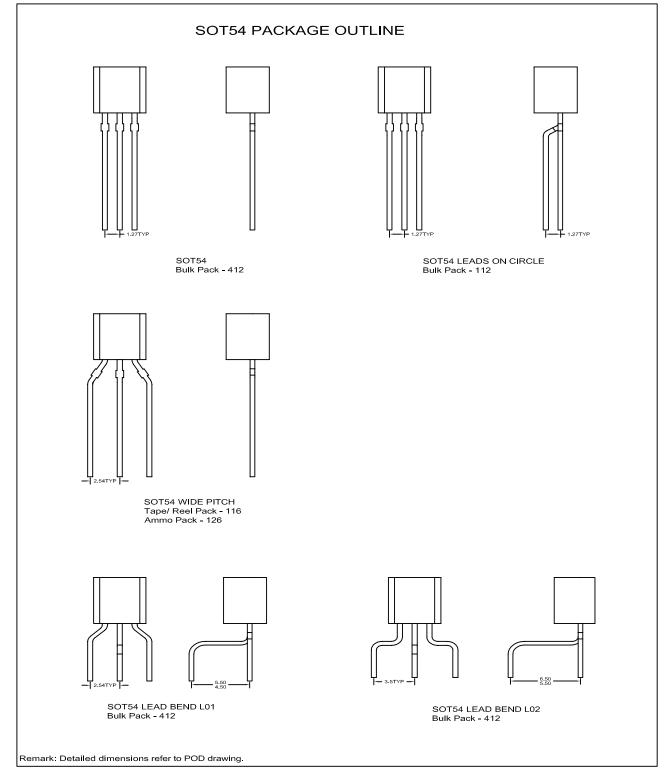
Silicon Diffused Power Transistor

BUJ100



BUJ100

MECHANICAL DATA



Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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