

Product data sheet

1. General description

High-voltage, high-speed planar-passivated NPN power switching transistor in a SOT78 (TO-220AB) plastic package.

2. Features and benefits

- Low thermal resistance
- Fast switching

3. Applications

- Inverters
- Motor control systems
- Electronic lighting ballasts
- DC-to-DC converters

4. Quick reference data

Table 1. Quick referen	ce data
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Symbol	Parameter	Conditions		Va	ues		Unit	
Absolute maximum rating								
V_{CESM}	peak collector-emitter voltage	V _{BE} = 0 V		7	00		V	
I _C	collector current (DC)				4		A	
P _{tot}	total power dissipation	T _{mb} ≤ 25 °C; <u>Fig. 1</u>	80		W			
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Static ch	aracteristics							
h_{FE}	DC current gain	I _C = 1 A; V _{CE} = 5 V; T _{mb} = 25 °C; Fig. 9		10	17	32		
		I_{c} = 500 mA; V_{ce} = 5 V; T_{mb} = 25 °C		13	22	32		

5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	mb	
2	С	collector	Ì Ì ◯ ſ	2
3	E	emitter		
mb	C	mounting base; connected to collector		3 sym056

6. Ordering information

Table 3. Ordering inform	nation		
Type number	e number Package		
	Name	Description	Version
BUJ103A	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

7. Marking

Table 4. Marking codes

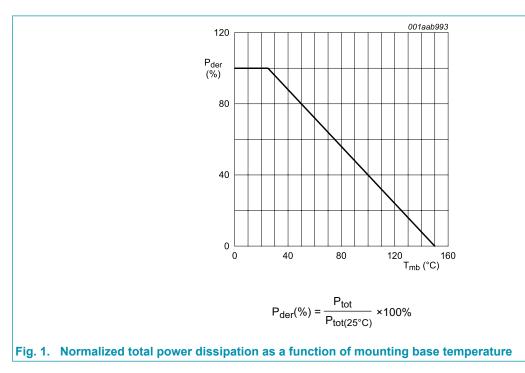
Type number	Marking codes
BUJ103A	BUJ103A

8. Limiting values

Table 5. Limiting values

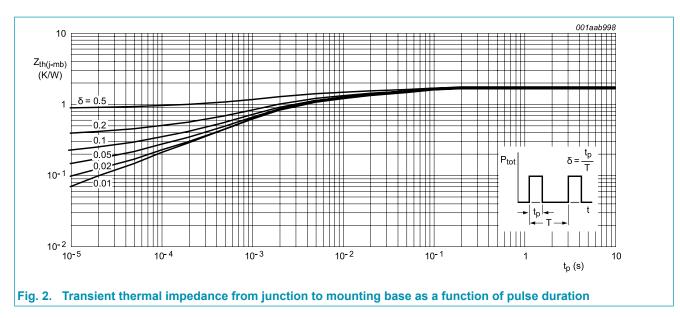
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Values	Unit
V _{CESM}	peak collector-emitter voltage	V _{BE} = 0 V	700	V
V _{CBO}	collector-base voltage	open emitter	700	V
V _{CEO}	collector-emitter voltage	open base	400	V
I _c	collector current (DC)		4	А
I _{CM}	peak collector current		8	А
I _B	base current (DC)		2	А
I _{BM}	peak base current		4	А
P _{tot}	total power dissipation	T _{mb} ≤ 25 °C; <u>Fig. 1</u>	80	W
T _{stg}	storage temperature		-65 to 150	°C
T _j	junction temperature		150	°C



9. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{\text{th(j-mb)}}$	thermal resistance from junction to mounting base	<u>Fig. 2</u>	-	-	1.56	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



Unit

mΑ

mΑ

mΑ

mΑ

mΑ

V

V

V

μs

μs

μs

μs

ns

10. Characteristics

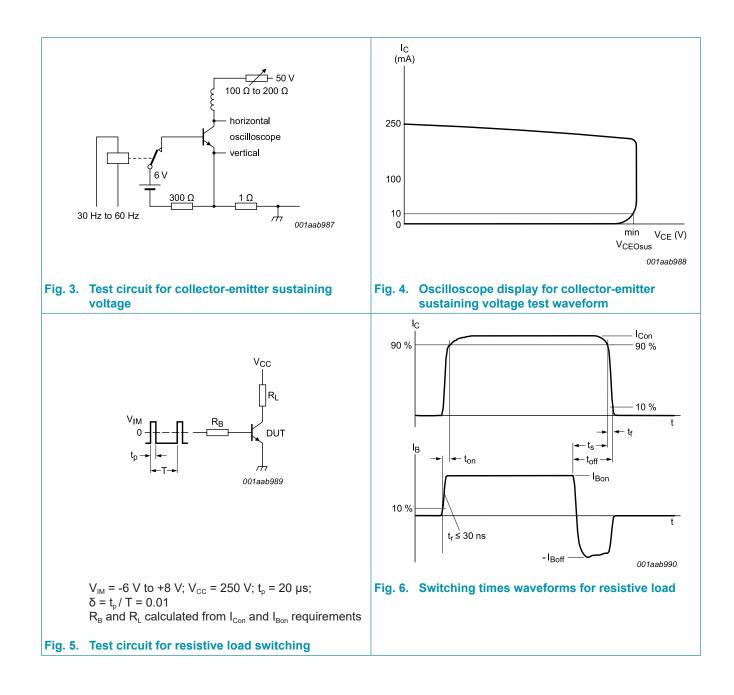
Table 7. Characteristics Symbol **Parameter Conditions** Min Typ Max Static characteristics V_{BE} = 0 V; V_{CE} = V_{CESMmax}; T_{mb} = 25 °C; [1] collector-emitter cut-off 1 I_{CES} _ _ current $V_{BE} = 0 V; V_{CE} = V_{CESMmax}; T_i = 125 °C; [1]$ 2 _ collector-base cut-off V_{BE} = 0 V; V_{CE} = V_{CESMmax}; T_{mb} = 25 °C; [1] 1 I_{CBO} -current V_{CEO} = V_{CEOMmax} = 400 V; T_{mb} = 25 °C; [1] collector-emitter cut-off 0.1 I_{CEO} _ current $V_{EB} = 7 \text{ V}; I_{C} = 0 \text{ A}; T_{mb} = 25 \text{ °C}$ emitter-base cut-off 0.1 I_{EBO} _ current V_{CEOsus} $I_B = 0 A$; $I_C = 10 mA$; L = 25 mH; collector-emitter 400 _ _ sustaining voltage T_{mb} = 25 °C; <u>Fig. 3</u>; <u>Fig. 4</u> $I_{c} = 3.0 \text{ A}; I_{B} = 0.6 \text{ A}; T_{mb} = 25 \text{ °C};$ V_{CEsat} collector-emitter 0.25 1 _ saturation voltage Fig. 10 $I_{c} = 3.0 \text{ A}; I_{B} = 0.6 \text{ A}; T_{mb} = 25 \text{ °C};$ 0.97 V_{BEsat} base-emitter saturation _ 1.5 Fig. 11 voltage DC current gain $I_{c} = 1 \text{ mA}; V_{ce} = 5 \text{ V}; T_{mb} = 25 \text{ °C};$ 17 10 32 h_{FE} Fig. 9 I_C = 500 mA; V_{CE} = 5 V; T_{mb} = 25 °C 13 22 32 I_{c} = 2.0 A; V_{ce} = 5 V; T_{mb} = 25 °C DC saturation current 11 16 22 h_{FEsat} gain I_C = 3.0 A; V_{CE} = 5 V; T_{mb} = 25 °C 12.5 _ _ **Dynamic characteristics** Switching times (resistive load); Fig. 5; Fig. 6 turn-on time $I_{Con} = 2.5 \text{ A}; I_{Bon} = -I_{Boff} = 0.5 \text{ A}; R_{L} = 75 \Omega;$ 0.52 0.6 ton _ T_{mb} = 25 °C $I_{Con} = 2.5 \text{ A}; I_{Bon} = -I_{Boff} = 0.5 \text{ A}; R_{L} = 75 \Omega;$ _ 2.7 3.3 t_{stg} storage time $T_{mb} = 25 \ ^{\circ}C$ $I_{Con} = 2.5 \text{ A}; I_{Bon} = -I_{Boff} = 0.5 \text{ A}; R_{L} = 75 \Omega;$ 0.3 0.35 t fall time _ T_{mb} = 25 °C Switching times (inductive load); Fig. 7; Fig. 8 storage time $I_{Con} = 2 A$; $I_{Bon} = 0.4 A$; $L_{B} = 1 \mu H$; 1.2 1.4 t_{stg} -V_{BB} = -5 V; T_{mb} = 25 °C $$\begin{split} I_{\text{Con}} &= 2 \text{ A}; \text{ } I_{\text{Bon}} = 0.4 \text{ A}; \text{ } L_{\text{B}} = 1 \text{ } \mu\text{H}; \\ V_{\text{BB}} &= -5 \text{ V}; \text{ } T_{\text{mb}} = 25 \text{ }^{\circ}\text{C} \end{split}$$ fall time 30 60 t_f -Switching times (inductive load); Fig. 7; Fig. 8

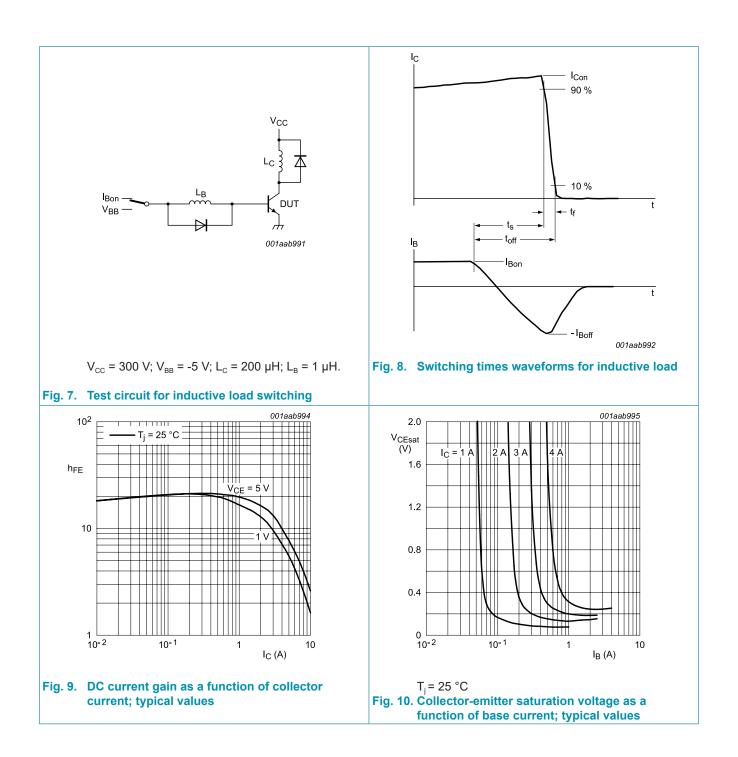
t _{stg}	storage time	$\begin{split} I_{Con} &= 2 \text{ A}; \ I_{Bon} = 0.4 \text{ A}; \ L_{B} = 1 \mu\text{H}; \\ V_{BB} &= -5 V; \ T_{j} = 100 \ ^{\circ}\text{C}; \ T_{mb} = 25 \ ^{\circ}\text{C} \end{split}$	-	-	1.8	μs
t _f	fall time	$\begin{split} I_{Con} &= 2 \text{ A}; \ I_{Bon} = 0.4 \text{ A}; \ L_{B} = 1 \mu\text{H}; \\ V_{BB} &= -5 V; \ T_{j} = 100 \ ^{\circ}\text{C}; \ T_{mb} = 25 \ ^{\circ}\text{C} \end{split}$	-	-	120	ns

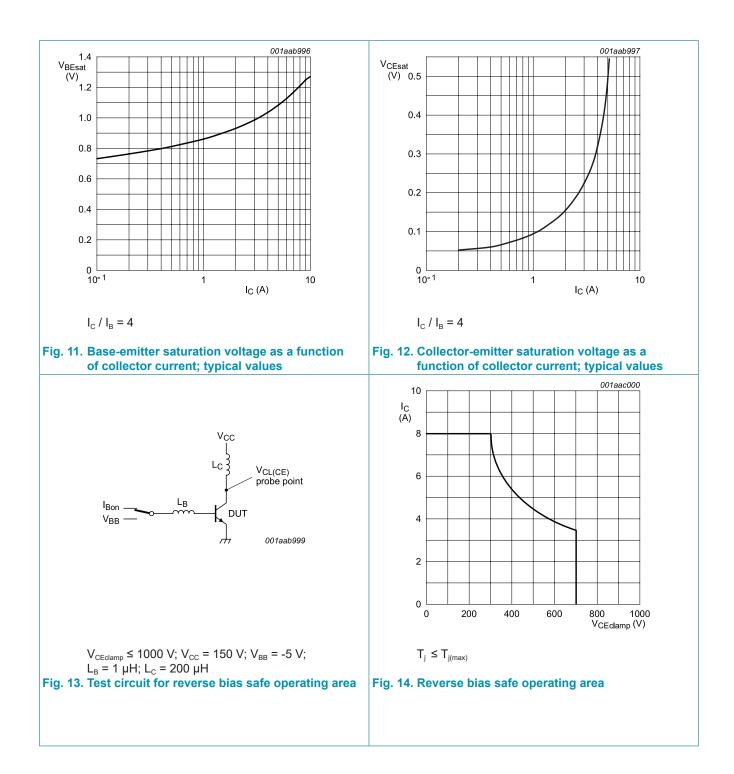
[1] Measured with half sine-wave voltage (curve tracer).

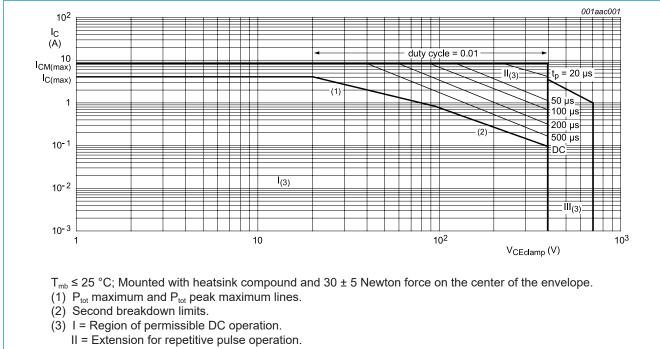
Silicon diffused power transistor

BUJ103A





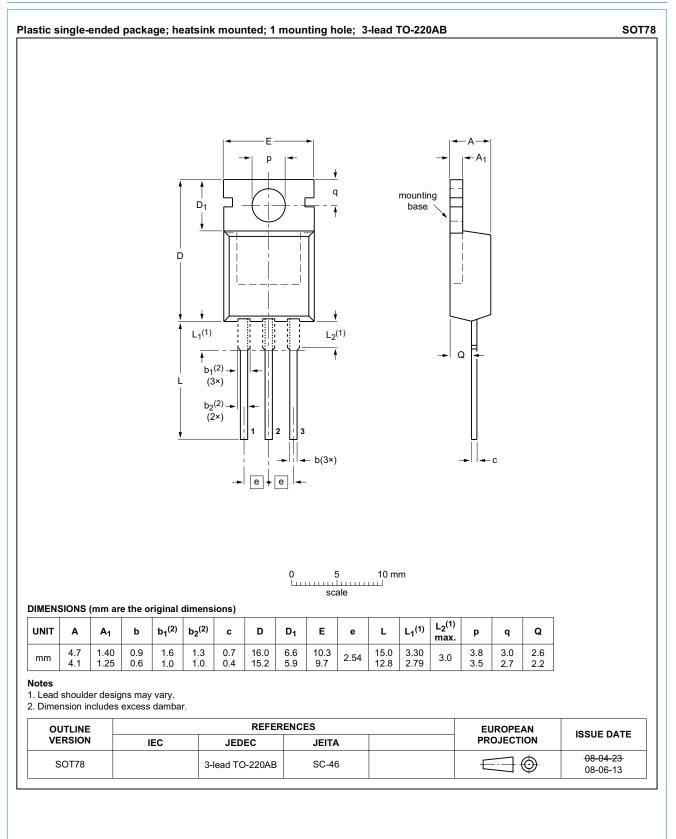




III = Extension during turn-on in single transistor converters provided that $R_{BE} \le 100 \Omega$ and $t_p \le 0.6 \mu s$.

Fig. 15. Forward bias safe operating area

11. Package outline



12. Revision history

Table 8. Revision hi	story							
Document ID	Release date	Data sheet status	Change notice	Supersedes				
BUJ103A v.5	20180329	Product data sheet	-	BUJ103A v.4				
Modifications: Change from NXP version to WeEn version								
BUJ103A v.4	20111108	Product data sheet	-	BUJ103A v.3				
Modifications:	 Modifications: The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. 							
BUJ103A v.3	20050303	Product data sheet	-	BUJ103A_HG v.2				
BUJ103A_HG v.2	19980918	Product data sheet	-	BUJ103A v.1				
BUJ103A v.1	19980801	Product data sheet	-	-				

BUJ103A

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.ween-semi.com</u>.

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