



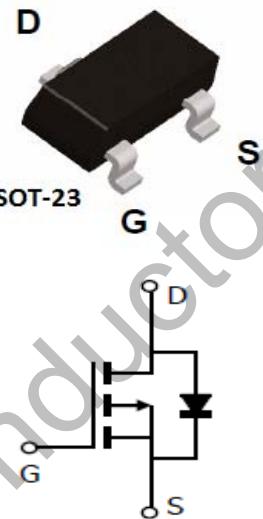
WG3401

30V P-Channel MOSFET

Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g = 6.4\text{nC}$ (Typ.).
- $\text{BV}_{DSS} = -30\text{V}, \text{I}_D = -4\text{A}$
- $\text{R}_{DS(on)} : 75\text{m}\Omega$ (Max) @ $\text{V}_G = -4.5\text{V}$
- 100% Avalanche Tested

SOT-23



MARKING:A19T

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current ^a	I_D	-4.0	A
$T_c = 70^\circ\text{C}$		-2.5	
Drain Current –Pulsed ^a	I_{DM}	-16.8	A
Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	1.56	W
Power Dissipation – Derate above 25°C		0.012	W/ $^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient1	$R_{\theta JA}$	90	$^\circ\text{C}/\text{W}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	30	-30*	---	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	---	---	-1	μA
Gate-Body Leakage	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
On Characteristics ^a						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.7	---	-1.3	V
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-4.2\text{A}$	---	50	60	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-3.5\text{A}$	---	62	75	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-2.0\text{A}$	---	80	110	$\text{m}\Omega$
Forward Transconductance	g_{fs}	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-3\text{A}$	---	5.5	---	S
Drain-Source Diode Characteristics ^a						
Continuous Source Current	I_{s}	$V_G=V_D=0\text{V}$, Force Current	---	---	-4.2	A
Pulsed Source Current	I_{sm}		---	---	-16.8	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-1.0\text{A}, T_J=25^\circ\text{C}$	---	---	-1.0	V
Dynamic Characteristics ^b						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	---	515	745	pF
Output Capacitance	C_{oss}		---	55	80	pF
Reverse Transfer Capacitance	C_{rss}		---	20	30	pF
Switching Characteristics ^b						
Total Gate Charge	Q_{g}	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-3\text{A}$	---	6.4	9	nC
Gate-Source Charge	Q_{gs}		---	0.9	1	nC
Gate-Drain Charge	Q_{gd}		---	1.6	3	nC
Turn-On Delay Time	$T_{\text{d(on)}}$	$V_{\text{DD}}=-10\text{V}, V_{\text{GS}}=-4.5\text{V}, R_{\text{G}}=25\Omega, I_{\text{D}}=-1\text{A}$	---	5	9	ns
Rise Time	T_{r}		---	17.4	33	ns
Turn-Off Delay Time	$T_{\text{d(off)}}$		---	40.7	80	ns
Fall Time	T_{f}		---	11.4	23	ns

Notes: a. Repetitive Rating: Pulsed width limited by maximum junction temperature.

b. Pulse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$. Essential independent of operating temperature.

c. Guaranteed by design, not subject to production testing.

Typical Characteristics

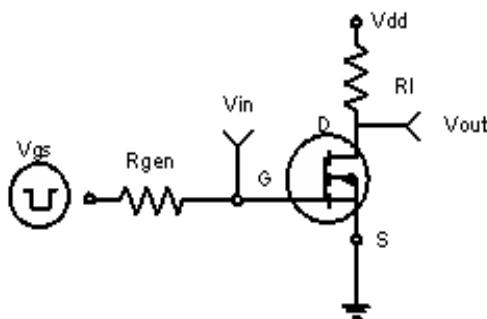


Figure 1:Switching Test Circuit

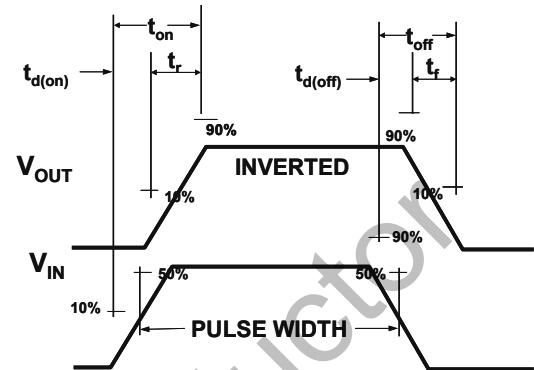


Figure 2:Switching Waveforms

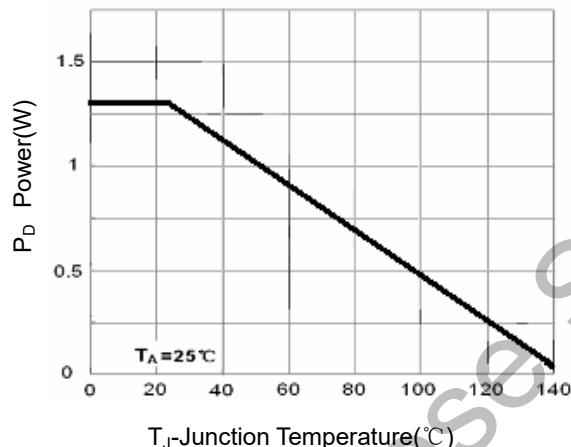


Figure 3 Power Dissipation

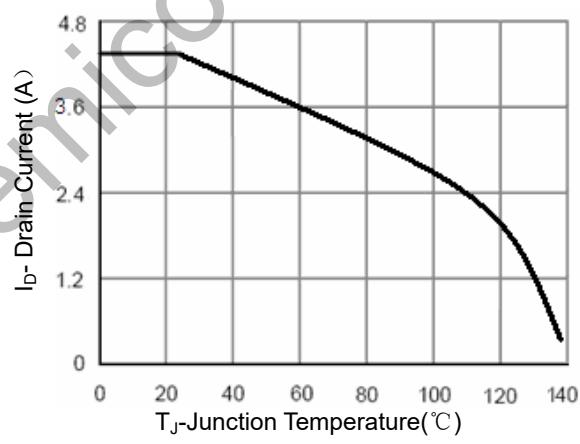


Figure 4 Drain Current

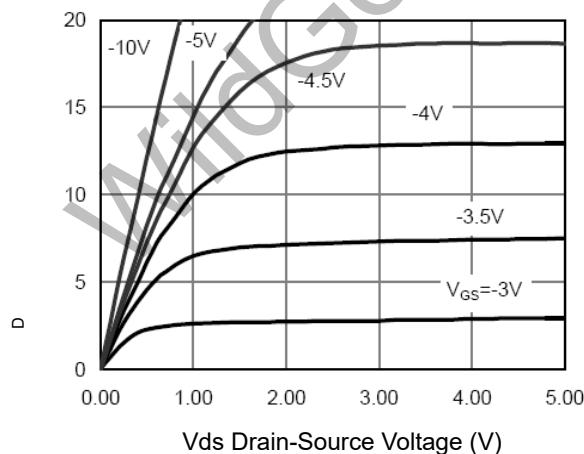


Figure 5 Output Characteristics

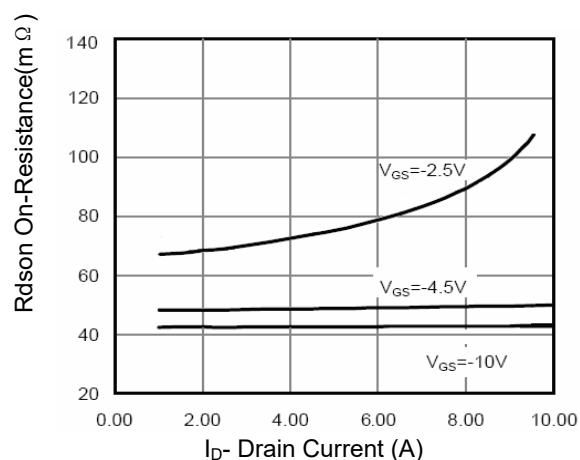
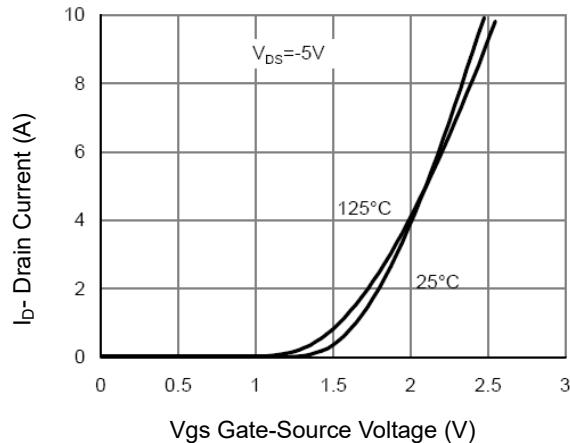
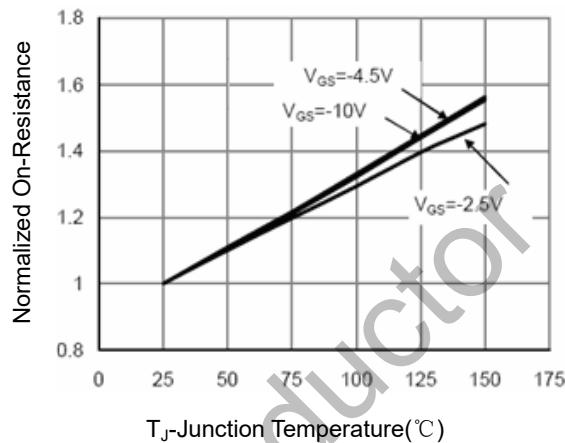
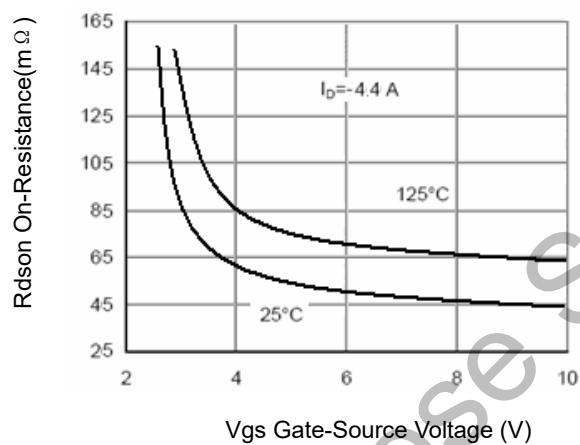
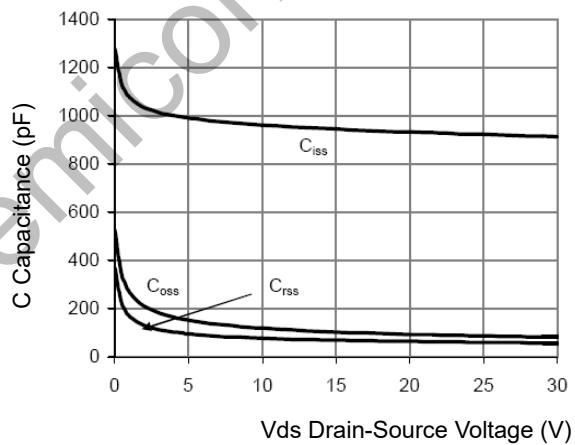
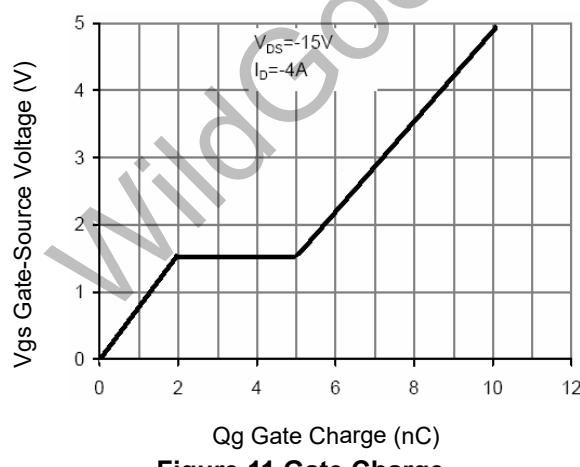
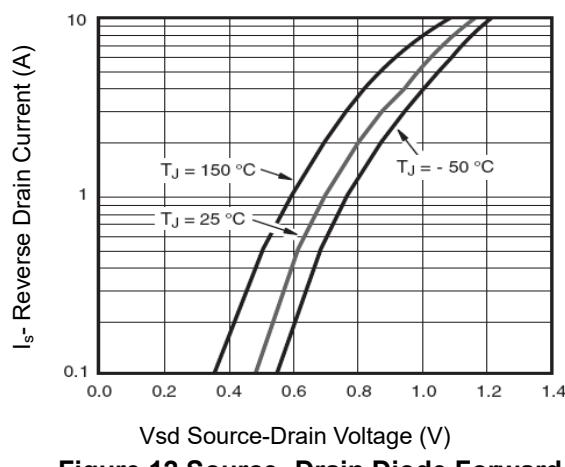
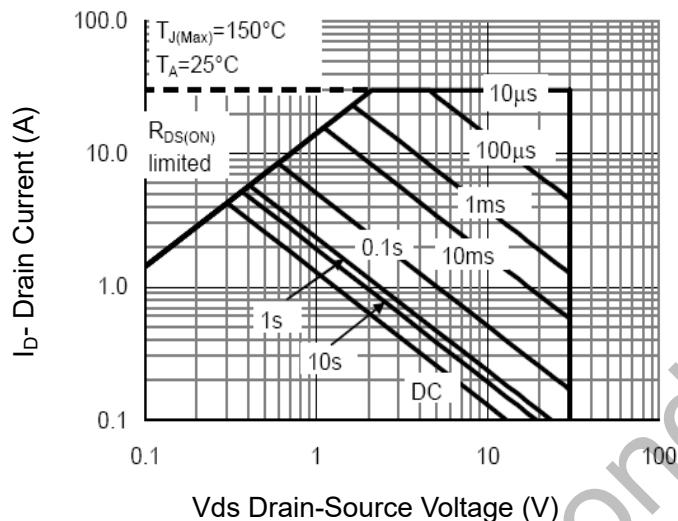
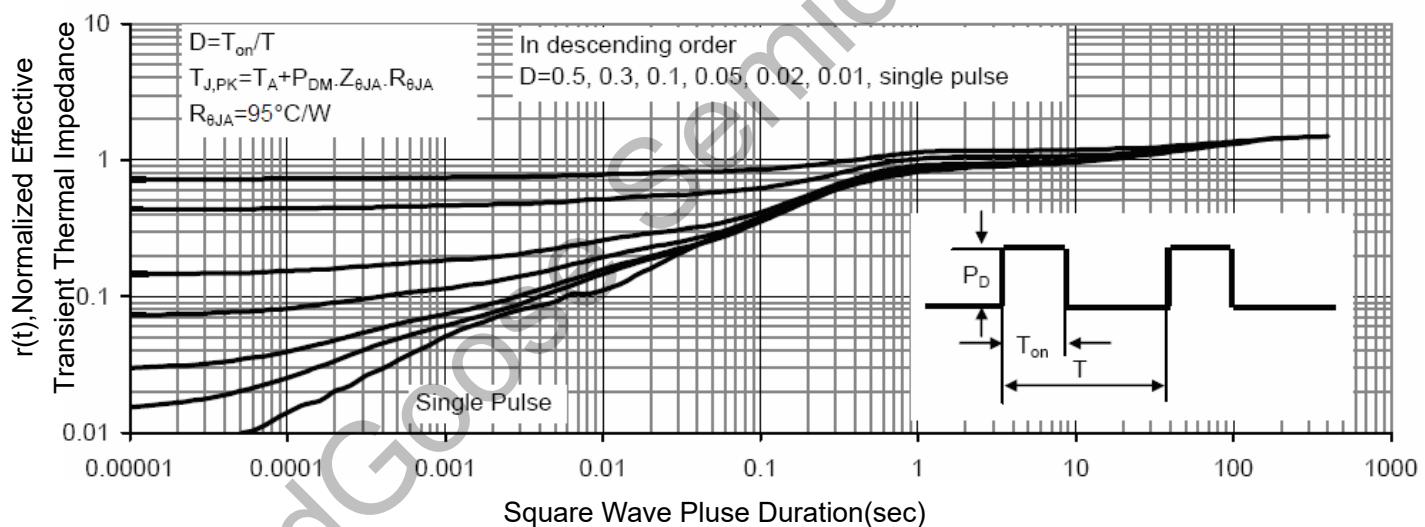


Figure 6 Drain-Source On-Resistance

Typical Characteristics (Continued)

**Figure 7 Transfer Characteristics****Figure 8 Drain-Source On-Resistance****Figure 9 $R_{DS(on)}$ vs V_{GS}** **Figure 10 Capacitance vs V_{DS}** **Figure 11 Gate Charge****Figure 12 Source-Drain Diode Forward**

Typical Characteristics (Continued)

**Figure 13 Safe Operation Area****Figure 14 Normalized Maximum Transient Thermal Impedance**

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