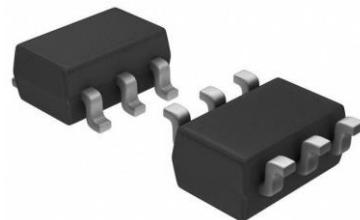


WNM01N11

Single N-Channel, 110V, 1.8A, Power MOSFET

[Http://www.sh-willsemi.com](http://www.sh-willsemi.com)

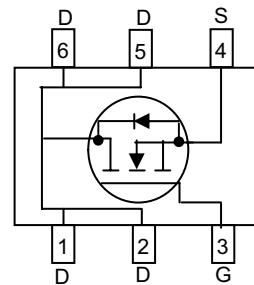
V_{DS} (V)	Typical R_{DS(on)} (Ω)
110	0.230@ V _{GS} =10V
	0.250@ V _{GS} =4.5V



SOT-23-6L

Descriptions

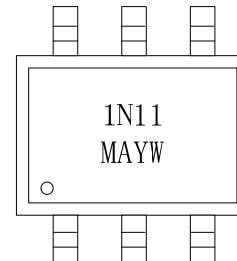
The WNM01N11 is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product WNM01N11 is Pb-free and Halogen-free.



Pin configuration (Top view)

Features

- Trench Technology
- Supper high density cell design
- Excellent ON resistance for higher DC current
- Small package SOT-23-6L



1N11 = Device Code

MA= Special Code

YW= Year&Week

Marking

Applications

- Driver for Relay, Solenoid, Motor, LED etc.
- DC-DC converter circuit
- Power Switch
- Load Switch
- Charging

Order information

Device	Package	Shipping
WNM01N11-6/TR	SOT-23-6L	3000/Reel&Tape

Absolute Maximum ratings

Parameter	Symbol	1S	10 S	Steady State	Unit	
Drain-Source Voltage	V _{DS}		110		V	
Gate-Source Voltage	V _{GS}		±20			
Continuous Drain Current ^{a d}	T _A =25°C	I _D	2.13	1.80	1.50	A
	T _A =70°C		1.70	1.44	1.20	
Maximum Power Dissipation ^{a d}	T _A =25°C	P _D	2.50	1.78	1.25	W
	T _A =70°C		1.60	1.14	0.80	
Continuous Drain Current ^{b d}	T _A =25°C	I _D	1.80	1.59	1.40	A
	T _A =70°C		1.44	1.27	1.12	
Maximum Power Dissipation ^{b d}	T _A =25°C	P _D	1.78	1.39	1.08	W
	T _A =70°C		1.14	0.88	0.69	
Pulsed Drain Current ^c	I _{DM}		7		A	
Operating Junction Temperature	T _J		-55 to 150		°C	
Lead Temperature	T _L		260		°C	
Storage Temperature Range	T _{stg}		-55 to 150		°C	

Thermal resistance ratings

Parameter	Symbol	Typical	Maximum	Unit	
Junction-to-Ambient Thermal Resistance ^a	t ≤ 1 s	R _{θJA}	36	50	°C/W
	t ≤ 10 s		50	70	
	Steady State		75	100	
Junction-to-Ambient Thermal Resistance ^b	t ≤ 1 s	R _{θJA}	50	70	°C/W
	t ≤ 10 s		75	90	
	Steady State		95	115	
Junction-to-Case Thermal Resistance	Steady State	R _{θJC}	55	70	

a Surface mounted on FR-4 Board using 1 square inch pad size, 1oz copper

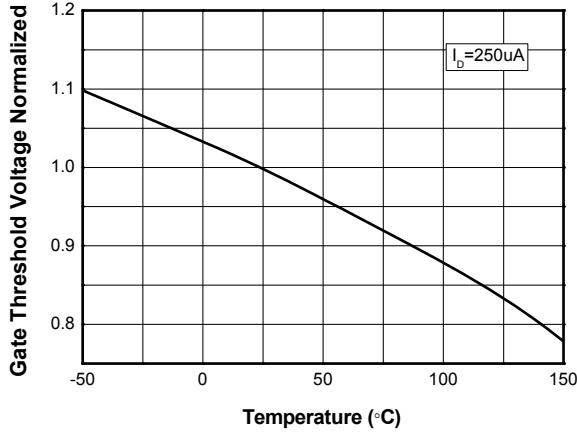
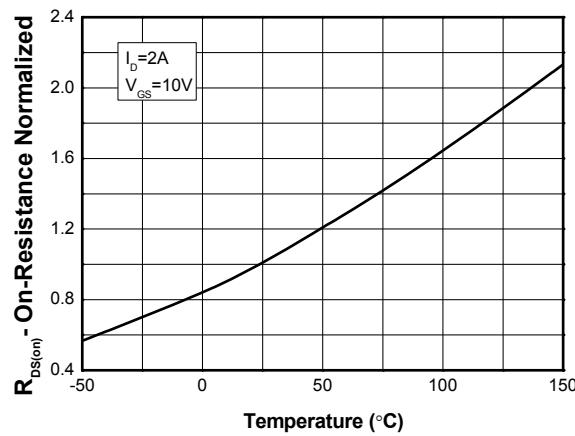
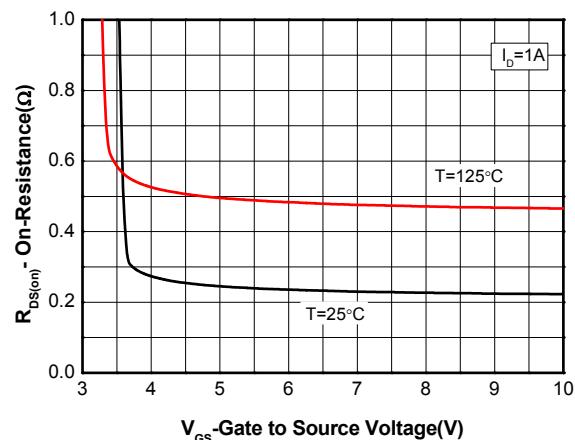
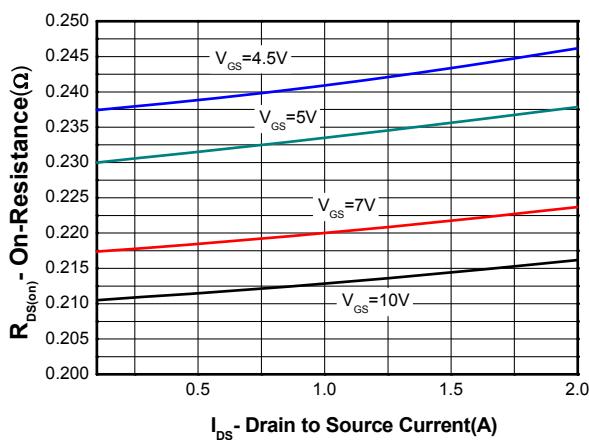
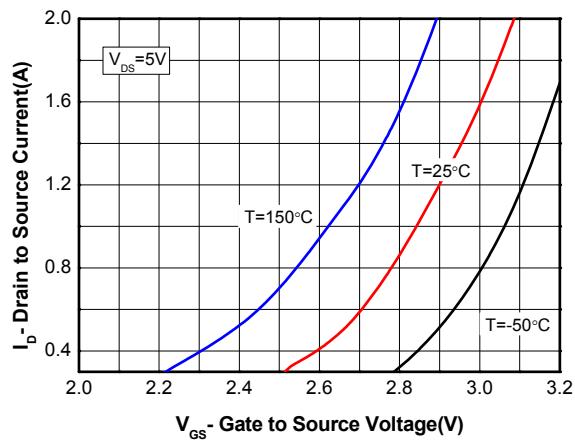
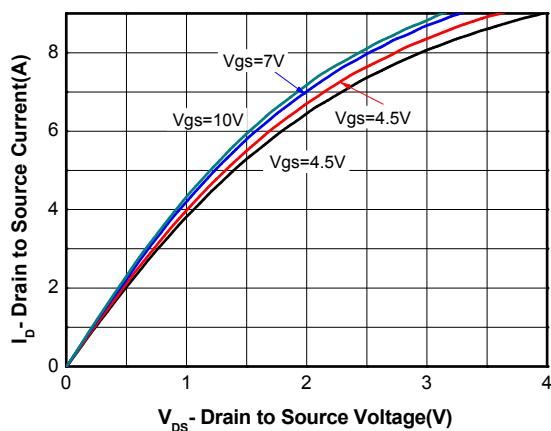
b Surface mounted on FR-4 board using minimum pad size, 1oz copper

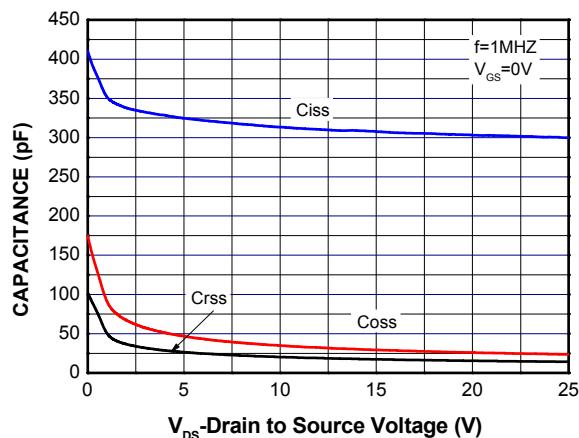
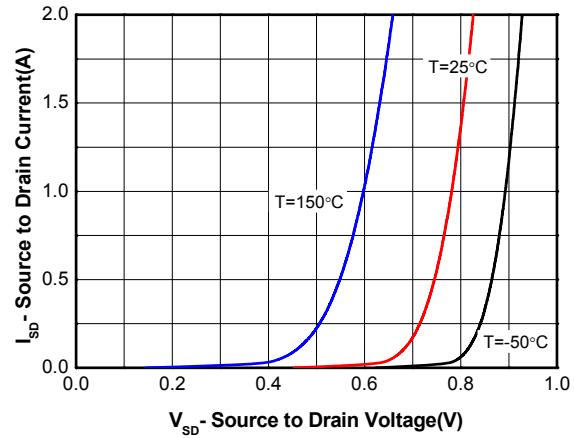
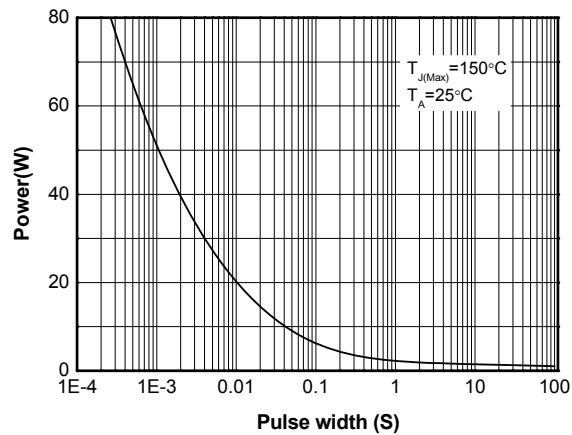
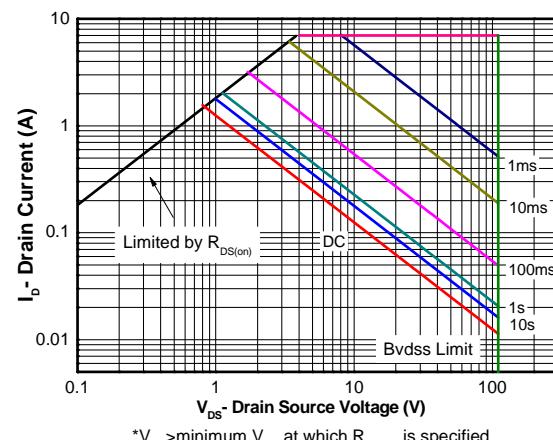
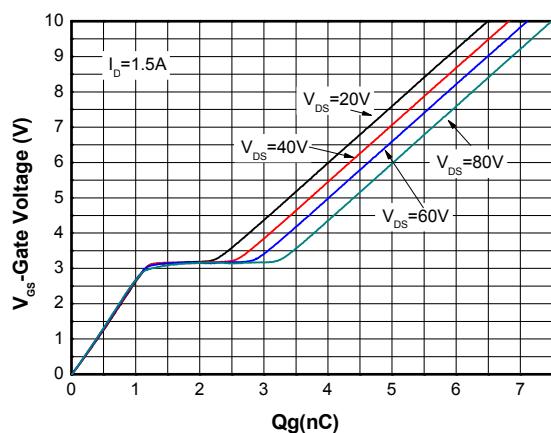
c Pulse width<380μs, Duty Cycle<2%

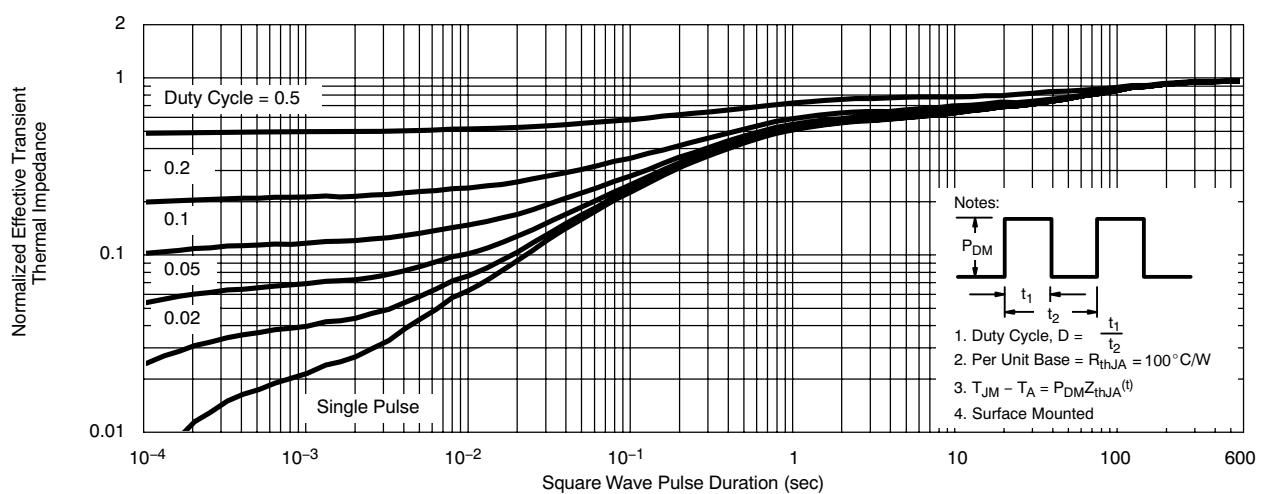
d Maximum junction temperature T_J=150°C.

Electronics Characteristics (Ta=25°C, unless otherwise noted)

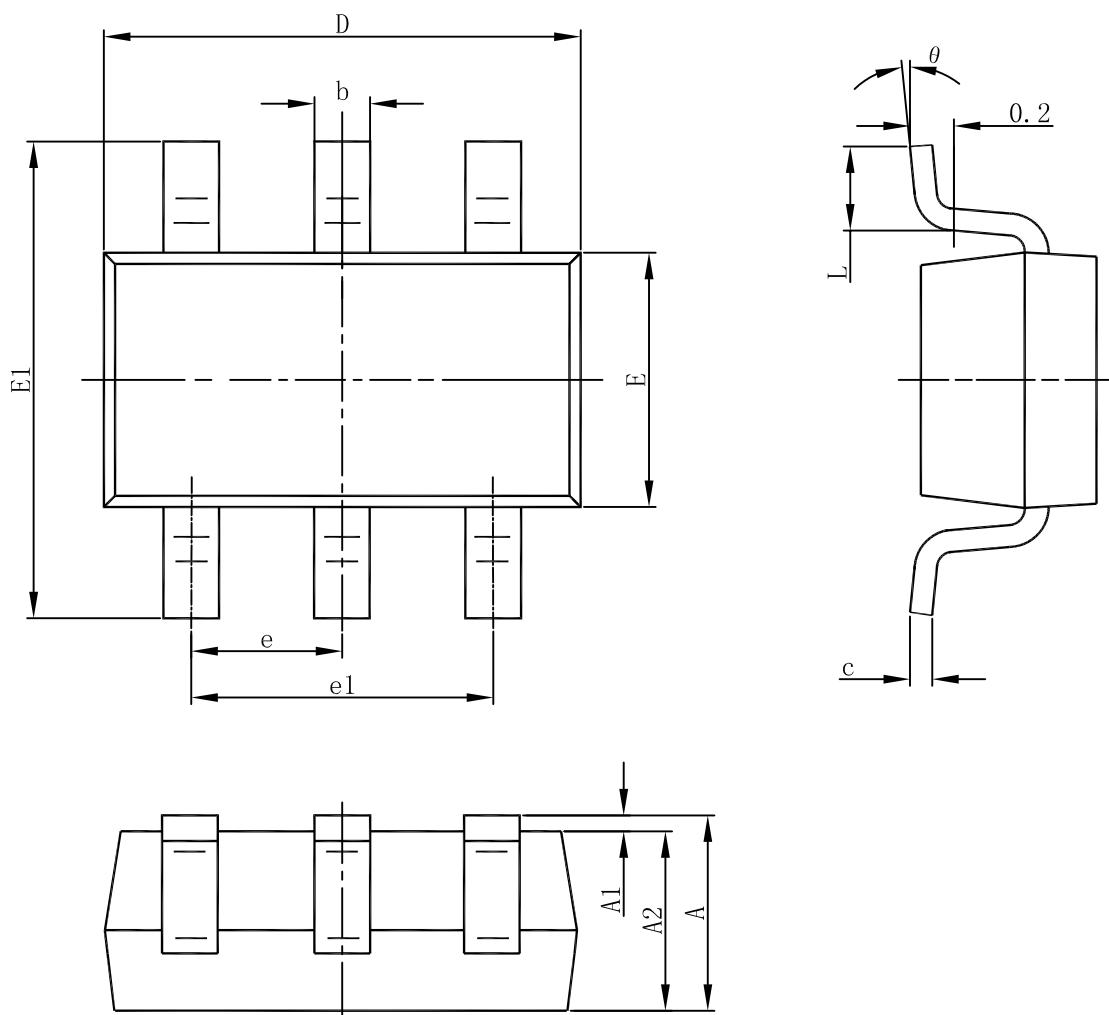
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250uA	110			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 90V, V _{GS} = 0V			1	uA
Gate-to-source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 250uA	1	1.9	2.5	V
Drain-to-source On-resistance ^{b, c}	R _{DS(on)}	V _{GS} = 10V, I _D = 1.4A		230	310	mΩ
		V _{GS} = 4.5V, I _D = 1.3A		250	350	
Forward Trans conductance	g _{fs}	V _{DS} = 10V, I _D = 3A		1.1		S
CAPACITANCES, CHARGES						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1.0 MHz, V _{DS} = 25 V		300		pF
Output Capacitance	C _{OSS}			25.6		
Reverse Transfer Capacitance	C _{RSS}			15.6		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DD} = 80 V, I _D = 1.5 A		7.5		nC
Threshold Gate Charge	Q _{G(TH)}			0.7		
Gate-to-Source Charge	Q _{GS}			1.1		
Gate-to-Drain Charge	Q _{GD}			2.1		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	td(ON)	V _{GS} = 10 V, V _{DD} = 50 V, R _L =50 Ω, R _G =3.3 Ω		11.8		ns
Rise Time	tr			13.2		
Turn-Off Delay Time	td(OFF)			32.8		
Fall Time	tf			4.8		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} = 0 V, I _S = 1A		0.8	1.2	V

Typical Characteristics (Ta=25°C, unless otherwise noted)


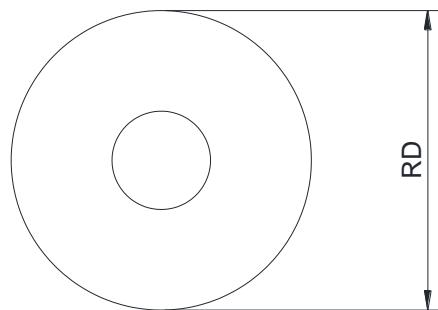
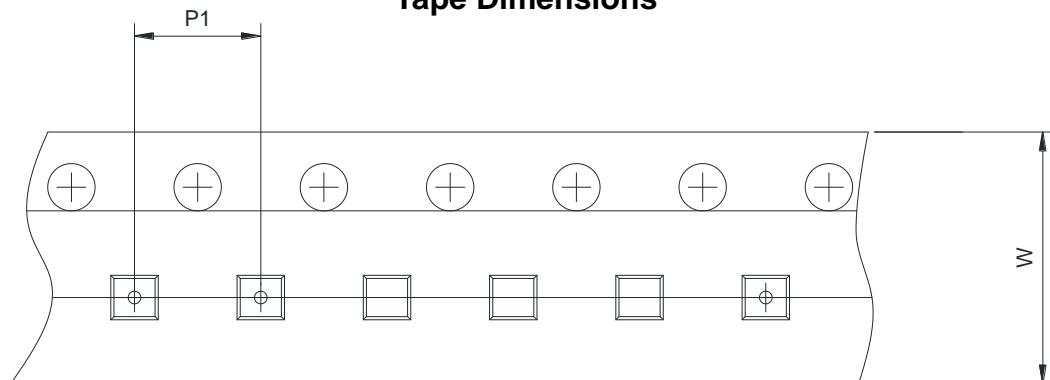
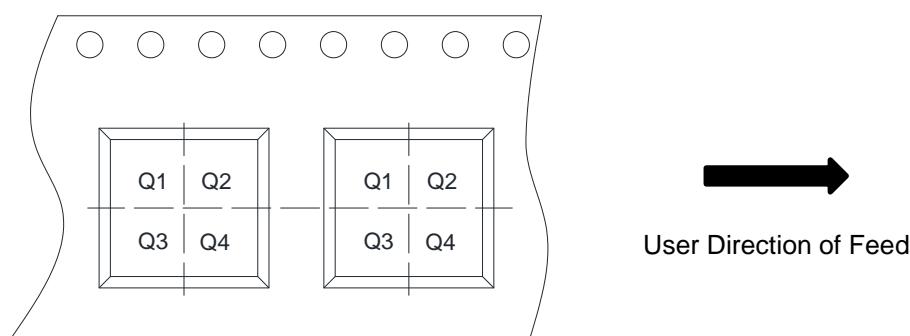

Capacitance

Body diode forward voltage

Single pulse power

Safe operating power

Gate charge Characteristics



Transient thermal response (Junction-to-Ambient)

Package outline dimensions
SOT-23-6L


Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	1.05	-	1.45
A1	0	-	0.15
A2	1.00	1.15	1.30
b	0.30	0.40	0.50
c	0.10	-	0.21
D	2.72	2.92	3.12
E1	2.60	2.80	3.00
E	1.40	1.60	1.80
e	0.85	0.95	1.05
e1	1.80	1.90	2.00
L	0.30	-	0.60
L1	0.25 BSC		
θ	0 °	-	8 °

TAPE AND REEL INFORMATION
Reel Dimensions

Tape Dimensions

Quadrant Assignments For PIN1 Orientation In Tape


RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch	<input type="checkbox"/> 13inch
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm	<input type="checkbox"/> 12mm <input type="checkbox"/> 16mm
P1	Pitch between successive cavity centers	<input type="checkbox"/> 2mm	<input checked="" type="checkbox"/> 4mm <input type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input type="checkbox"/> Q1	<input type="checkbox"/> Q2 <input checked="" type="checkbox"/> Q3 <input type="checkbox"/> Q4

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[EFC2J004NUZTDG](#) [FCAB21350L1](#) [P85W28HP2F-7071](#) [DMN1053UCP4-7](#) [NTE2384](#) [NTE2969](#) [NTE6400A](#) [DMC2700UDMQ-7](#)
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[DMN1006UCA6-7](#) [DMN16M9UCA6-7](#) [STF5N65M6](#) [IRF40H233XTMA1](#) [IPSA70R950CEAKMA1](#) [IPSA70R2K0CEAKMA1](#) [STU5N65M6](#)
[C3M0021120D](#)