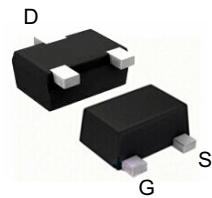


WNM3013

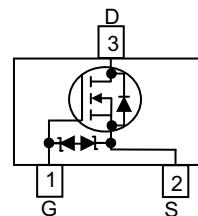
[Http://www.sh-willsemi.com](http://www.sh-willsemi.com)

Small Signal N-Channel, 50V, 0.25A, MOSFET

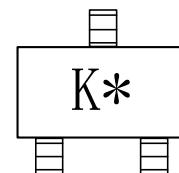
V_{DS} (V)	Typical R_{DS(on)} (Ω)
50	1.2@ V _{GS} =10V
	1.4@ V _{GS} =4.5V
	1.9@ V _{GS} =2.5V
	4.0@ V _{GS} =1.8V
ESD Rating: 2000V HBM	



SOT-723



Pin configuration (Top view)



K = Device Code

* = Month (A~Z)

Marking

Descriptions

The WNM3013 is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. This device is suitable for use in small signal switch. Standard Product WNM3013 is Pb-free and Halogen-free.

Features

- Trench Technology
- Supper high density cell design
- Excellent ON resistance for higher DC current
- HBM ESD protection >2 kV
- Small package SOT-723

Order information

Device	Package	Shipping
WNM3013-3/TR	SOT-723	8000/Reel&Tape

Applications

- Driver: Relay, Solenoid, Lamps,Hammers etc.
- Power supply converters circuit
- Load/Power Switching for potable device

Absolute Maximum ratings

Parameter	Symbol	10 S	Steady State	Unit
Drain-Source Voltage	V _{DS}	50		V
Gate-Source Voltage	V _{GS}	±20		
Continuous Drain Current ^{a d}	T _A =25°C	I _D	0.27	A
	T _A =70°C		0.21	
Maximum Power Dissipation ^{a d}	T _A =25°C	P _D	0.44	W
	T _A =70°C		0.28	
Continuous Drain Current ^{b d}	T _A =25°C	I _D	0.23	A
	T _A =70°C		0.18	
Maximum Power Dissipation ^{b d}	T _A =25°C	P _D	0.3	W
	T _A =70°C		0.2	
Pulsed Drain Current ^c	I _{DM}		1.0	A
Operating Junction Temperature	T _J		-55 to 150	°C
Lead Temperature	T _L		260	°C
Storage Temperature Range	T _{stg}		-55 to 150	°C

Thermal resistance ratings

Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ^a	t ≤ 10 s	R _{θJA}	225	285
	Steady State		270	
Junction-to-Ambient Thermal Resistance ^b	t ≤ 10 s	R _{θJA}	330	400
	Steady State		390	
Junction-to-Case Thermal Resistance	R _{θJC}	230	265	

a Surface mounted on FR-4 Board using 1 square inch pad size, 1oz copper

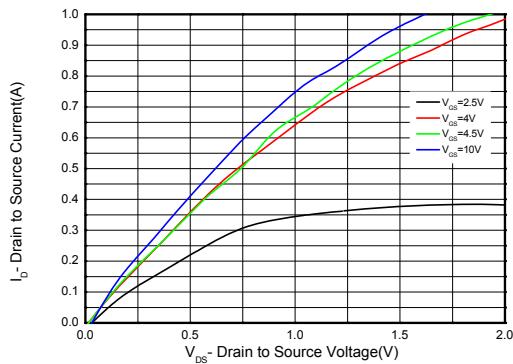
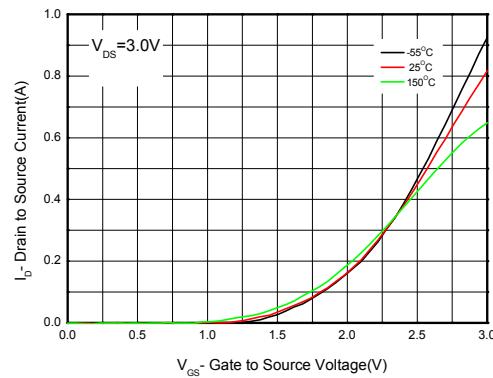
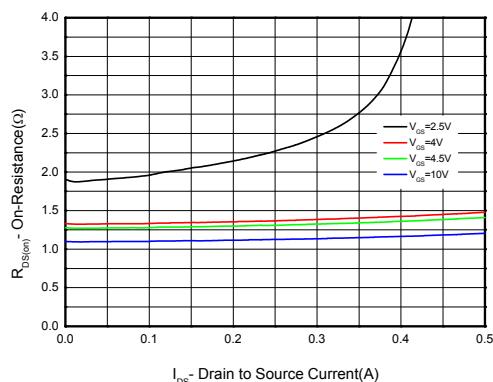
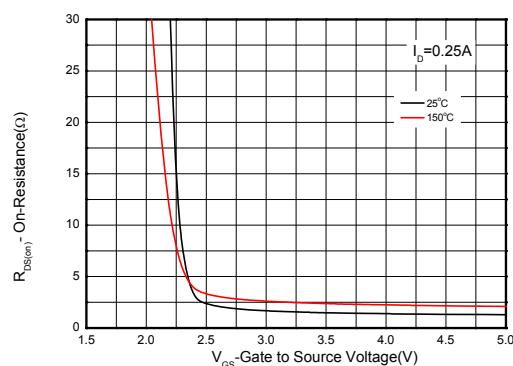
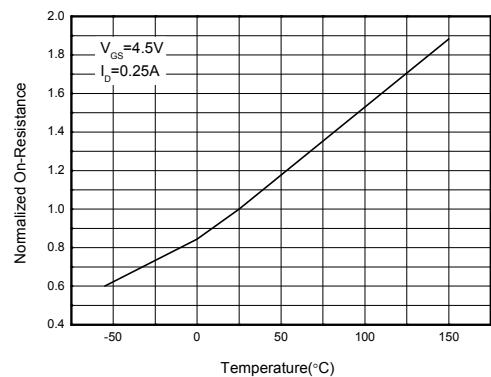
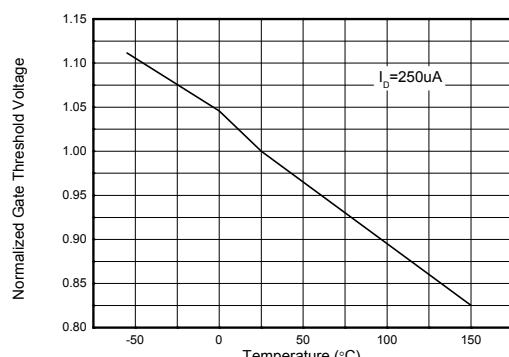
b Surface mounted on FR-4 board using minimum pad size, 1oz copper

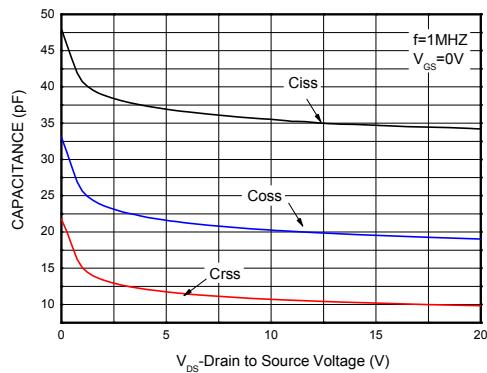
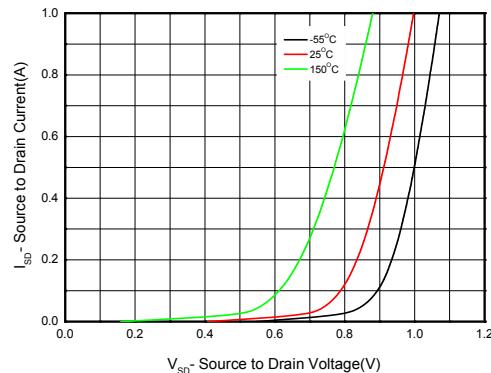
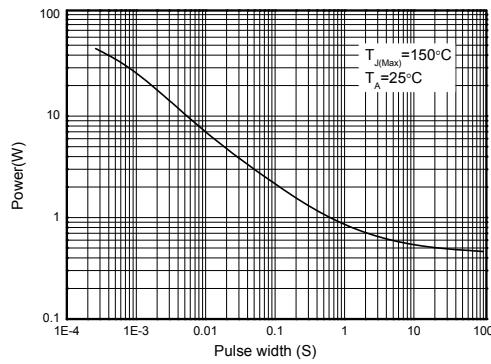
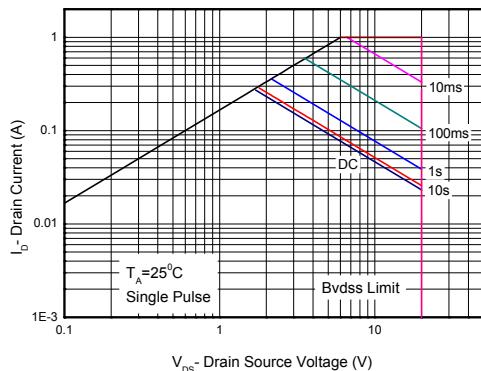
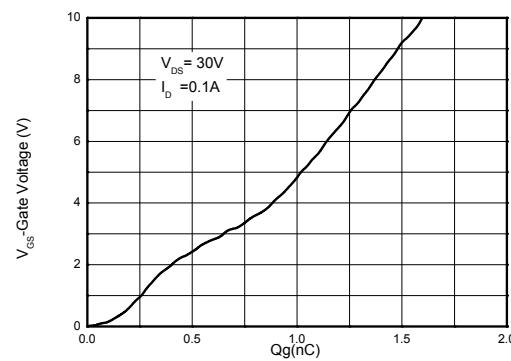
c Pulse width<380μs, Duty Cycle<2%

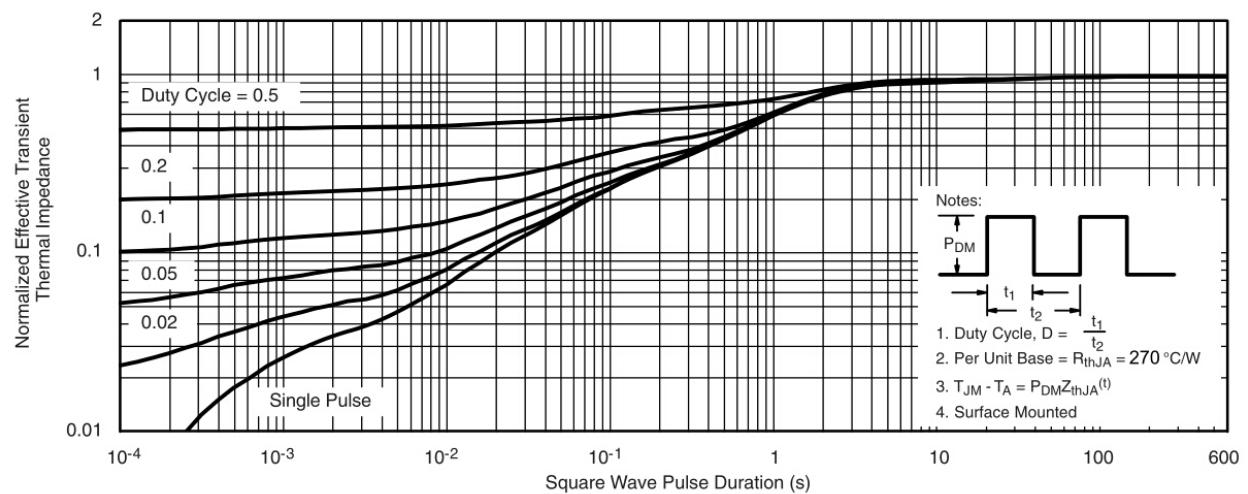
d Maximum junction temperature T_J=150°C.

Electronics Characteristics (Ta=25°C, unless otherwise noted)

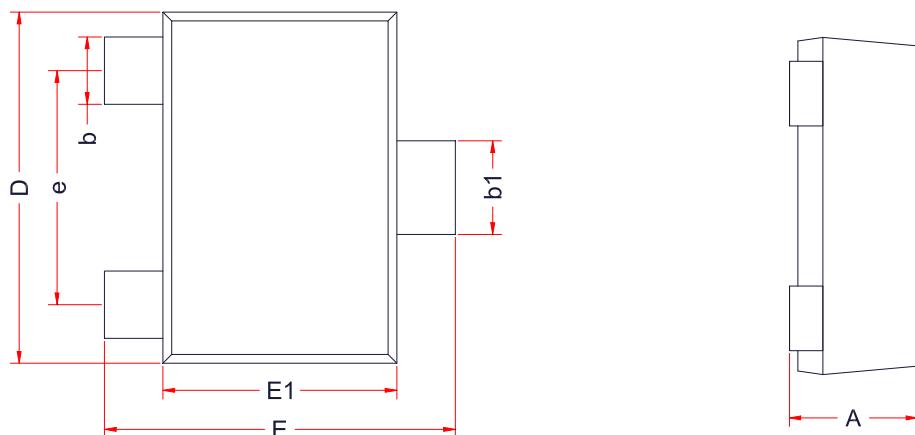
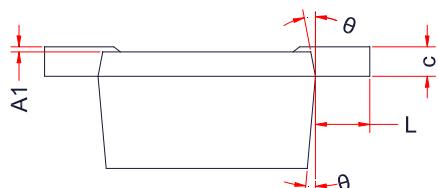
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250uA	50			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	uA
Gate-to-source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20V			±5	uA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 250uA	0.8	1.0	1.5	V
Drain-to-source On-resistance ^{b, c}	R _{DS(on)}	V _{GS} = 10V, I _D = 0.45A		1.2	3	Ω
		V _{GS} = 4.5V, I _D = 0.25A		1.3	4	
		V _{GS} = 4.0V, I _D = 0.25A		1.4	4	
		V _{GS} = 2.5V, I _D = 0.01A		1.9	6	
		V _{GS} = 1.8V, I _D = 0.01A		4.0	15	
Forward Trans conductance	g _{fs}	V _{DS} = 15V, I _D = 0.1A		0.5		S
CAPACITANCES, CHARGES						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, F = 1.0 MHz, V _{DS} = 5 V		36		pF
Output Capacitance	C _{OSS}			22		
Reverse Transfer Capacitance	C _{RSS}			12		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DD} = 30 V, I _D = 0.1 A		1.6		nC
Threshold Gate Charge	Q _{G(TH)}			0.25		
Gate-to-Source Charge	Q _{GS}			0.4		
Gate-to-Drain Charge	Q _{GD}			0.45		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V _{GS} = 5 V, V _{DD} = 5 V, R _L =500 Ω, R _G =10 Ω, I _D = 10m A		8.6		ns
Rise Time	t _r			4		
Turn-Off Delay Time	t _{d(OFF)}			23.8		
Fall Time	t _f			14.2		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} = 0 V, I _S = 0.25A		0.8	1.5	V

Typical Characteristics (Ta=25°C, unless otherwise noted)

Output characteristics

Transfer characteristics

On-Resistance vs. Drain current

On-Resistance vs. Gate-to-Source voltage

On-Resistance vs. Junction temperature

Threshold voltage vs. Temperature

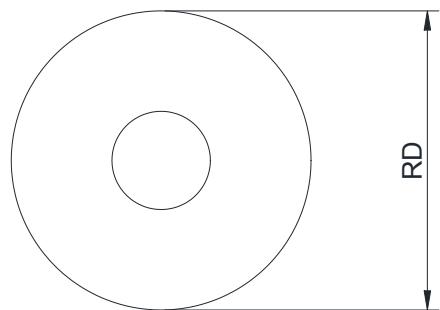
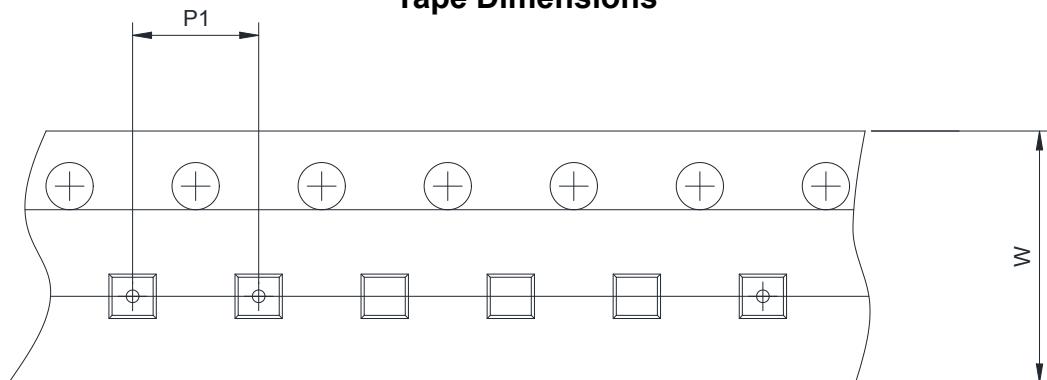
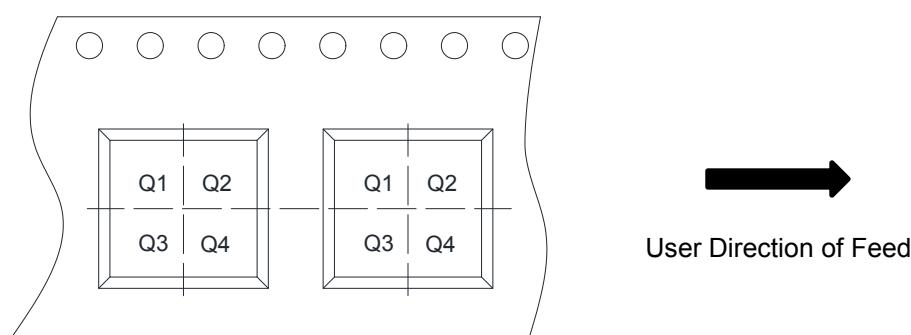

Capacitance

Body diode forward voltage

Single pulse power

Safe operating power

Gate charge Characteristics



Transient thermal response (Junction-to-Ambient)

Package outline dimensions
SOT-723

TOP VIEW
SIDE VIEW

SIDE VIEW

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.43	-	0.55
A1	0.00	-	0.05
c	0.08	0.13	0.18
b1	0.27	-	0.37
b	0.17	-	0.27
L1	0.15	0.20	0.25
D	1.15	1.20	1.25
E	1.15	1.20	1.25
E1	0.75	0.80	0.85
e	0.80 Ref.		
θ	7 ° Ref.		

TAPE AND REEL INFORMATION
Reel Dimensions

Tape Dimensions

Quadrant Assignments For PIN1 Orientation In Tape


RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch	<input type="checkbox"/> 13inch
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm	<input type="checkbox"/> 12mm <input type="checkbox"/> 16mm
P1	Pitch between successive cavity centers	<input checked="" type="checkbox"/> 2mm	<input type="checkbox"/> 4mm <input type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input type="checkbox"/> Q1	<input type="checkbox"/> Q2 <input checked="" type="checkbox"/> Q3 <input type="checkbox"/> Q4

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[EFC2J004NUZTDG](#) [FCAB21350L1](#) [P85W28HP2F-7071](#) [DMN1053UCP4-7](#) [NTE2384](#) [NTE2969](#) [NTE6400A](#) [DMC2700UDMQ-7](#)
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[C3M0021120D](#) [DMN6022SSD-13](#)