WPM3035

Single P-Channel,-30V,-23A,Power MOSFET

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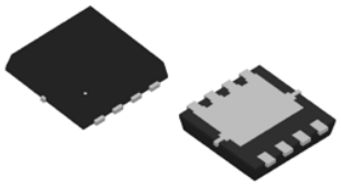
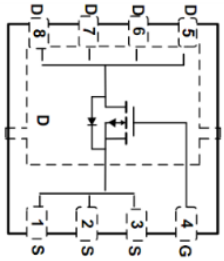
V_{DS} (V)	Typical $R_{DS(on)}$ (mΩ)
-30	16.5 @ V_{GS}=-10V
	24.0 @ V_{GS}=-4.5V



PDFN3333-8L

Descriptions

The WPM3035 is P-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product WPM3035 is Pb-free.



Pin configuration (Top view)

Features

- Trench Technology
- Supper high density cell design
- Excellent ON resistance
- Extremely Low Threshold Voltage
- Small package PDFN3333-8L

3035 = Device Code
PS =Special Code
Y = Year
W = Week(A~z)

Marking

Applications

- DC/DC converters
- Power supply converters circuit
- Load/Power Switching for portable device

Order information

Device	Package	Shipping
WPM3035-8/TR	PDFN3333-8L	2500/Tape&Reel

Absolute Maximum ratings

Parameter		Symbol	Maximum	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current [d]	T_C=25°C	I_D	-23	A
	T_C=100°C		-14	A
Pulsed Drain Current [c]		I_{DM}	-80	A
Continuous Drain Current	T_A=25°C	I_{DSM}	-11	A
	T_A=70°C		-9	
Avalanche Energy L=0.3mH		E_{AS}	43	mJ
Power Dissipation [b]	T_C=25°C	P_D	15	W
	T_C=100°C		6	
Power Dissipation [a]	T_A=25°C	P_{DSM}	3.7	W
	T_A=70°C		2.4	
Operating Junction Temperature		T_J	-55 to 150	°C
Storage Temperature Range		T_{STG}	-55 to 150	°C

Thermal resistance ratings

Single Operation					
Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance [a]	t ≤ 10 s	$R_{\theta JA}$	27	34	°C/W
	Steady State		54	67	
Junction-to-Case Thermal Resistance	Steady State	$R_{\theta JC}$	6.5	8.1	

Note:

a The value of $R_{\theta JA}$ is measured with the device mounted on 1-inch^2 (6.45cm^2) with 2oz.(0.071mm thick) Copper pad on a 1.5*1.5 inch^2, 0.06-inch thick FR4 PCB, in a still air environment with T_A =25°C. The power dissipation P_{DSM} is based on $R_{\theta JA}$ t≤10s value and the $T_{J(MAX)}$=150°C. The value in any given application is determined by the user's specific board design.

b The power dissipation P_D is based on $T_{J(MAX)}$=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

c Repetitive rating, ~10us pulse width, duty cycle ~1%, keep initial T_J =25°C, the maximum allowed junction temperature of 150°C.

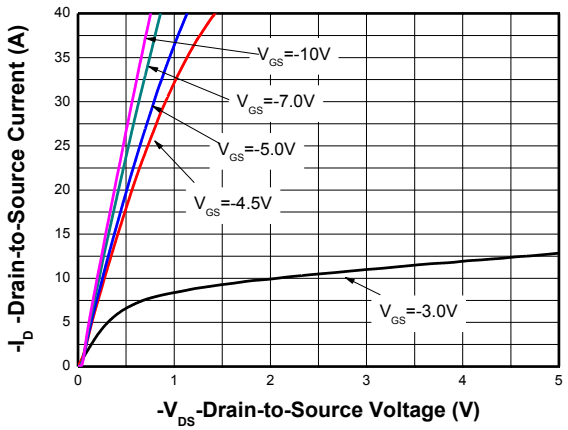
d The maximum current rating by source bonding technology.

e The static characteristics are obtained using ~380us pulses, duty cycle ~1%.

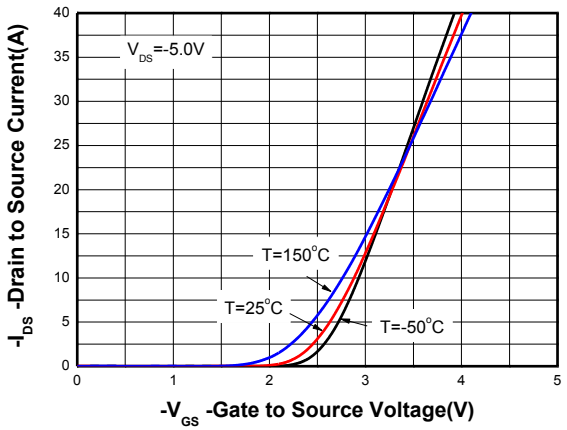
Electronics Characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV_{DSS}	V_{GS} = 0 V, I_D = -250uA	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =-24V, V_{GS} = 0V			-1	uA
Gate-to-source Leakage Current	I_{GSS}	V_{DS} = 0 V, V_{GS} = ±20V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	V_{GS} = V_{DS}, I_D = -250uA	-1.3	-1.6	-2.4	V
Drain-to-source On-resistance	$R_{DS(on)}$	V_{GS} =-10V, I_D = -10A		16.5	20	mΩ
		V_{GS} =-4.5V, I_D = -8A		24.0	32	
Forward Transconductance	g_{FS}	V_{DS} = -15 V, I_D = -10A		6		S
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C_{ISS}	V_{GS} = 0 V, F = 1.0MHz, V_{DS} = -15 V		1200		pF
Output Capacitance	C_{OSS}			180		
Reverse Transfer Capacitance	C_{RSS}			130		
Total Gate Charge	$Q_{G(TOT)}$	V_{GS} = -10 V, V_{DS} = -24 V, I_D =-10 A		20		nC
Threshold Gate Charge	$Q_{G(TH)}$			1.7		
Gate-to-Source Charge	Q_{GS}			3.4		
Gate-to-Drain Charge	Q_{GD}			3.6		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	td(ON)	V_{GS} = -10 V, V_{DD} =-24 V, I_D=-10A, R_G=3.3Ω		10		ns
Rise Time	tr			19.2		
Turn-Off Delay Time	td(OFF)			53.6		
Fall Time	tf			14.4		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V_{SD}	V_{GS} = 0 V, I_S = -1A		-0.7	-1.2	V

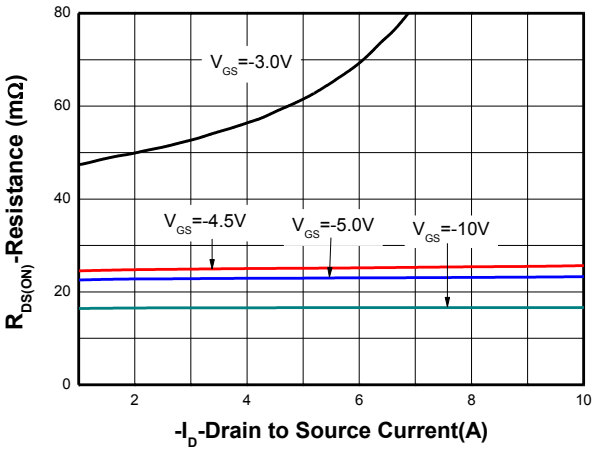
Typical Characteristics (Ta=25°C, unless otherwise noted)



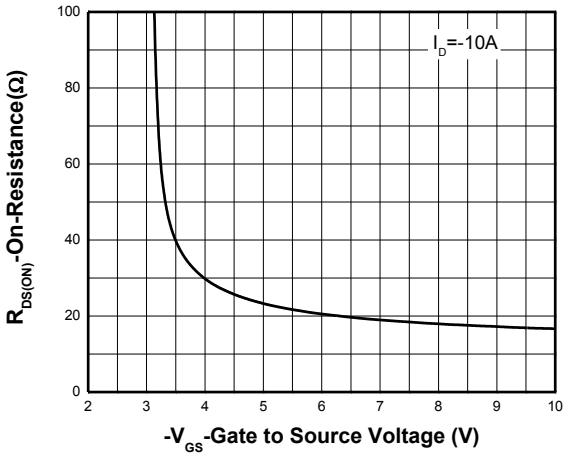
Output Characteristics [e]



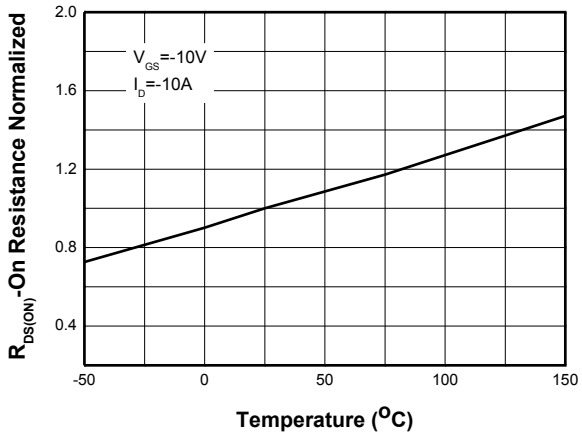
Transfer Characteristics [e]



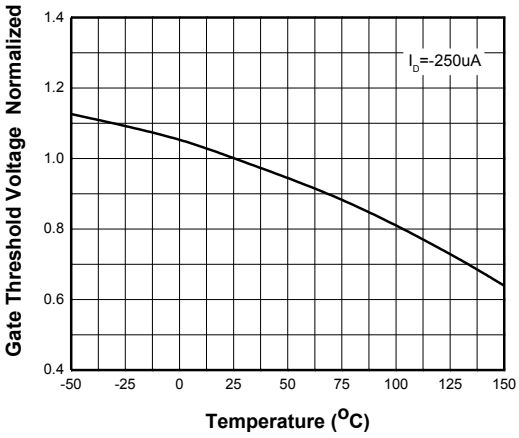
On-Resistance vs. Drain Current [e]



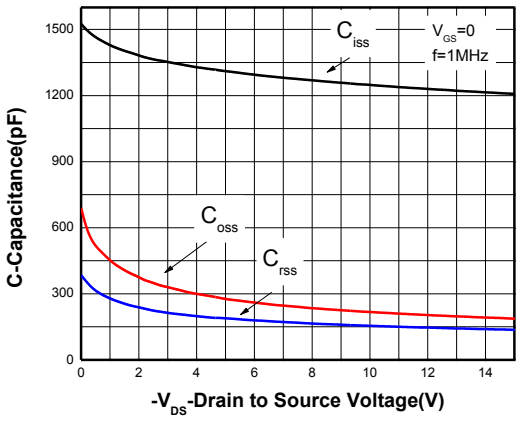
On-Resistance vs. Gate-to-Source Voltage [e]



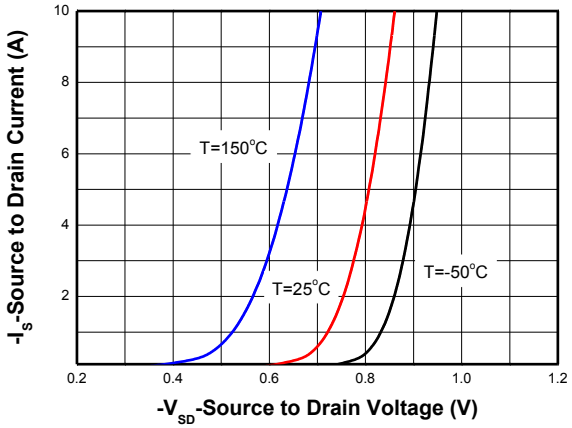
On-Resistance vs. Junction Temperature [e]



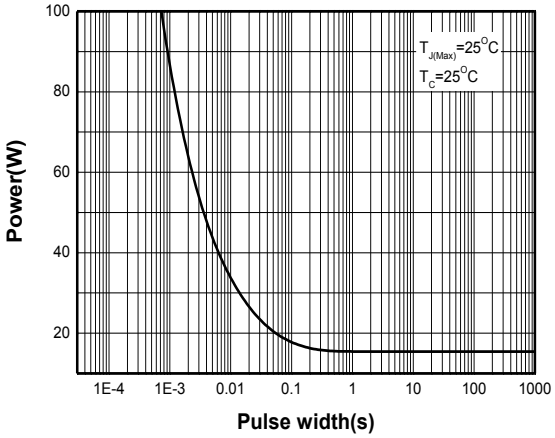
Threshold voltage vs. Temperature



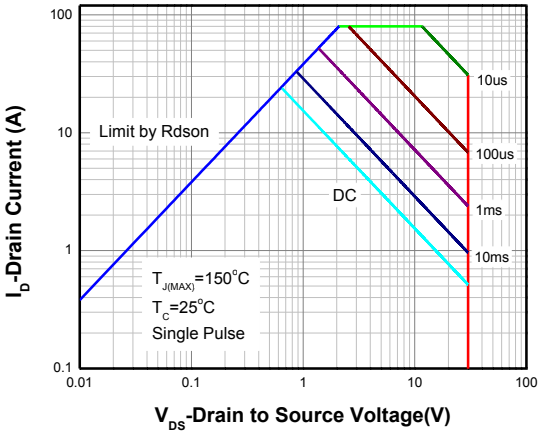
Capacitance



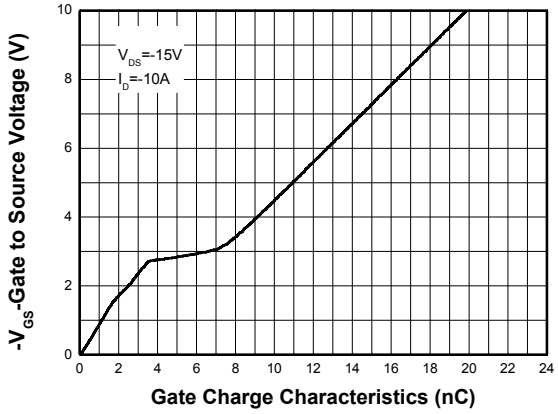
Body Diode Forward Voltage [e]



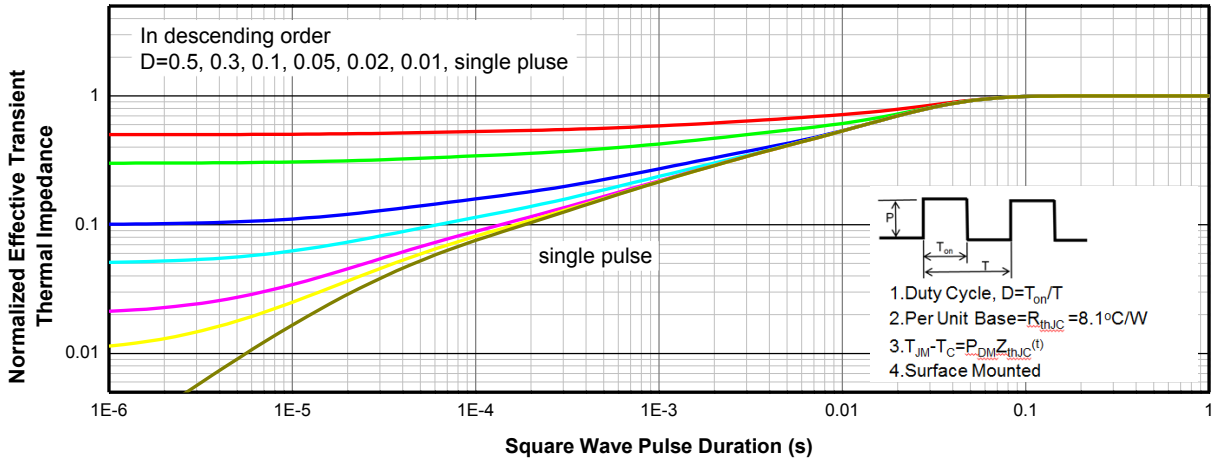
Single pulse power



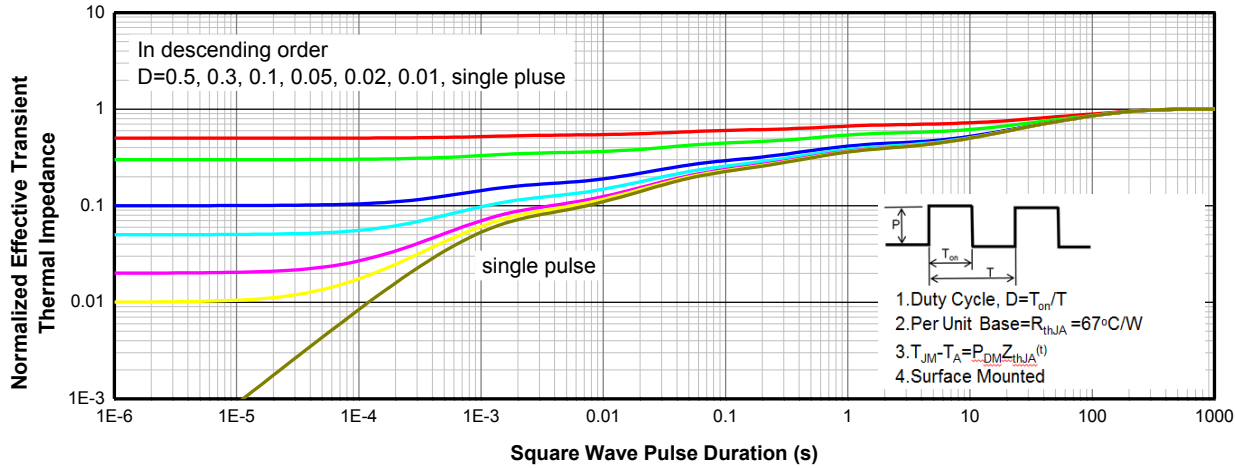
Safe operating power



Gate Charge Characteristics



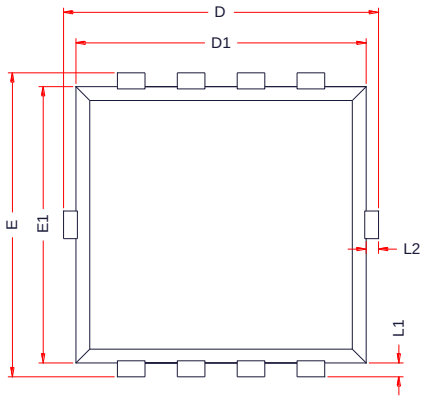
Transient thermal response (Junction-to-Case)



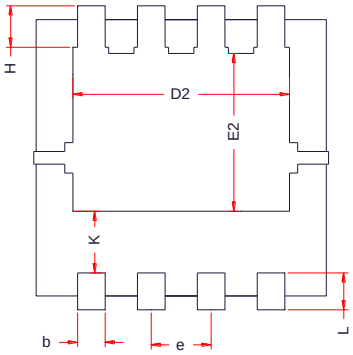
Transient Thermal Response (Junction-to-Ambient)

PACKAGE OUTLINE DIMENSIONS

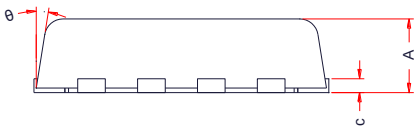
PDFN3333-8L



TOP VIEW



BOTTOM VIEW

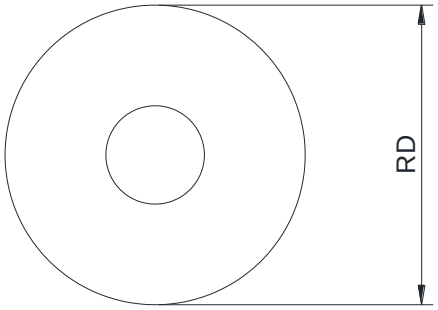


SIDE VIEW

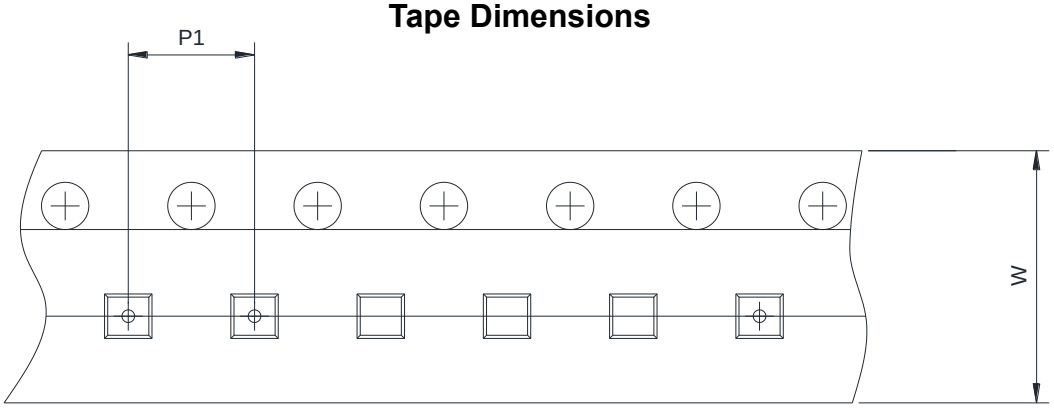
Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.70	0.80	0.90
b	0.25	0.30	0.35
c	0.14	0.15	0.20
D	3.10	3.30	3.50
D1	3.05	3.15	3.25
D2	2.35	2.45	2.55
e	0.55	0.65	0.75
E	3.10	3.30	3.50
E1	2.90	3.00	3.10
E2	1.64	1.74	1.84
H	0.32	0.42	0.52
K	0.59	0.69	0.79
L	0.25	0.40	0.55
L1	0.10	0.15	0.20
L2	-	-	0.15
θ	8°	10°	12°

TAPE AND REEL INFORMATION

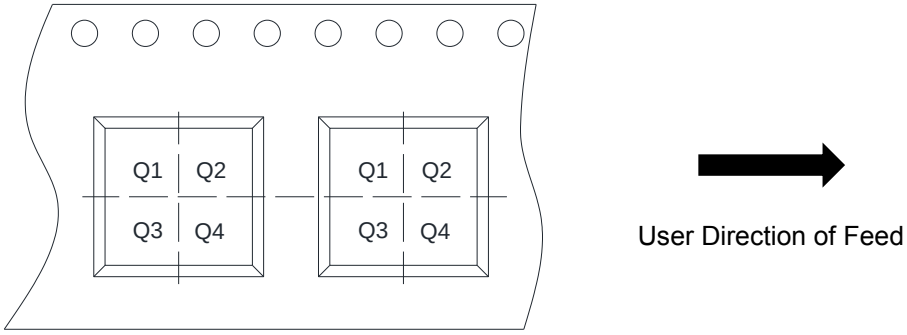
Reel Dimensions



Tape Dimensions



Quadrant Assignments For PIN1 Orientation In Tape



RD	Reel Dimension	☐ 7inch ☑ 13inch
W	Overall width of the carrier tape	☐ 8mm ☑ 12mm ☐ 16mm
P1	Pitch between successive cavity centers	☐ 2mm ☐ 4mm ☑ 8mm
Pin1	Pin1 Quadrant	☑ Q1 ☐ Q2 ☐ Q3 ☐ Q4

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