

WS72042

600nA Nano-Power Rail-to-Rail Input Output Operational Amplifiers

[Http://www.omnivision-group.com](http://www.omnivision-group.com)

Descriptions

The WS72042 is a dual low-voltage operational amplifier with rail-to-rail input/output swing. Ultra low power makes this amplifier ideal for battery-powered and portable applications. The WS72042 has a gain-bandwidth product of 29kHz (TYP) and is unity gain stable. These specifications make this operational amplifier appropriate for low frequency applications, such as battery current monitoring and sensor conditioning.

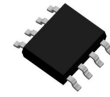
WS72042 is available with MSL 3 Level in SOP-8L, MSOP-8L and DFN2x2-8L package. Standard products are Pb-Free and halogen-Free.

Applications

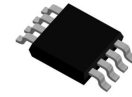
- Handsets and Mobile Accessories
- Current Sensing
- Wireless Remote Sensors, Active RFID Readers
- Environment / Gas / Oxygen Sensors
- Threshold Detectors / Discriminators
- Low Power Filters
- Battery or Solar Powered Devices
- Sensor Network Powered by Energy Scavenging

Features

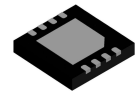
- Wide Supply Voltage : 1.6~5.5 V
- Quiescent Current per Amplifier : 610 nA Typical
- GBWP : 29 kHz
- Rail-to-Rail Input/Output Swing
- Unity Gain Stable
- -40°C to 85°C Operation Temperature Range
- Available in Green SOP-8L, MSOP-8L and DFN2x2-8L Packages



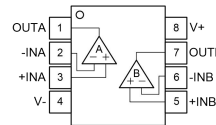
SOP-8L



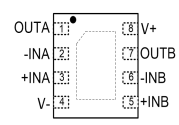
MSOP-8L



DFN2x2-8L



SOP-8L / MSOP-8L

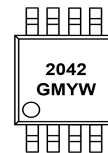


DFN2x2-8L

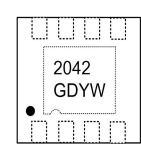
Pin configuration (Top view)



SOP-8L



MSOP-8L



DFN2x2-8L

Marking

- 2042 = Device code
- GS, GM, GD = Special code
- Y = Year code
- W = Week code

Order Information

Device	Package	Shipping
WS72042S-8/TR	SOP-8L	4000/Reel &Tape
WS72042M-8/TR	MSOP-8L	4000/Reel &Tape
WS72042D-8/TR	DFN2x2-8L	3000/Reel &Tape

Pin Descriptions

Pin Number	Symbol	Descriptions
1	OUTA	Output of Amplifier A
2	-INA	Inverting input of Amplifier A
3	+INA	Non-inverting input of Amplifier A
4	V-	Negative Supply
5	+INB	Non-inverting input of Amplifier B
6	-INB	Inverting input of Amplifier B
7	OUTB	Output of Amplifier B
8	V+	Positive Supply

Absolute Maximum Ratings⁽¹⁾

Parameter	Symbol	Value	Unit
Supply Voltage, ([V+] - [V-])	$V_S^{(2)}$	6	V
Input Common Mode Voltage Range	V_{ICR}	(V ⁻)-0.3 to (V ⁺)+0.3	V
Output Short-Circuit Duration	$t_{SO}^{(3)}$	Unlimited	/
Operating Free-Air Temperature Range	T_A	-40 to 125	°C
Storage Temperature Range	T_{STG}	-65 to 150	°C
Junction Temperature Range	T_J	150	°C
Lead Temperature Range	T_L	260	°C

Note:

1. Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are only stress ratings, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions are not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
2. All voltage values, except differential voltage are with respect to network terminal.
3. A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many amplifiers are shorted. Thermal resistance varies the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

ESD, Electrostatic Discharge Protection

Symbol	Parameter	Condition	Minimum level	Unit
HBM	Human Body Model ESD	MIL-STD-883H Method 3015.8 JEDEC-EIA/JESD22-A114A	±8000	V
CDM	Charged Device Model ESD	JEDEC-EIA/JESD22-C101E	±2000	V
MM	Machine Model ESD	JEDEC-EIA/JESD22-A115	±400	V

Electronics Characteristics

At $T_A = 25^\circ\text{C}$. $V_S = 5\text{V}$, $V_{CM} = V_{OUT} = V_S/2$, $R_{load} = 100\text{k}\Omega$, $C_{load} = 60\text{pF}$, V_{SHDN} is unconnected, unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Typ.	Max.	Unit
V_{OS}	Input Offset Voltage		$V_{CM} = V_S/2$ and $V_{CM} = \text{GND}$	-3.0	± 0.1	3.0	mV
α_{VOS}	Input Offset Voltage Drift				2.8		$\mu\text{V}/^\circ\text{C}$
I_{IB}	Input Bias Current				<10		pA
I_{OS}	Input Offset Current				<10		pA
V_n	Input Voltage Noise		$f = 0.1\text{Hz to } 10\text{Hz}$		5		μV_{P-P}
e_n	Input Voltage Noise Density		$f = 1\text{kHz}$		125		$\text{nV}/\sqrt{\text{Hz}}$
R_{IN}	Input Resistance				>1		$\text{T}\Omega$
CMRR	Common Mode Rejection Ratio		$V_{CM} = 0.1\text{V to } 4.9\text{V}$	55	75		dB
V_{CM}	Common Mode Input Voltage Range			$(V^-) - 0.3$		$(V^+) + 0.3$	V
PSRR	Power Supply Rejection Ratio			70	95		dB
A_{VOL}	Open Loop Large Signal Gain		$V_{OUT} = 2.5\text{V}$, $R_{load} = 100\text{k}\Omega$		120		dB
			$V_{OUT} = 0.1\text{V to } 4.9\text{V}$, $R_{load} = 100\text{k}\Omega$	75	120		dB
V_{OL}, V_{OH}	Output Swing from Supply Rail		$R_{load} = 100\text{k}\Omega$		3		mV
R_{OUT}	Closed-Loop Output Impedance		$G = 1, f = 1\text{kHz}, I_{OUT} = 0$		0.7		Ω
I_{SC}	Output Short-Circuit Current		Sink or Source Current	12	20		mA
V_{DD}	Supply Voltage			1.6		5.5	V
I_Q	Quiescent Current per Amplifier				660	900	nA
PM	Phase Margin		$R_{load} = 100\text{k}\Omega$, $C_{load} = 60\text{pF}$		80		degrees
GM	Gain Margin		$R_{load} = 100\text{k}\Omega$, $C_{load} = 60\text{pF}$		18		dB
GBWP	Gain-Bandwidth Product		$f = 1\text{kHz}$		29		kHz
t_s	Settling Time	1.5 to 3.5V, Unity Gain	0.1%		0.18		ms
		2.45 to 2.55V, Unity Gain	0.1%		0.02		
SR	Slew Rate		$A_V = 1$, $V_{OUT} = 1.5\text{V to } 3.5\text{V}$, $R_{load} = 100\text{k}\Omega$, $C_{load} = 60\text{pF}$		9		$\text{mV}/\mu\text{s}$
FPBW	Full Power Bandwidth ^{Note1}		$2V_{P-P}$		600		Hz

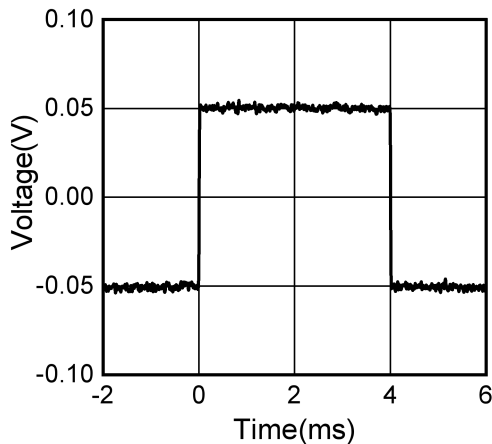
Note:

1. Full power bandwidth is calculated from the slew rate $\text{FPBW} = \text{SR}/(\pi \cdot V_{P-P})$.

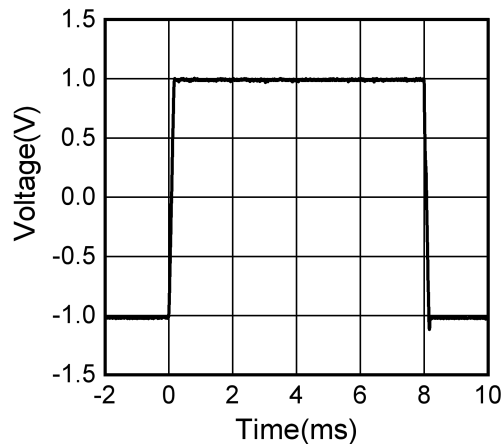
Typical Characteristics

$T_A=25^{\circ}\text{C}$, $V_S=5\text{V}$, $V_{\text{CM}}=V_S/2$, unless otherwise noted

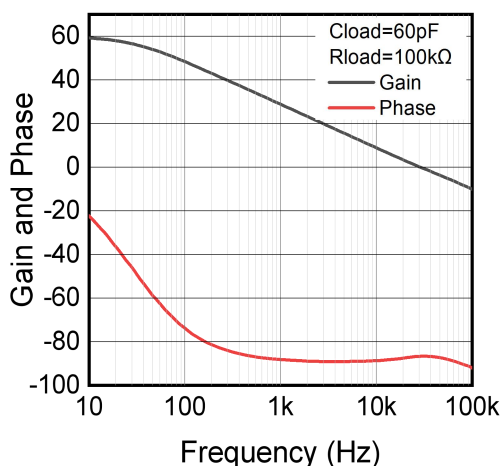
Small-Signal Step Response, 100mV Step



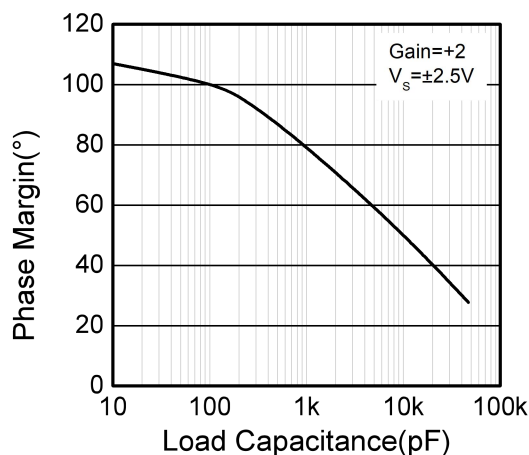
Large-Signal Step Response, 2V Step



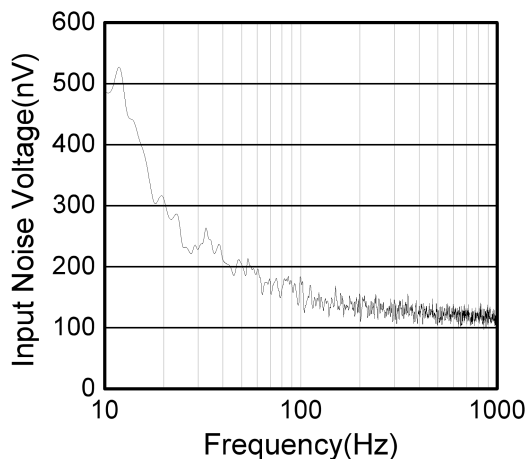
Open-Loop Gain and Phase



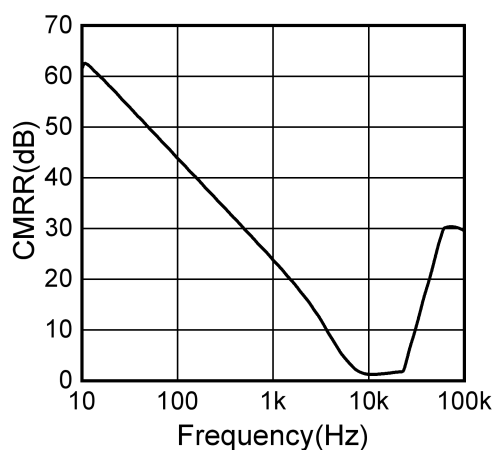
Phase Margin vs. C_{load} (Stable for Any C_{load})



Input Voltage Noise Spectral Density



CMRR vs. Frequency

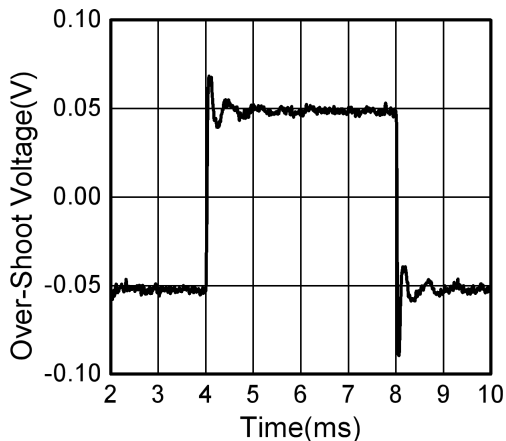


Typical Characteristics (continued)

$T_A=25^{\circ}\text{C}$, $V_S=5\text{V}$, $V_{\text{CM}}=V_S/2$, unless otherwise noted

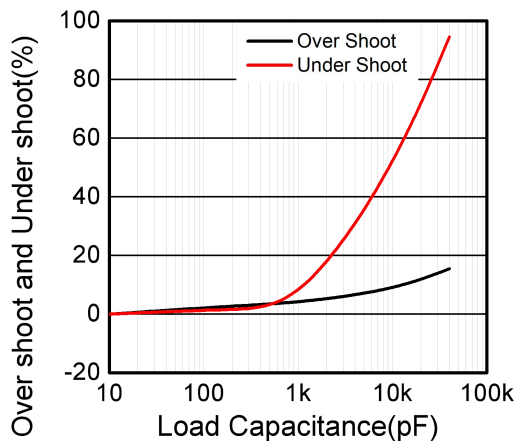
Over-Shoot Voltage

Gain=-1, $C_{\text{LOAD}} = 40\text{nF}$, $V_S=\pm 2.5\text{V}$



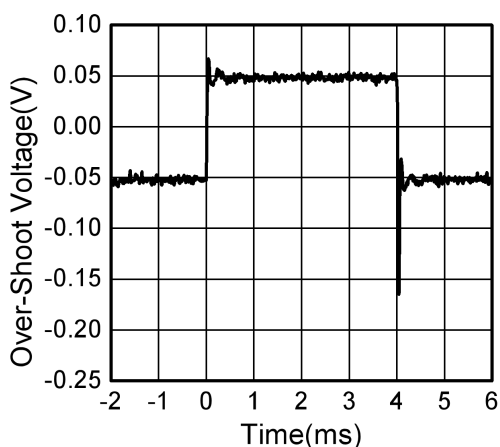
Over-Shoot % vs. C_{load}

Gain=-1, $C_{\text{LOAD}} = 40\text{nF}$, $V_S=\pm 2.5\text{V}$



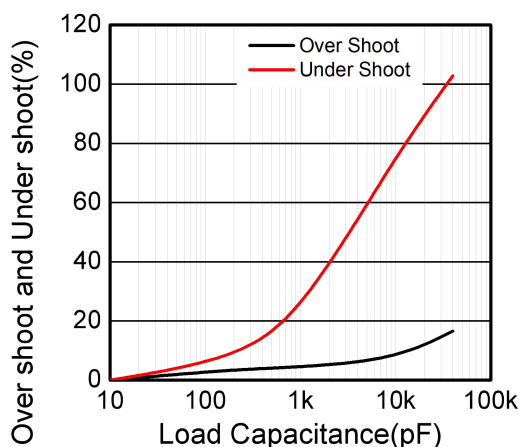
Over-Shoot Voltage

Gain=+1, $C_{\text{LOAD}} = 40\text{nF}$, $V_S=\pm 2.5\text{V}$

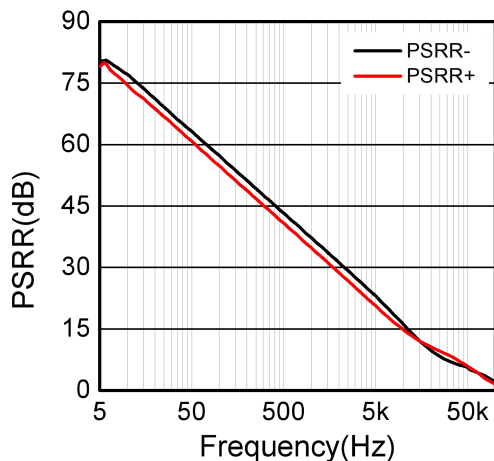


Over-Shoot % vs. C_{load}

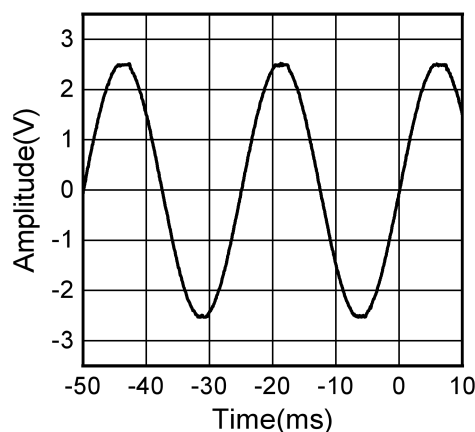
Gain =+1, $C_{\text{LOAD}} = 40\text{nF}$, $V_S=\pm 2.5\text{V}$



Power-Supply Rejection Ratio



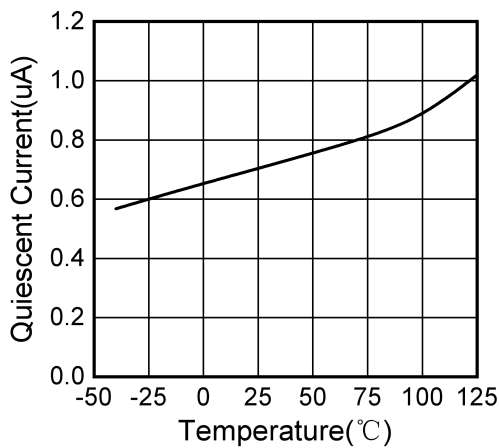
$V_{\text{IN}} = -0.2\text{V}$ to 5.7V , No Phase Reversal



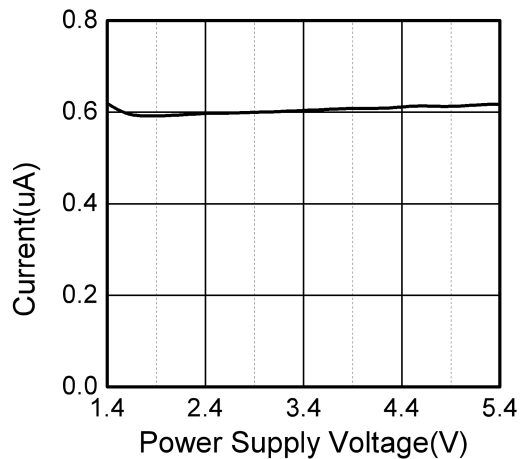
Typical Characteristics (continued)

$T_A=25^{\circ}\text{C}$, $V_S=5\text{V}$, $V_{\text{CM}}=V_S/2$, unless otherwise noted

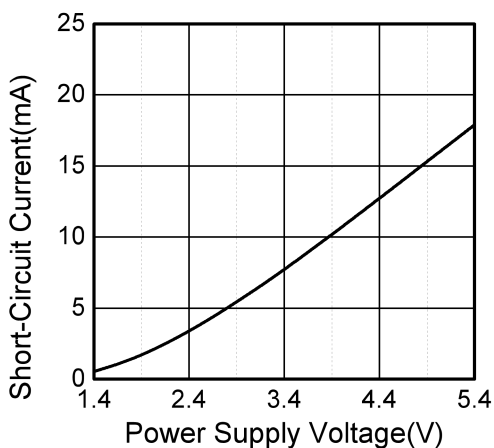
Quiescent Supply Current vs. Temperature



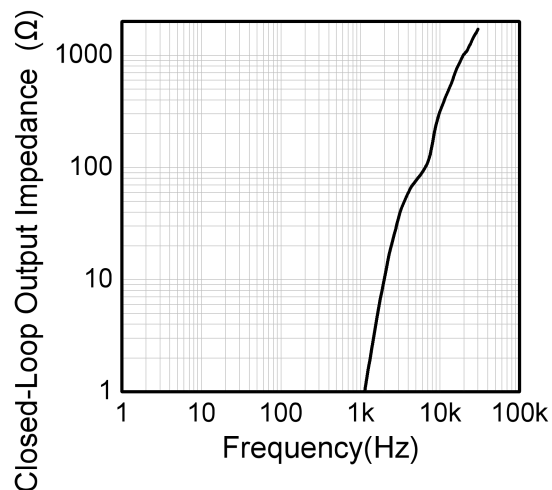
Quiescent Supply Current vs. Supply Voltage



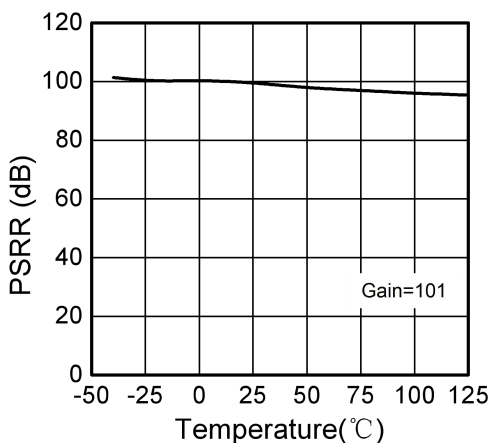
Short-Circuit Current vs. Supply Voltage



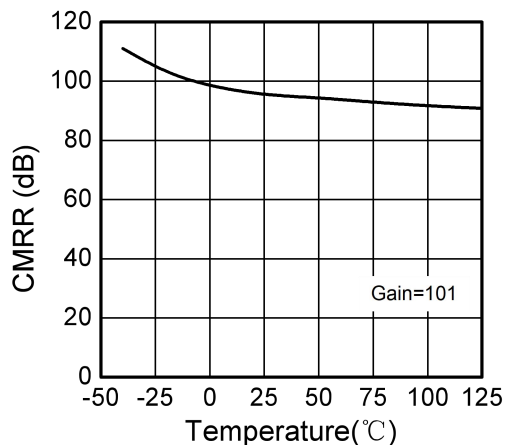
Closed-Loop Output Impedance vs. Frequency



PSRR vs. Frequency



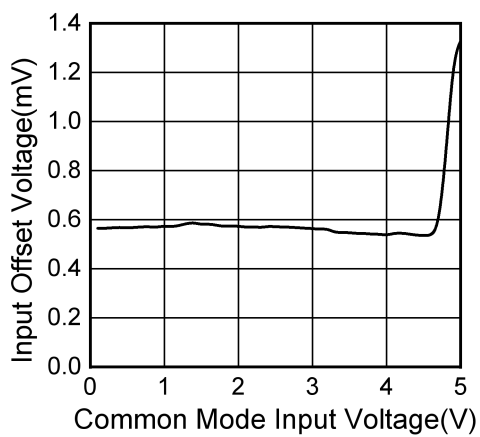
CMRR vs. Temperature



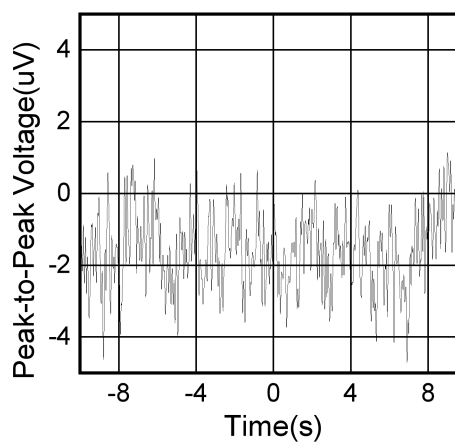
Typical Characteristics (continued)

$T_A=25^{\circ}\text{C}$, $V_S=5\text{V}$, $V_{\text{CM}}=V_S/2$, unless otherwise noted

Input Offset Voltage vs. Common Mode
Input Voltage

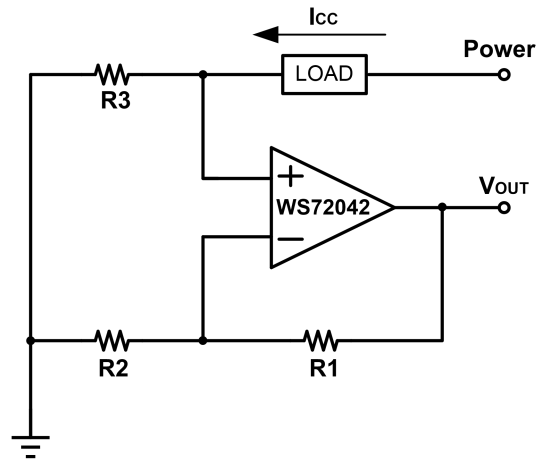


0.1Hz to 10Hz Time Domain Output
Voltage Noise



Application Circuit

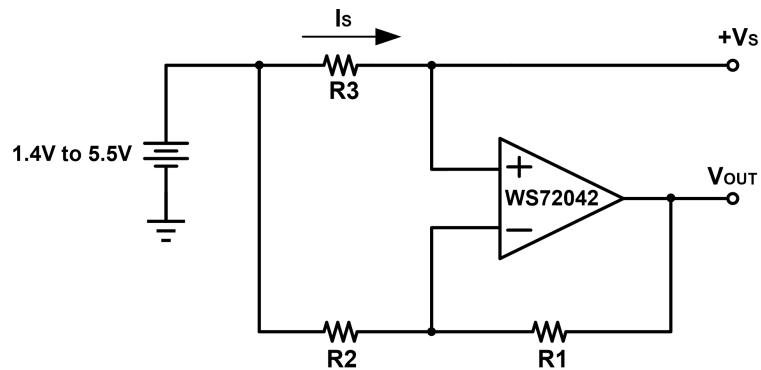
(1) WS72042 in Low Side Battery Current Sensor



Application Circuit for Low Side Battery Current Sensor

$$V_{OUT} = I_{CC} \times R_3 \times \left(\frac{R_1}{R_2} + 1 \right)$$

(2) WS72042 in High Side Battery Current Sensor

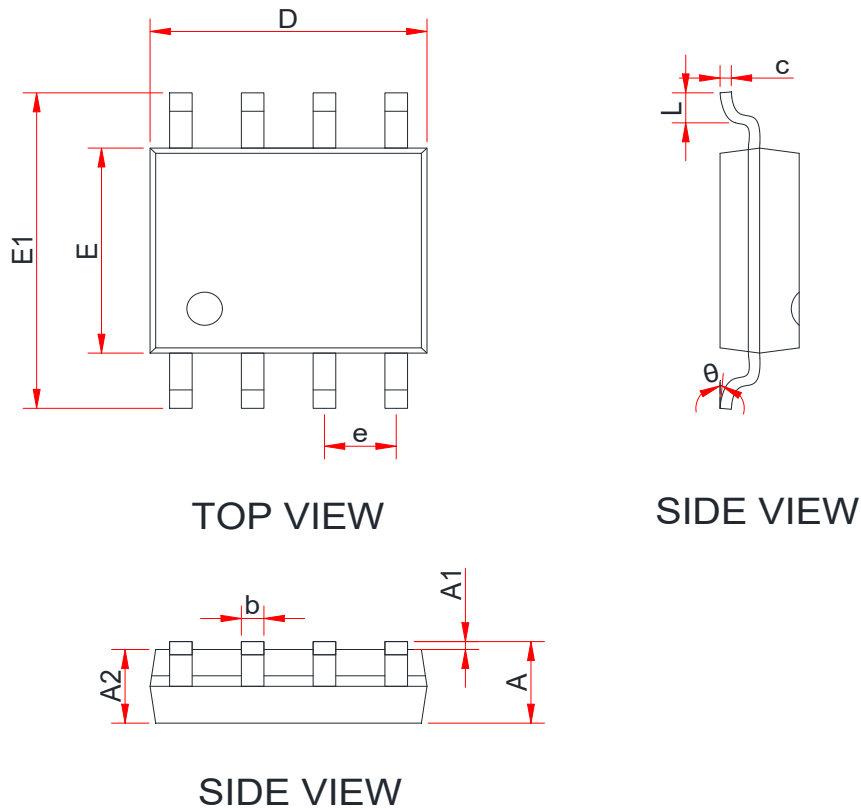


Application Circuit for High Side Battery Current Sensor

$$I_S = \frac{+V_S - V_{OUT}}{R_1 \times R_3 \div R_2}$$

PACKAGE OUTLINE DIMENSIONS

SOP-8L

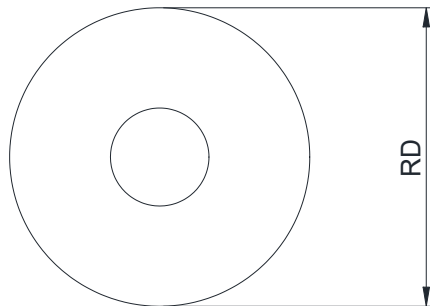


Symbol	Dimensions In Millimeters (mm)		
	Min.	Typ.	Max.
A	1.35	1.55	1.75
A1	0.05	0.15	0.25
A2	1.25	1.40	1.65
b	0.33	-	0.51
c	0.15	-	0.26
D	4.70	4.90	5.10
E	3.70	3.90	4.10
E1	5.80	6.00	6.20
e	1.27BSC		
L	0.40	-	1.27
θ	0°	-	8°

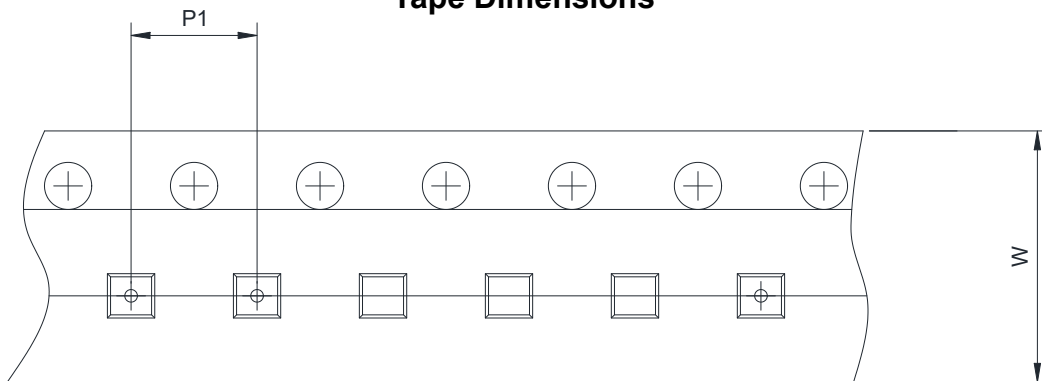
TAPE AND REEL INFORMATION

SOP-8L

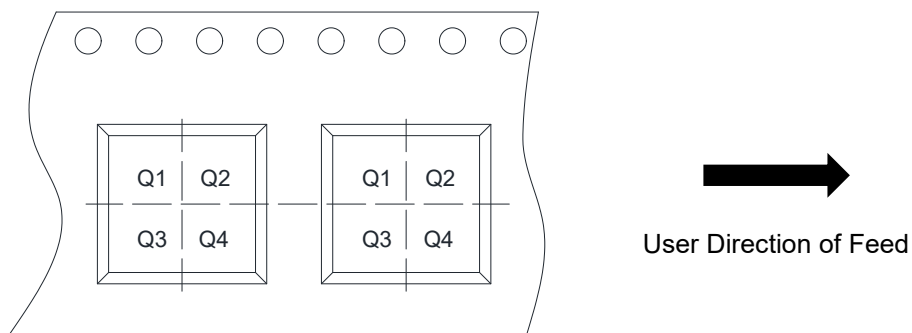
Reel Dimensions



Tape Dimensions



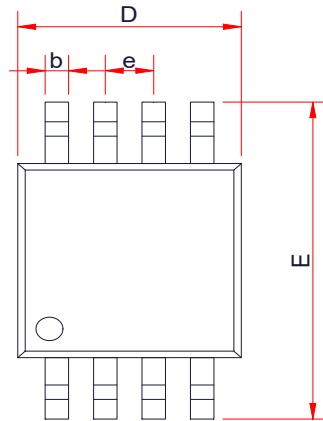
Quadrant Assignments For PIN1 Orientation In Tape



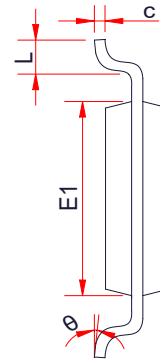
RD	Reel Dimension	<input type="checkbox"/> 7inch	<input checked="" type="checkbox"/> 13inch		
W	Overall width of the carrier tape	<input type="checkbox"/> 8mm	<input checked="" type="checkbox"/> 12mm		
P1	Pitch between successive cavity centers	<input type="checkbox"/> 2mm	<input type="checkbox"/> 4mm	<input checked="" type="checkbox"/> 8mm	
Pin1	Pin1 Quadrant	<input checked="" type="checkbox"/> Q1	<input type="checkbox"/> Q2	<input type="checkbox"/> Q3	<input type="checkbox"/> Q4

PACKAGE OUTLINE DIMENSIONS

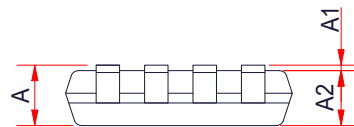
MSOP-8L



TOP VIEW



SIDE VIEW



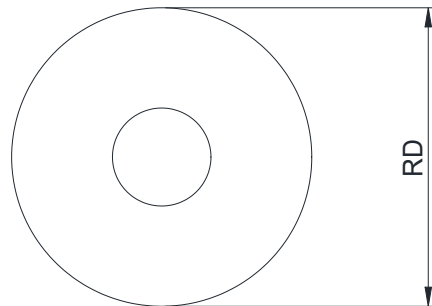
SIDE VIEW

Symbol	Dimensions In Millimeters (mm)		
	Min.	Typ.	Max.
A	-	-	1.10
A1	0.02	-	0.15
A2	0.75	0.80	0.95
b	0.25	-	0.38
c	0.09	-	0.23
D	2.90	3.00	3.10
E	4.75	4.90	5.05
E1	2.90	3.00	3.10
e	0.65 BSC		
L	0.40	-	0.80
θ	0°	-	6°

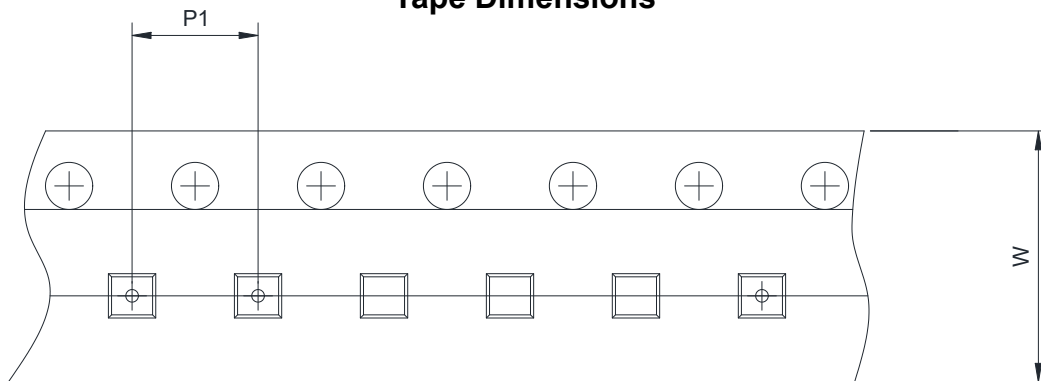
TAPE AND REEL INFORMATION

MSOP-8L

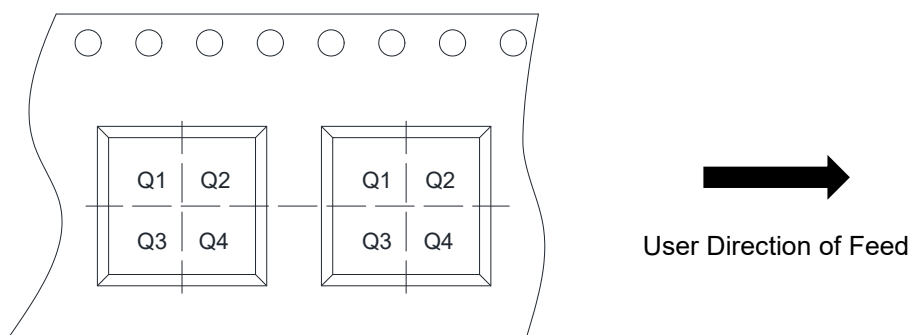
Reel Dimensions



Tape Dimensions



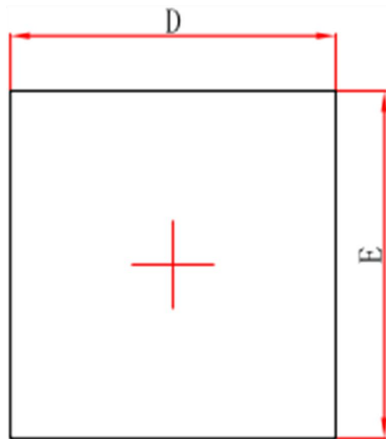
Quadrant Assignments For PIN1 Orientation In Tape



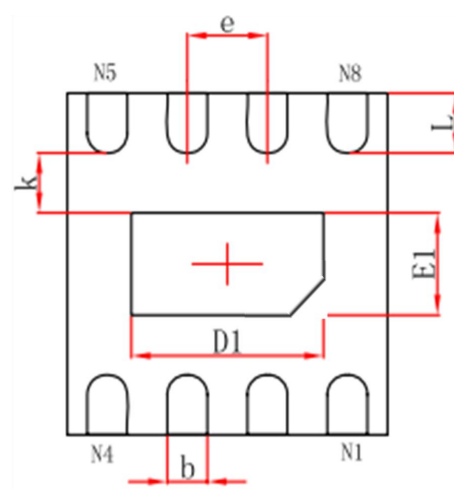
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Pin1	Pin1 Quadrant	<input checked="" type="checkbox"/> Q1	<input type="checkbox"/> Q2	<input type="checkbox"/> Q3	<input type="checkbox"/> Q4

PACKAGE OUTLINE DIMENSIONS

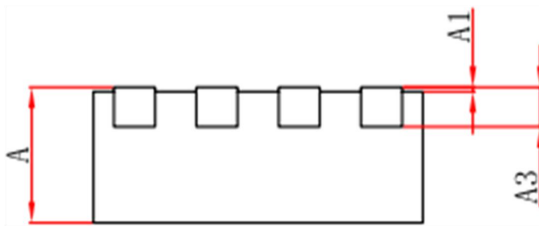
DFN2x2-8L



TOP VIEW



BOTTOM VIEW



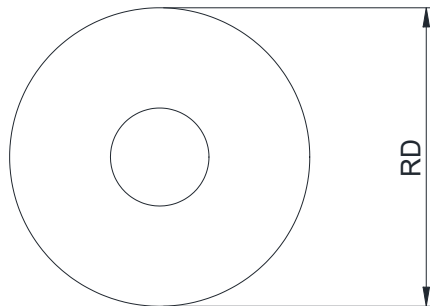
SIDE VIEW

Symbol	Dimensions In Millimeters (mm)		
	Min.	Typ.	Max.
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A3	0.20 REF		
D	2.00 BSC		
E	2.00 BSC		
D1	1.25	1.30	1.35
E1	0.75	0.80	0.85
k	0.20		
b	0.19	0.24	0.29
e	0.50 BSC		
L	0.30	0.35	0.40

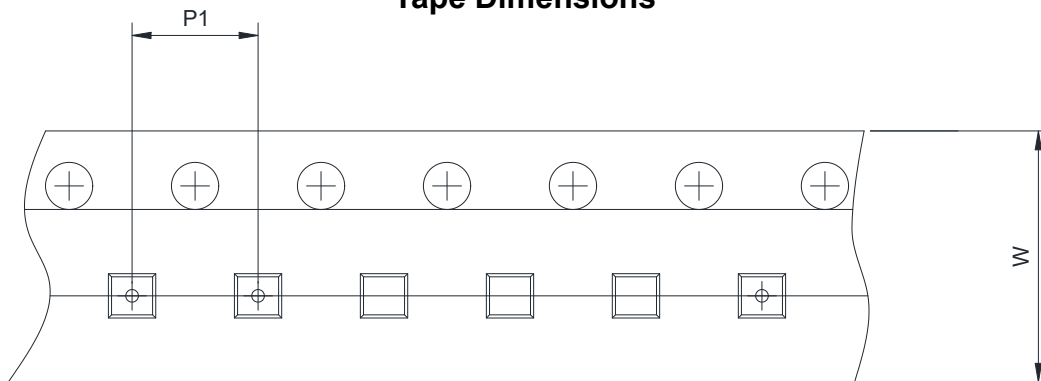
TAPE AND REEL INFORMATION

DFN2x2-8L

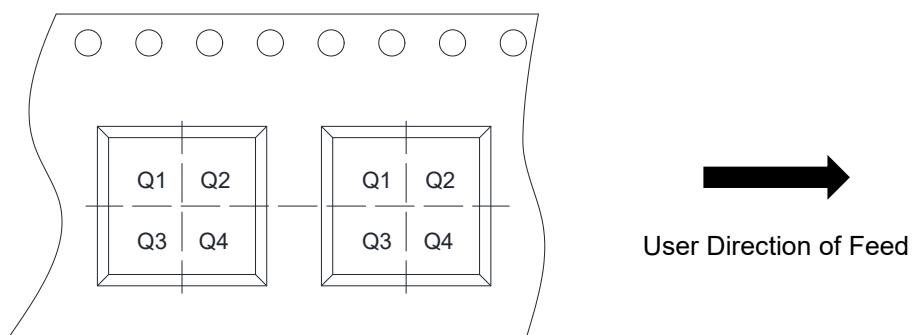
Reel Dimensions



Tape Dimensions



Quadrant Assignments For PIN1 Orientation In Tape



RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch	<input type="checkbox"/> 13inch		
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm	<input type="checkbox"/> 12mm		
P1	Pitch between successive cavity centers	<input type="checkbox"/> 2mm	<input checked="" type="checkbox"/> 4mm	<input type="checkbox"/> 8mm	
Pin1	Pin1 Quadrant	<input checked="" type="checkbox"/> Q1	<input type="checkbox"/> Q2	<input type="checkbox"/> Q3	<input type="checkbox"/> Q4

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[430227FB](#) [LT1678IS8](#) [NCV33202DMR2G](#) [NJM324E](#) [M38510/13101BPA](#) [NTE925](#) [AZV358MTR-G1](#) [AP4310AUMTR-AG1](#)
[AZV358MMTR-G1](#) [SCY33178DR2G](#) [NCV20034DR2G](#) [NTE778S](#) [NTE871](#) [NTE937](#) [NJU7057RB1-TE2](#) [SCY6358ADR2G](#)
[NJM2904CRB1-TE1](#) [UPC4570G2-E1-A](#) [UPC4741G2-E1-A](#) [UPC4574GR-9LG-E1-A](#) [NJM8532RB1-TE1](#) [EL2250CS](#) [EL5100IS](#) [EL5104IS](#)
[EL5127CY](#) [EL5127CYZ](#) [EL5133IW](#) [EL5152IS](#) [EL5156IS](#) [EL5162IS](#) [EL5202IY](#) [EL5203IY](#) [EL5204IY](#) [EL5210CS](#) [EL5210CYZ](#)
[EL5211IYE](#) [EL5220CY](#) [EL5223CLZ](#) [EL5223CR](#) [EL5224ILZ](#) [EL5227CLZ](#) [EL5227CRZ](#) [EL5244CS](#) [EL5246CS](#) [EL5246CSZ](#) [EL5250IY](#)
[EL5251IS](#) [EL5257IS](#) [EL5260IY](#) [EL5261IS](#)