

General Description

The WSD20L50DN is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent R_{DS(on)} and gate charge for most of the synchronous buck converter applications.

The WSD20L50DN meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		10s	Steady State	
V _{DS}	Drain-Source Voltage	-20		V
V _{GS}	Gate-Source Voltage	±12		V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-50		A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ -10V ¹	-22		A
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-13.5	-10	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -10V ¹	-10.5	-8.0	A
I _{DM}	Pulsed Drain Current ²	-70		A
EAS	Single Pulse Avalanche Energy ³	36		mJ
I _{AS}	Avalanche Current	-12		A
P _D @T _C =25°C	Total Power Dissipation ⁴	31.25		W
P _D @T _A =25°C	Total Power Dissipation ⁴	3.1	2.0	W
T _{STG}	Storage Temperature Range	-55 to 150		°C
T _J	Operating Junction Temperature Range	-55 to 150		°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	80	°C/W
R _{θJA}	Thermal Resistance Junction-Ambient ¹ (t ≤ 10s)	---	40	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	4.0	°C/W

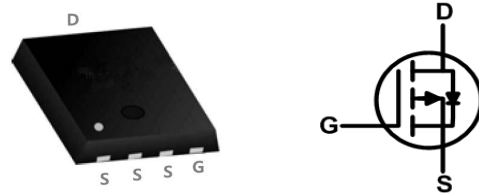
Product Summary

BVDSS	R _{DS(on)}	I _D
-20V	9.0mΩ	-50A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN3.3x3.3A-8_EP Pin Configuration



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.0232	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V, I _D =-10A	---	9	11	mΩ
		V _{GS} =-2.5V, I _D =-8A	---	11	15	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.5	---	-1.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	4.6	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-16V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-16V, V _{GS} =0V, T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-5V, I _D =-10A	---	13	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	9	---	Ω
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-10V, V _{GS} =-4.5V, I _D =-11A	---	25	---	nC
Q _{gs}	Gate-Source Charge		---	1.6	---	
Q _{gd}	Gate-Drain Charge		---	11	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-10V, V _{GS} =-4.5V, R _G =6Ω, I _D =-1A, R _L =15Ω	---	9	---	ns
T _r	Rise Time		---	13	---	
T _{d(off)}	Turn-Off Delay Time		---	26	---	
T _f	Fall Time		---	167	---	
C _{iss}	Input Capacitance	V _{DS} =-10V, V _{GS} =0V, f=1MHz	---	1620	---	pF
C _{oss}	Output Capacitance		---	320	---	
C _{rss}	Reverse Transfer Capacitance		---	290	---	

Diode Characteristics

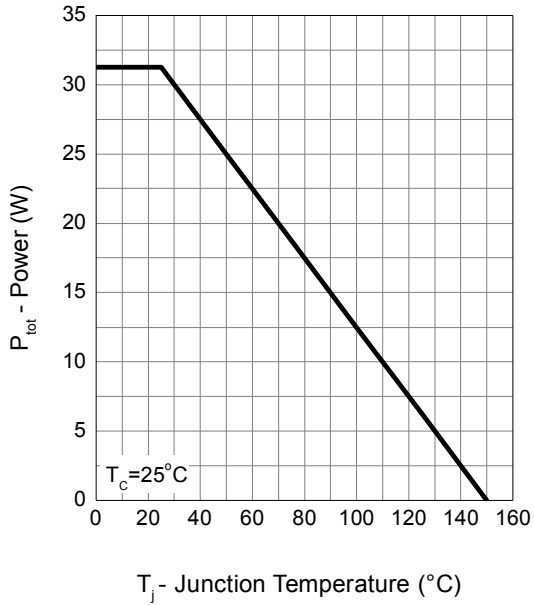
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	-10	A
I _{SM}	Pulsed Source Current ^{2,6}		---	---	-40	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1	V
t _{rr}	Reverse Recovery Time	I _F =-20A, dI/dt=100A/μs, T _J =25°C	---	63	---	nS
Q _{rr}	Reverse Recovery Charge		---	54	---	nC

Note :

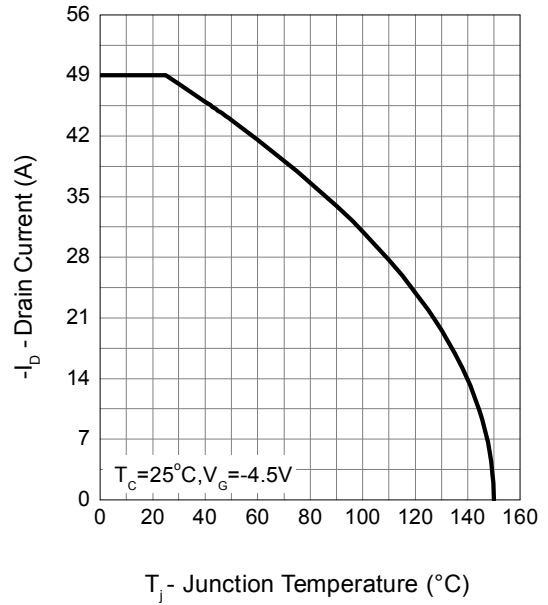
- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t_≤10sec.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V_{DD}=-25V, V_{GS}=-10V, L=0.5mH, I_{AS}=-18A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

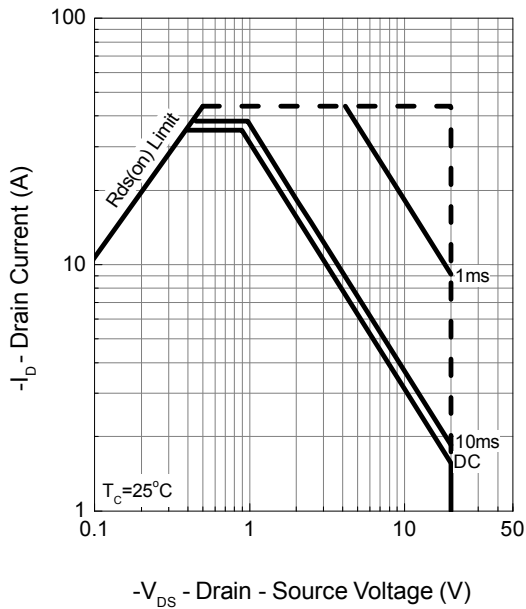
Power Dissipation



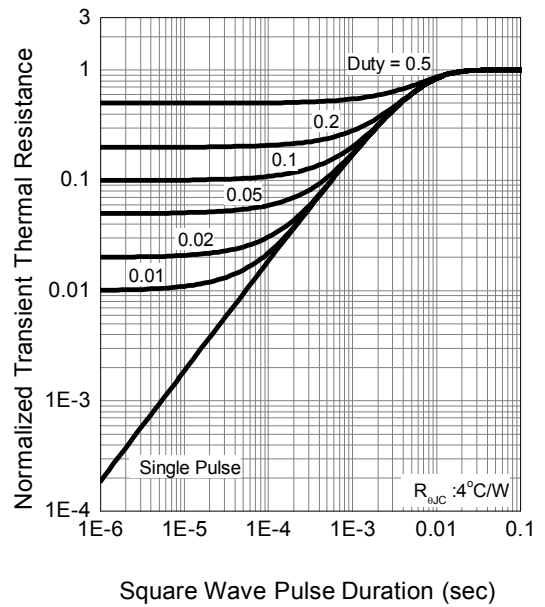
Drain Current



Safe Operation Area

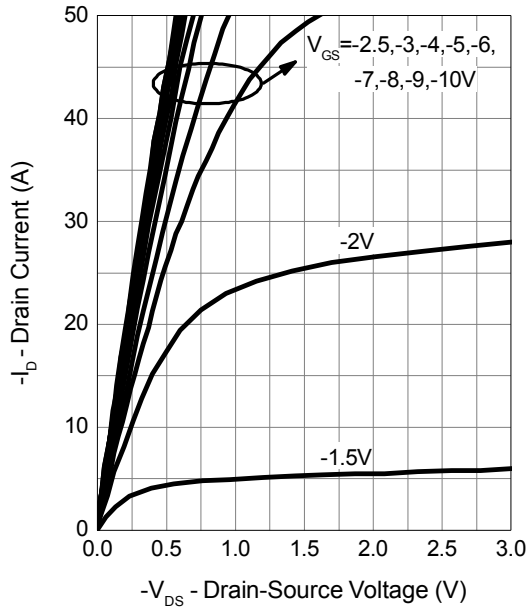


Thermal Transient Impedance

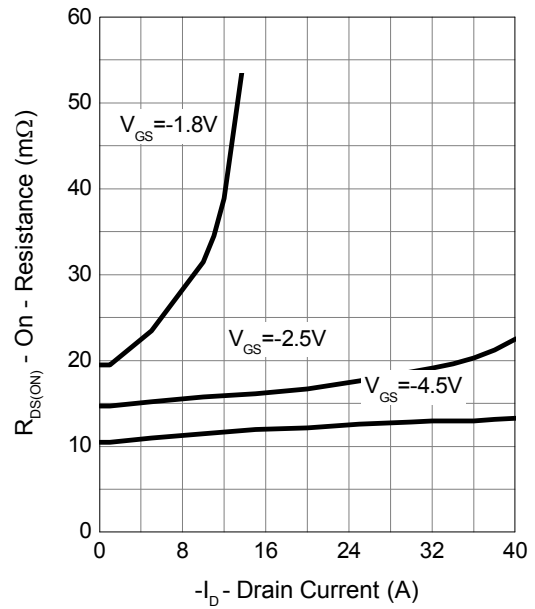


Typical Characteristics

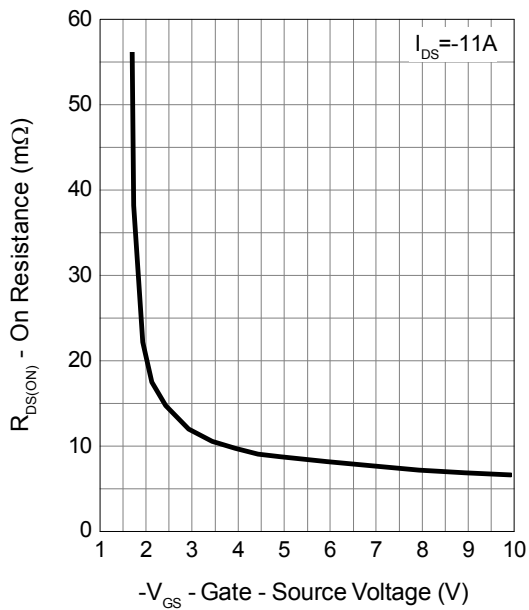
Output Characteristics



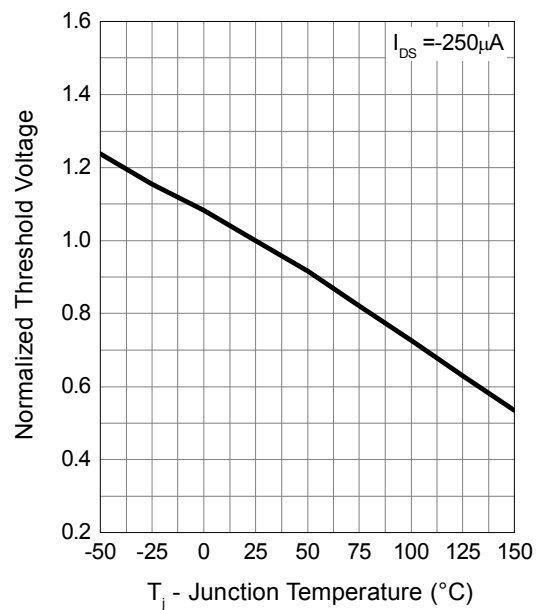
Drain-Source On Resistance



Gate-Source On Resistance

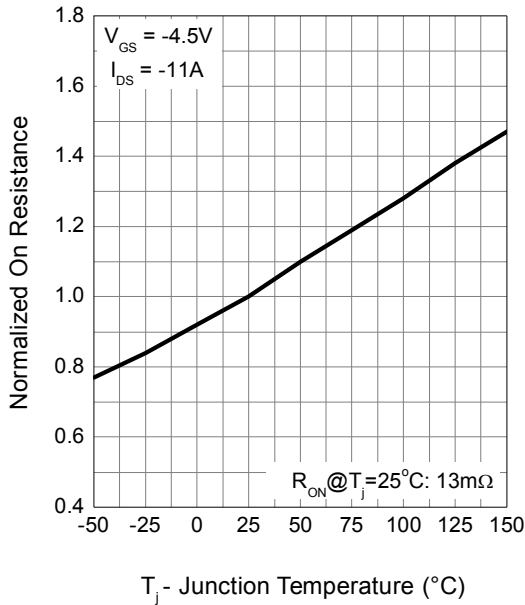


Gate Threshold Voltage

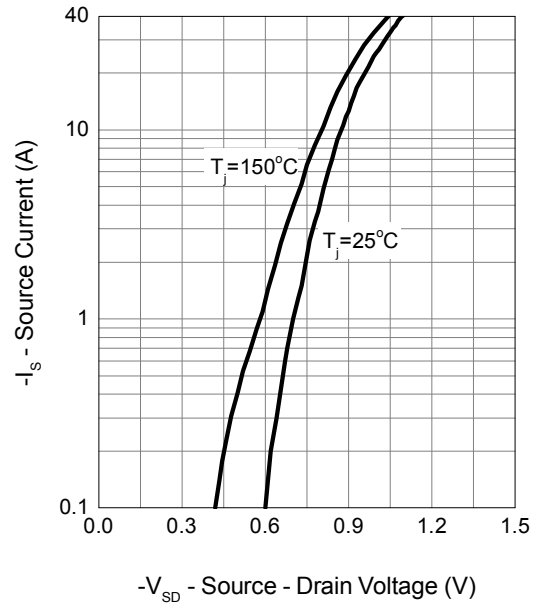


Typical Characteristics

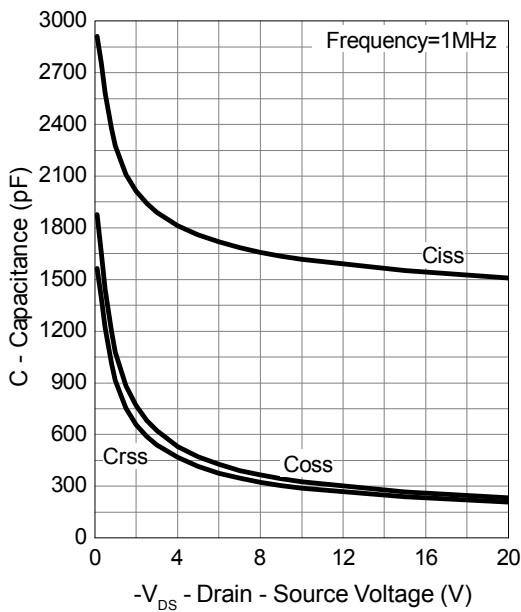
Drain-Source On Resistance



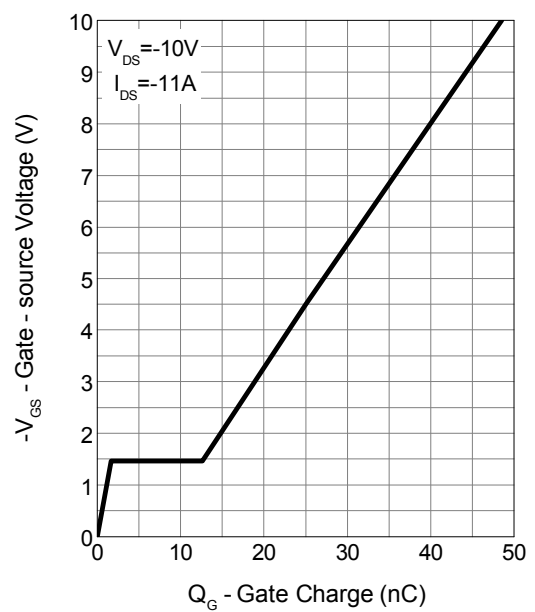
Source-Drain Diode Forward



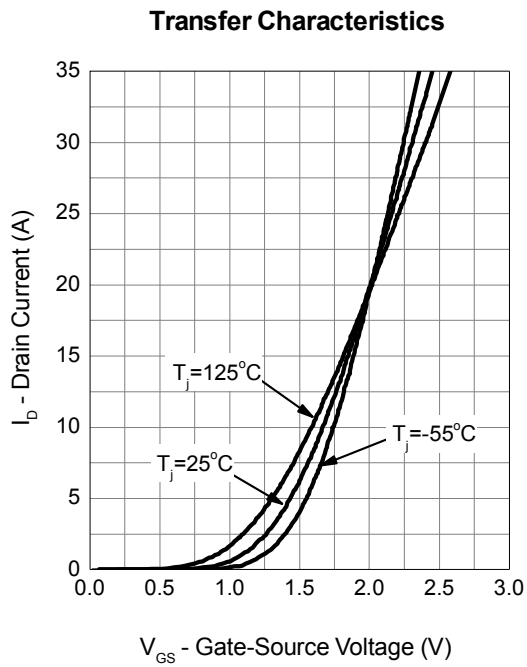
Capacitance



Gate Charge



Typical Characteristics





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