

General Description

The WSD25280DN56G is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent R_{DS(on)} and gate charge for most of the synchronous buck converter applications.

The WSD25280DN56G meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

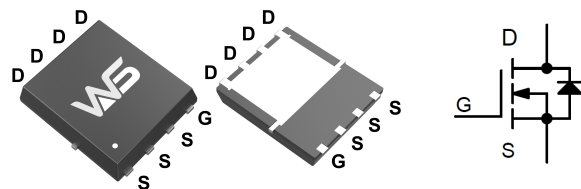
Product Summary

BVDSS	R _{DS(on)}	I _D
25V	0.7mΩ	280A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application

DFN5X6-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	25	V
V _{GS}	Gate-Source Voltage	±20	V
I _{D@T_C=25°C}	Continuous Drain Current (Silicon Limited) ^{1,7}	280	A
I _{D@T_C=70°C}	Continuous Drain Current(Silicon Limited) ^{1,7}	190	A
I _{DM}	Pulsed Drain Current ²	600	A
EAS	Single Pulse Avalanche Energy ³	1200	mJ
I _{AS}	Avalanche Current	100	A
P _{D@T_C=25°C}	Total Power Dissipation ⁴	83	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	23	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	1.5	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	25	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.022	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	0.7	0.9	mΩ
		V _{GS} =4.5V, I _D =20A	---	1.4	1.9	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	---	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-6.1	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =20V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =20V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =10A	---	40	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	3.8	1.5	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V, V _{GS} =4.5V, I _D =20A	---	72	---	nC
Q _{gs}	Gate-Source Charge		---	18	---	
Q _{gd}	Gate-Drain Charge		---	24	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GEN} =10V, R _G =1Ω, I _D =10A	---	33	---	ns
T _r	Rise Time		---	55	---	
T _{d(off)}	Turn-Off Delay Time		---	62	---	
T _f	Fall Time		---	22	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	7752	---	pF
C _{oss}	Output Capacitance		---	1120	---	
C _{rss}	Reverse Transfer Capacitance		---	650	---	

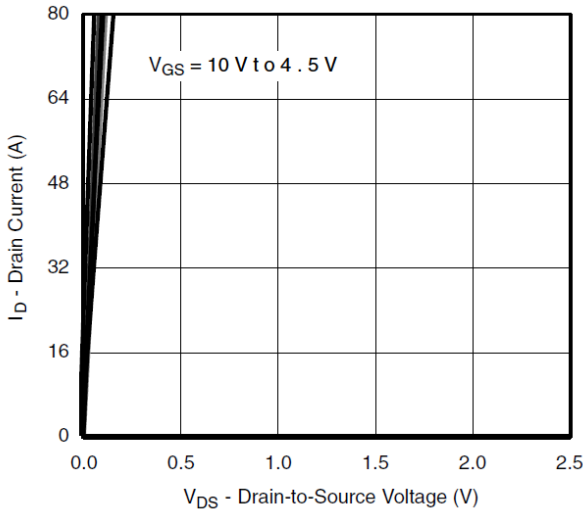
Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	69	A
I _{SM}	Pulsed Source Current ^{2,6}		---	---	180	A
V _{SD}	Diode Forward Voltage ²		---	---	1.2	V

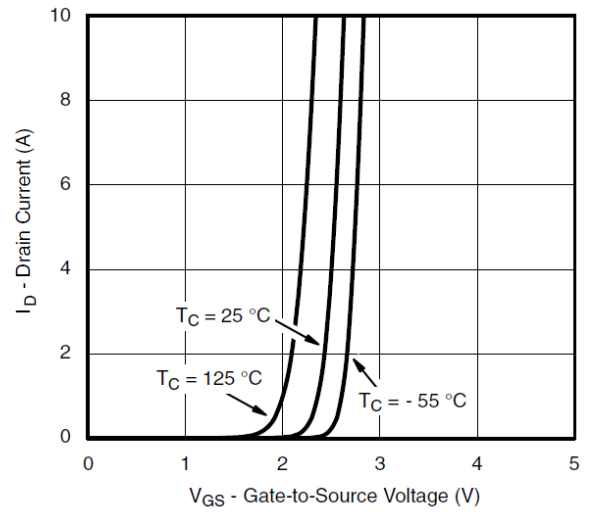
Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=30A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.
- 7.Package limitation current is 100A.

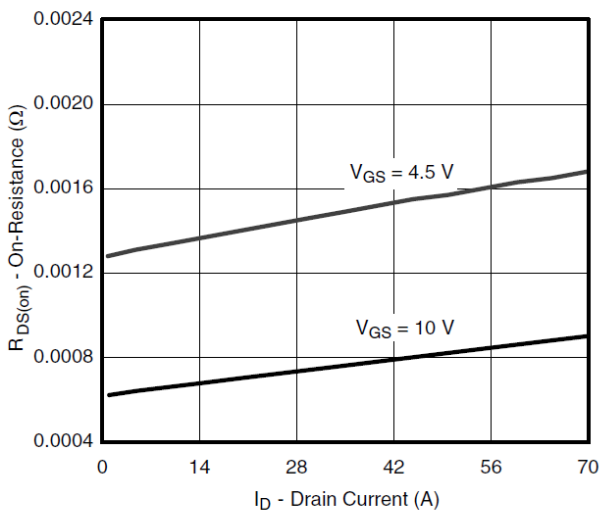
Typical Characteristics



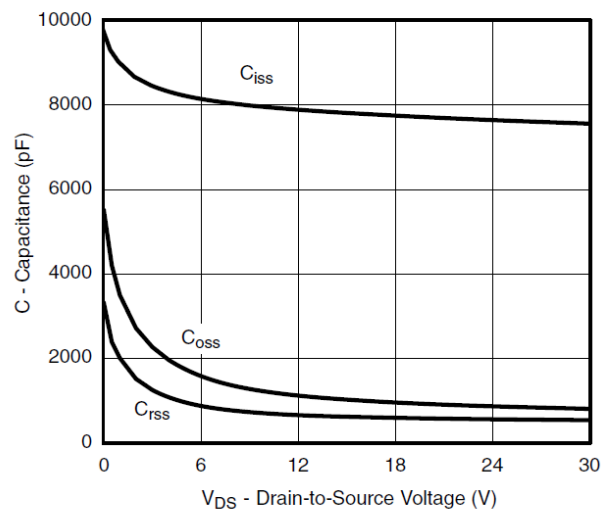
Output Characteristics



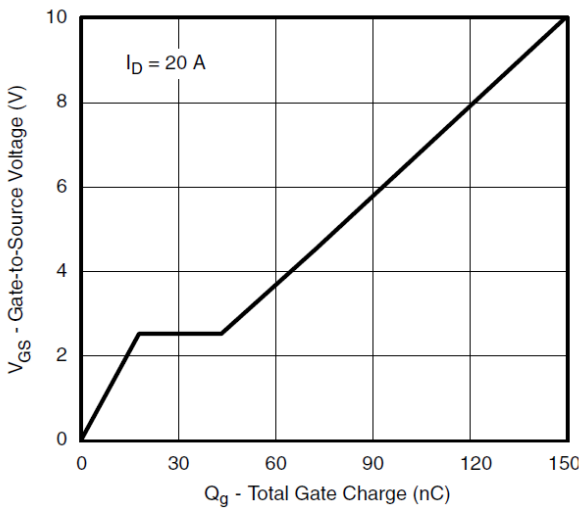
Transfer Characteristics



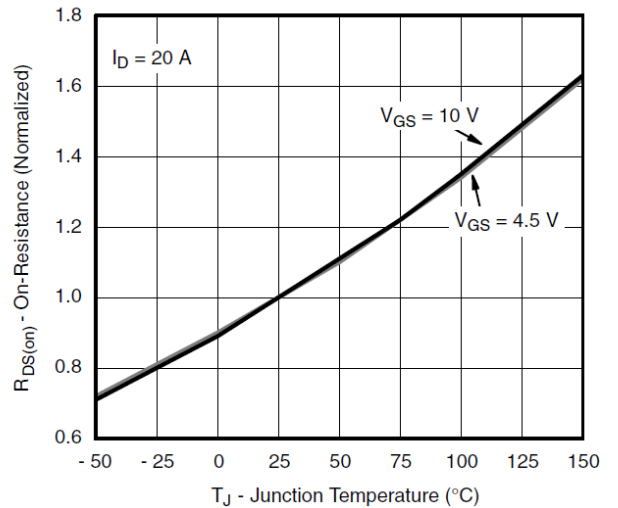
On-Resistance vs. Drain Current



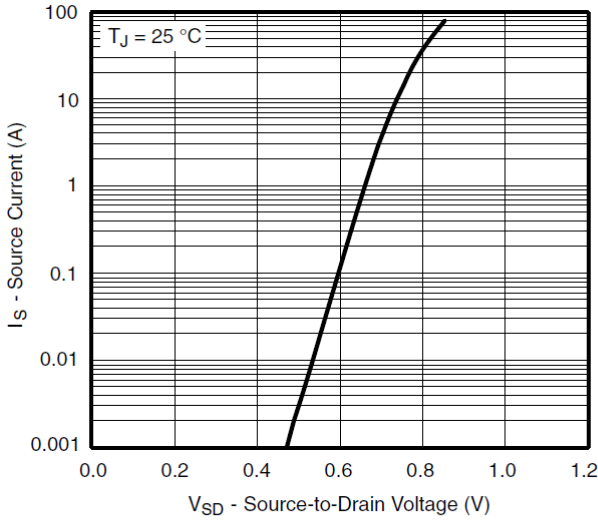
Capacitance



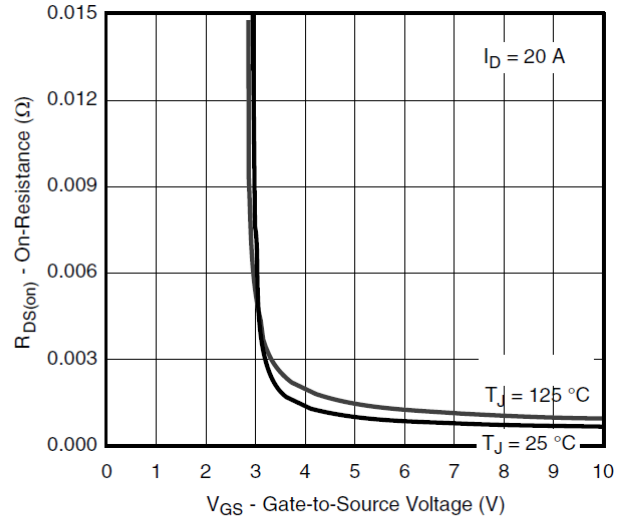
Gate Charge



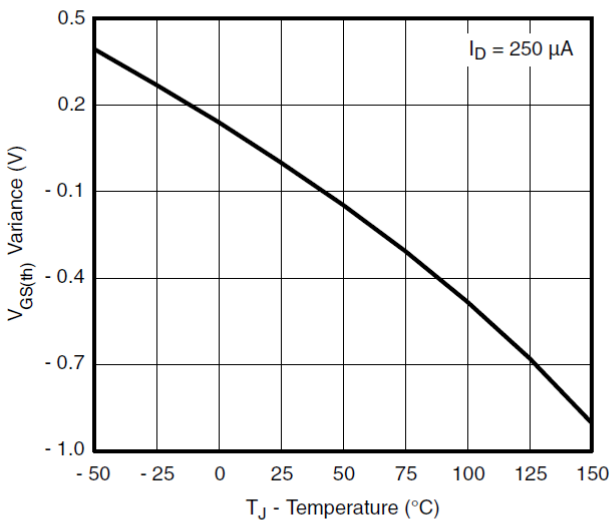
On-Resistance vs. Junction Temperature



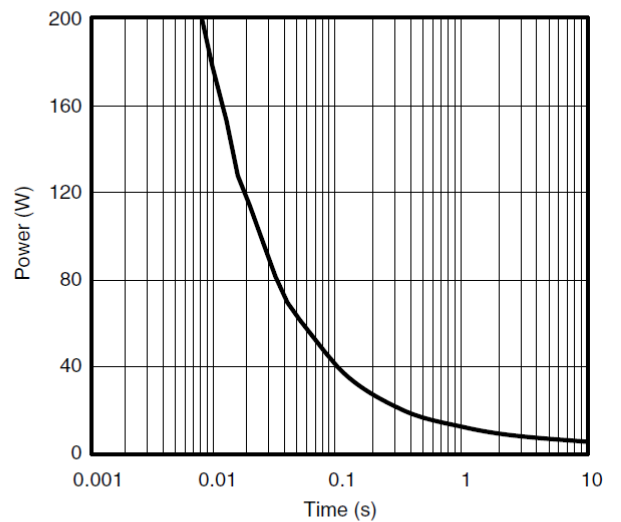
Source-Drain Diode Forward Voltage



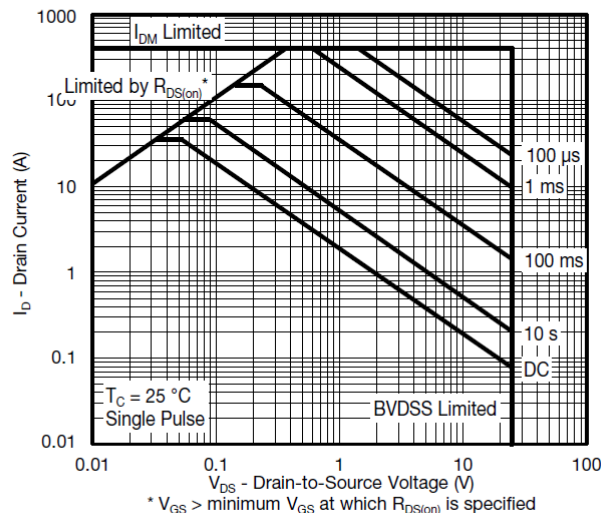
On-Resistance vs. Gate-to-Source Voltage



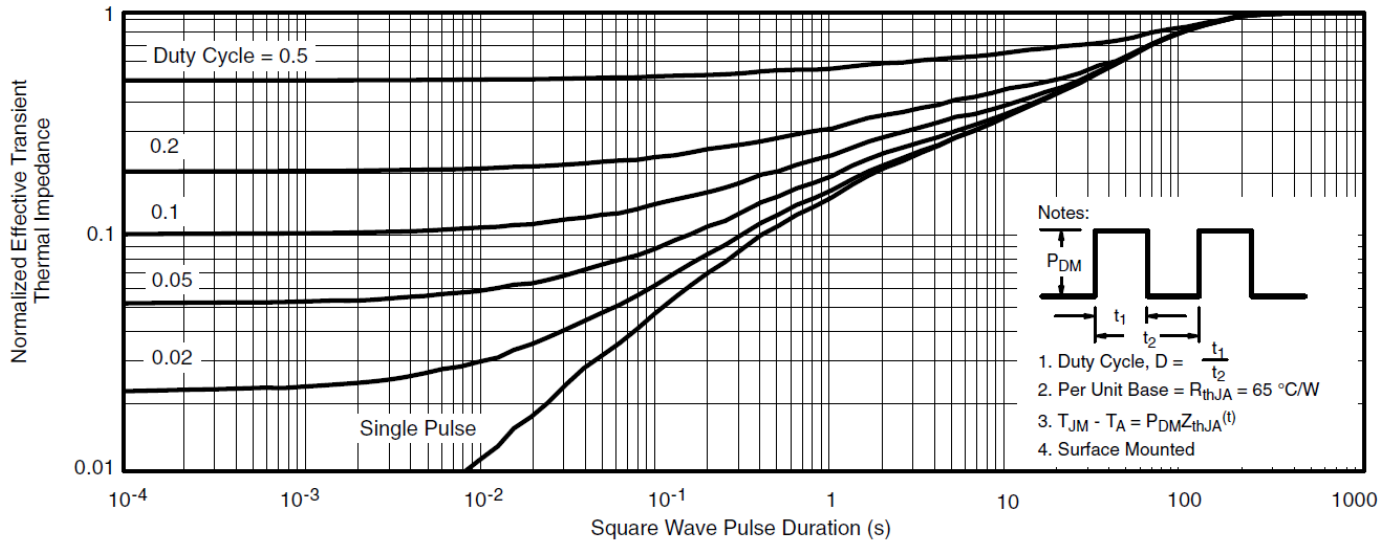
Threshold Voltage



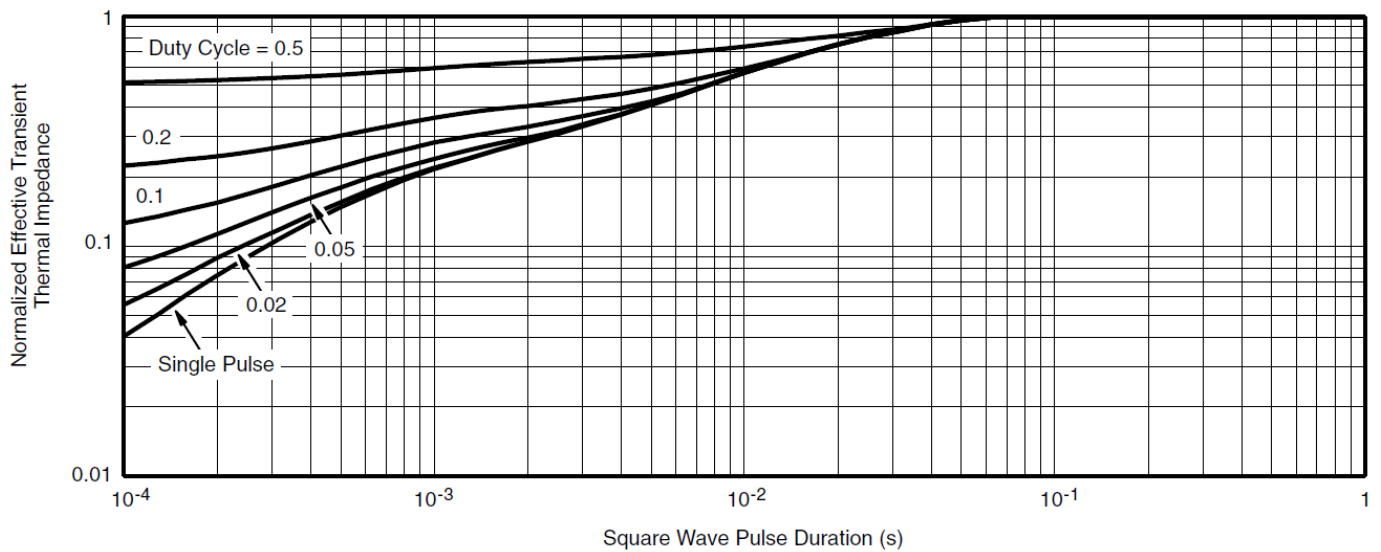
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



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