## General Description

The WSD3072DN33 uses advanced trench technology to provide excellent $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

The WSD3072DN33 meet the RoHS and Green Product requirement, 100% $E_{AS}$ guaranteed with full function reliability approved.

## Product Summery

| $BV_{DSS}$ | $R_{DS(ON)}$ | $I_D$ |
|---|---|---|
| 30V | 3.5mΩ | 72A |

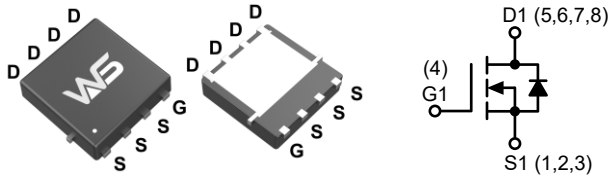
## Applications

- Battery protection
- Load switch
- Uninterruptible power supply

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% $E_{AS}$ Guaranteed
- Green Device Available

## DFN3X3-8L Pin Configuration




## Absolute Maximum Ratings ($T_C$=25°C, Unless Otherwise Noted)

| Symbol | Parameter | Rating | Units |
|---|---|---|---|
| $V_{DS}$ | Drain-Source Voltage | 30 | V |
| $V_{GS}$ | Gate-Source Voltage | ±20 | |
| $I_D$@$T_C$=25°C | Continuous Drain Current, $V_{GS}$ @ 10V [1] | 72 | A |
| $I_D$@$T_C$=100°C | Continuous Drain Current, $V_{GS}$ @ 10V [1] | 48 | |
| $I_{DM}$ | Pulsed Drain Current [2] | 160 | |
| $E_{AS}$ | Single Pulse Avalanche Energy [3] | 315 | mJ |
| $I_{AS}$ | Avalanche Current | 38 | A |
| $P_D$@$T_C$=25°C | Total Power Dissipation [4] | 59 | W |
| $T_{STG}$ | Storage Temperature Range | -55 to 150 | °C |
| $T_J$ | Operating Junction Temperature Range | -55 to 150 | |

## Thermal Data

| Symbol | Parameter | Typ. | Max. | Units |
|---|---|---|---|---|
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient [1] | --- | 62 | °C/W |
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case [1] | --- | 2.1 | |

## Electrical Characteristics ($T_J$=25°C, Unless Otherwise Noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Units |
|---|---|---|---|---|---|---|
| $BV_{DSS}$ | Drain-Source Breakdown Voltage | $V_{GS}$=0V , $I_D$=250μA | 30 | --- | --- | V |
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance [2] | $V_{GS}$=10V , $I_D$=30A | --- | 3.5 | 4.5 | mΩ |
| | | $V_{GS}$=4.5V , $I_D$=15A | --- | 6.5 | 8.5 | |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS}$=$V_{DS}$ , $I_D$=250μA | 1.0 | 1.6 | 2.5 | V |
| $\Delta V_{GS(th)}$ | $V_{GS(th)}$ Temperature Coefficient | | --- | -6.16 | --- | mV/°C |
| $I_{DSS}$ | Drain-Source Leakage Current | $V_{DS}$=24V , $V_{GS}$=0V , $T_J$=25°C | --- | --- | 1.0 | μA |
| | | $V_{DS}$=24V , $V_{GS}$=0V , $T_J$=55°C | --- | --- | 5.0 | |
| $I_{GSS}$ | Gate-Source Leakage Current | $V_{GS}$=±20V , $V_{DS}$=0V | --- | --- | ±100 | nA |
| $g_{fs}$ | Forward Transconductance | $V_{DS}$=5V , $I_D$=30A | --- | 22 | --- | S |
| $R_g$ | Gate Resistance | $V_{DS}$=0V , $V_{GS}$=0V , f = 1.0MHz | --- | 1.7 | 3.4 | Ω |
| $Q_g$ | Total Gate Charge (4.5V) | $V_{DS}$=15V , $V_{GS}$=4.5V , $I_D$=15A | --- | 20 | --- | nC |
| $Q_{gs}$ | Gate-Source Charge | | --- | 7.6 | --- | |
| $Q_{gd}$ | Gate-Drain Charge | | --- | 7.2 | --- | |
| $T_{d(on)}$ | Turn-On Delay Time | $V_{DD}$=15V , $V_{GS}$=10V , $R_G$=3.3Ω , $I_D$=15A | --- | 7.8 | --- | ns |
| $T_r$ | Rise Time | | --- | 15 | --- | |
| $T_{d(off)}$ | Turn-Off Delay Time | | --- | 37.3 | --- | |
| $T_f$ | Fall Time | | --- | 10.5 | --- | |
| $C_{iss}$ | Input Capacitance | $V_{DS}$=15V , $V_{GS}$=0V , f = 1.0MHz | --- | 2295 | --- | pF |
| $C_{oss}$ | Output Capacitance | | --- | 267 | --- | |
| $C_{rss}$ | Reverse Transfer Capacitance | | --- | 210 | --- | |

## Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Units |
|---|---|---|---|---|---|---|
| $I_S$ | Continuous Source Current [1,6] | $V_G$=$V_D$=0V , Force Current | --- | --- | 80 | A |
| $I_{SM}$ | Pulsed Source Curren [2,6] | | --- | --- | 160 | A |
| $V_{SD}$ | Diode Forward Voltage [2] | $V_{GS}$=0V , $I_S$=1A , $T_J$=25°C | --- | --- | 1.0 | V |
| $t_{rr}$ | Reverse Recovery Time | $I_F$=30A, dI/dt=100A/μs , $T_J$=25°C | --- | 14 | --- | ns |
| $Q_{rr}$ | Reverse Recovery Charge | | --- | 5 | --- | nC |

Note:

1. The data tested by surface mounted on a 1 inch$^2$ FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width ≤ 300μs , duty cycle ≤ 2%
3. The $E_{AS}$ data shows Max. rating . The test condition is $V_{DD}$=24V, $V_{GS}$=10V, L=0.5mH, $I_{AS}$=38A
4. The power dissipation is limited by 150°C junction temperature.
5. The data is theoretically the same as $I_D$ and $I_{DM}$ , in real applications , should be limited by total power dissipation.

## Typical Characteristics

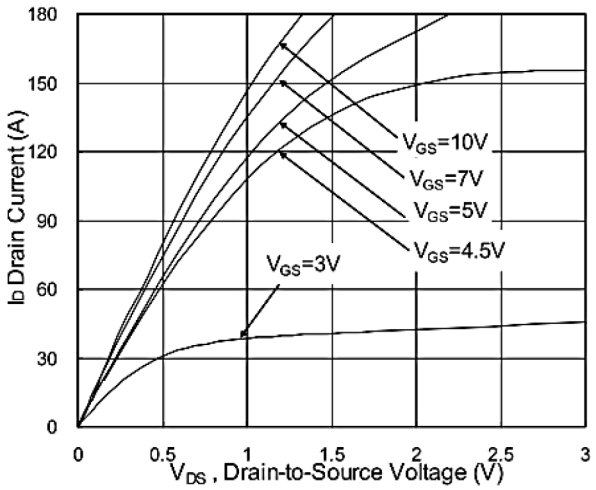



**Fig.1 Typical Output Characteristics**

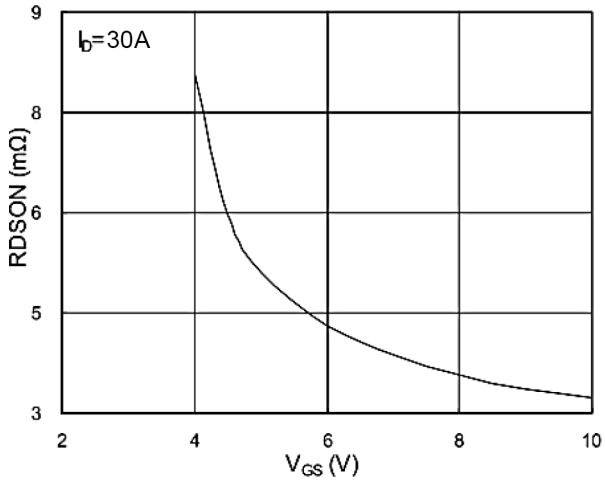



**Fig.2 On-Resistance vs. G-S Voltage**

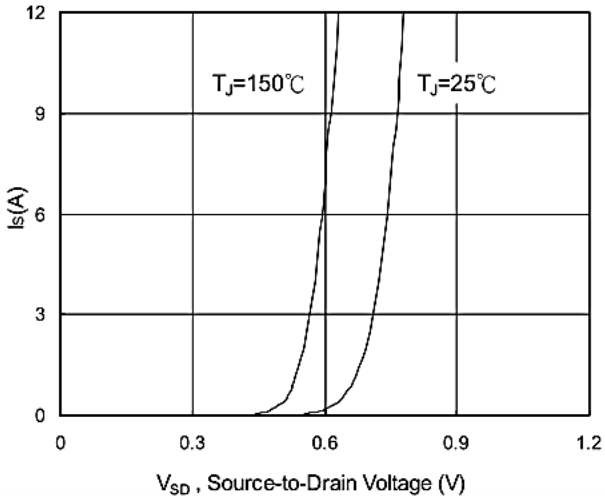



**Fig.3 Forward Characteristics of Reverse**

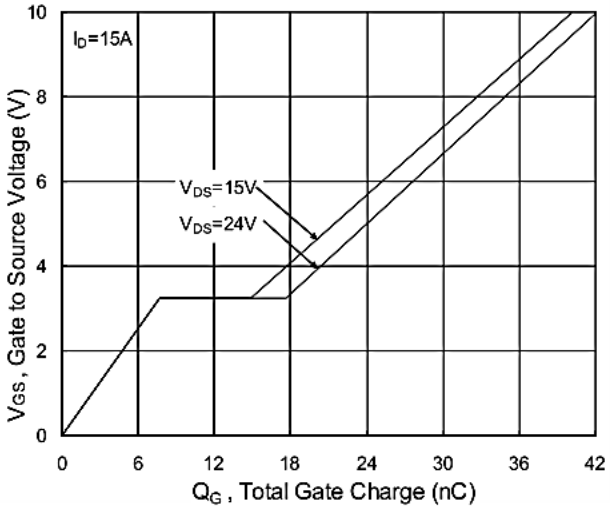



**Fig.4 Gate-Charge Characteristics**

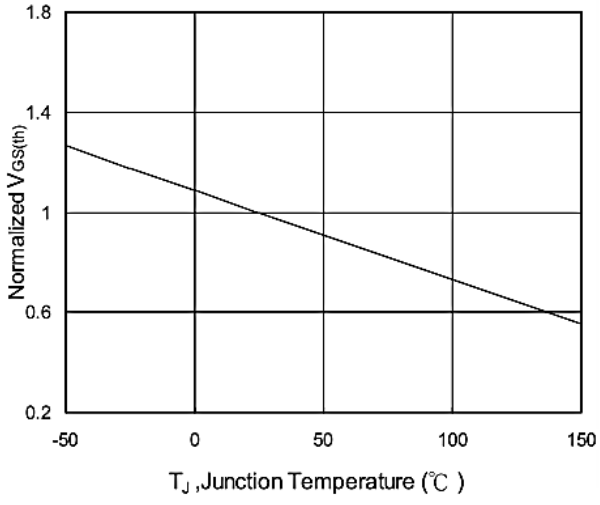



**Fig.5 Normalized $V_{GS(th)}$ vs. $T_J$**

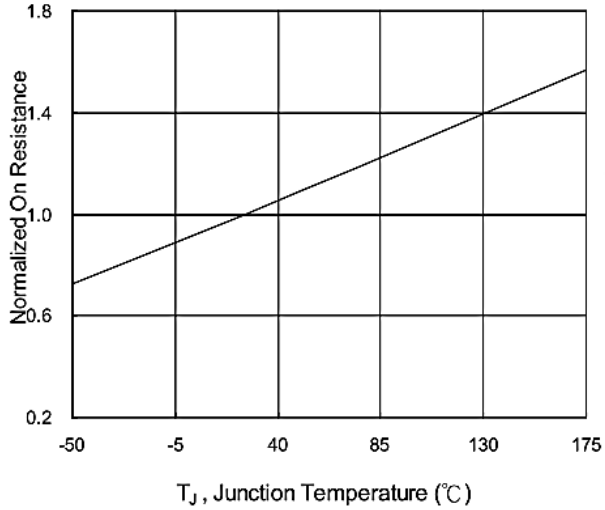



**Fig.6 Normalized $R_{DSON}$ vs. $T_J$**

## Typical Characteristics (Cont.)

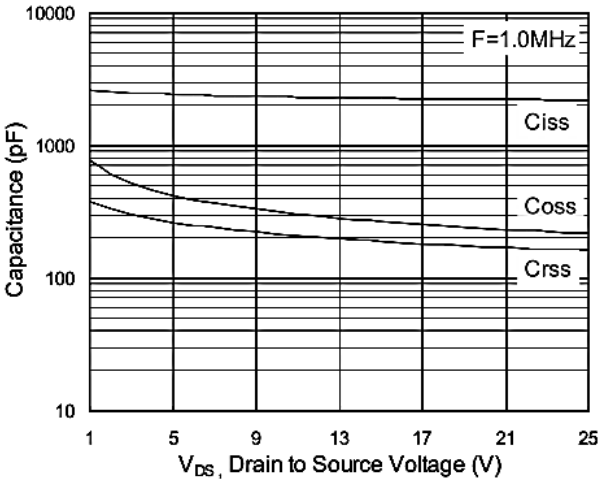



Fig.7 Capacitance

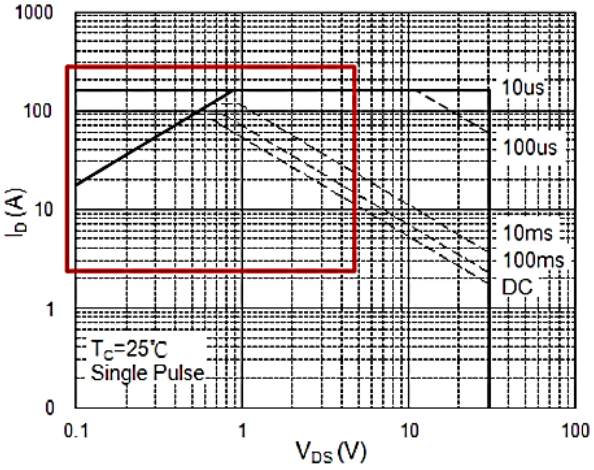



Fig.8 Safe Operating Area

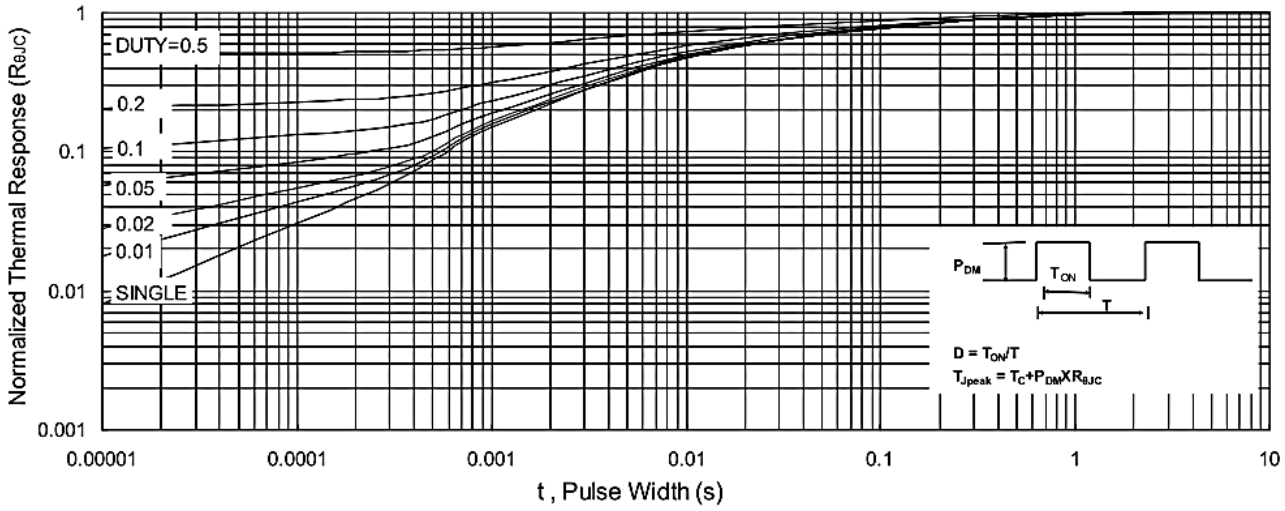



Fig.9 Normalized Maximum Transient Thermal Impedance

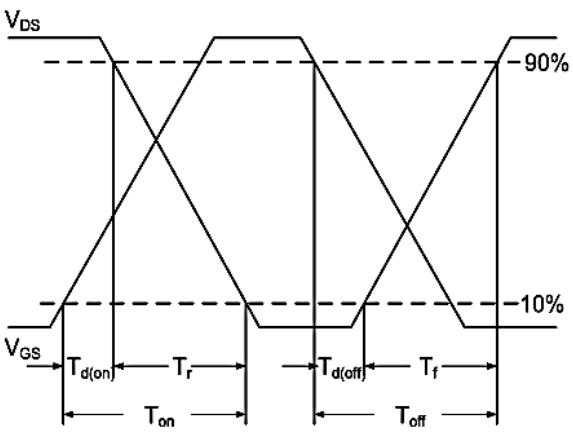



Fig.10 Switching Time Waveform

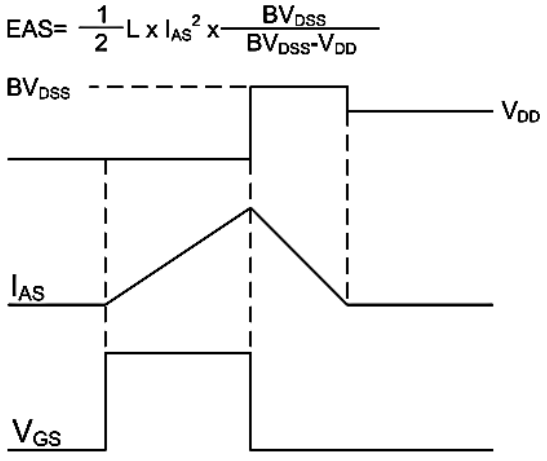
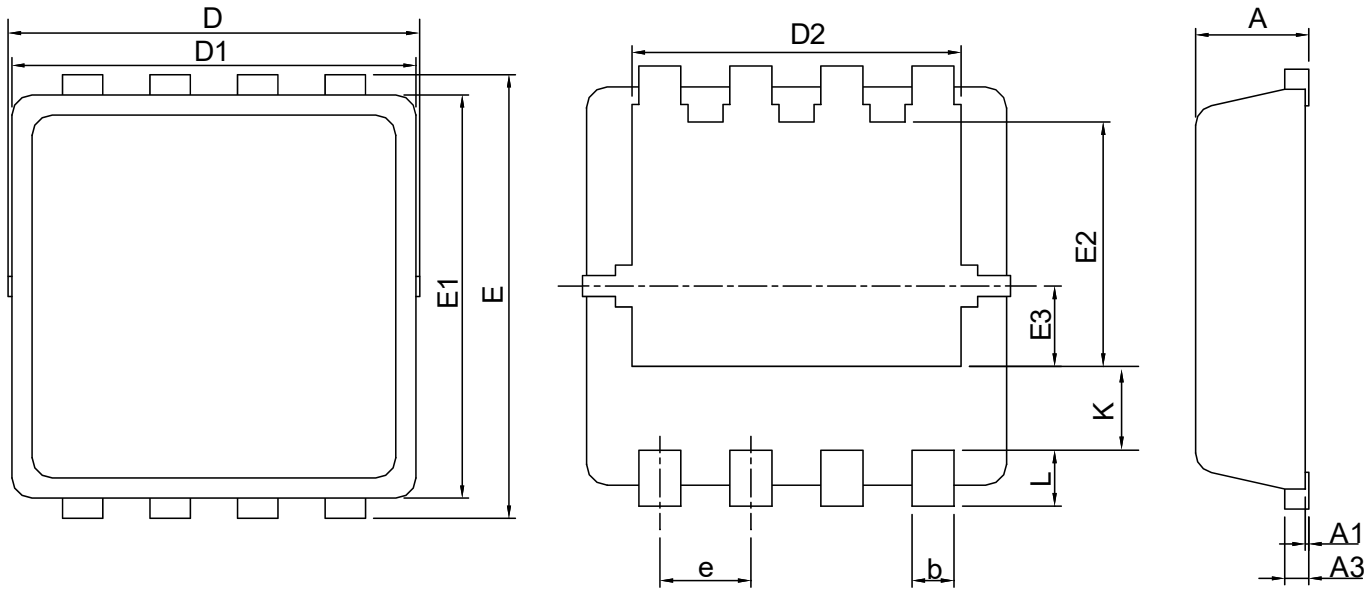


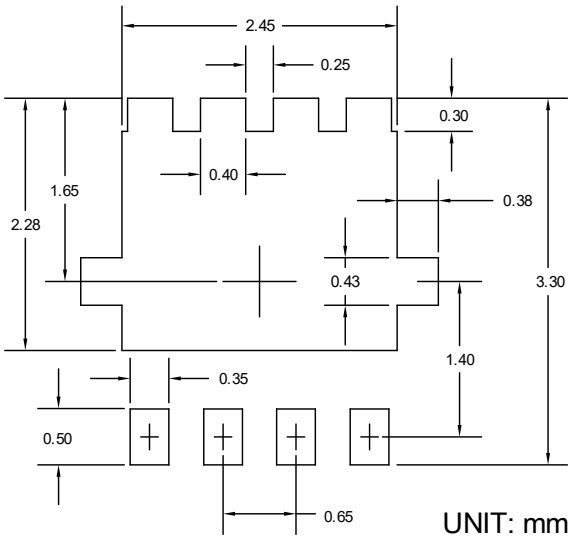

Fig.11 Unclamped Inductive Switching Waveform

## Packaging information




| SYMBOL | DFN3X3-8L | | | |
|---|---|---|---|---|
| | MILLIMETERS | | INCHES | |
| | MIN. | MAX. | MIN. | MAX. |
| A | 0.80 | 1.00 | 0.031 | 0.039 |
| A1 | 0.00 | 0.05 | 0.000 | 0.002 |
| A3 | 0.10 | 0.25 | 0.004 | 0.010 |
| b | 0.24 | 0.35 | 0.009 | 0.014 |
| D | 2.90 | 3.30 | 0.114 | 0.130 |
| D1 | 2.90 | 3.10 | 0.114 | 0.122 |
| D2 | 2.25 | 2.45 | 0.089 | 0.096 |
| E | 3.10 | 3.30 | 0.122 | 0.130 |
| E1 | 2.90 | 3.10 | 0.114 | 0.122 |
| E2 | 1.65 | 1.85 | 0.065 | 0.073 |
| E3 | 0.56 | 0.58 | 0.022 | 0.023 |
| e | 0.65 BSC | | 0.026 BSC | |
| K | 0.475 | 0.775 | 0.019 | 0.031 |
| L | 0.30 | 0.50 | 0.012 | 0.020 |

**RECOMMENDED LAND PATTERN**



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