

**N-Ch MOSFET** 

## **General Description**

The WSD45N10GDN56 is the highest performance SGT N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSD45N10GDN56 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

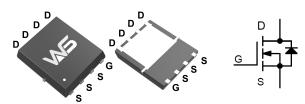
## **Product Summery**

BVDSS	RDSON	ID
100V	17.5mΩ	45A

# **Applications**

- DC-DC Converter.
- Motor Control.

# **DFN5X6 Pin Configuration**



# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	100	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	45	Α	
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	33	Α	
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	12	Α	
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	9.6	Α	
I <sub>DM</sub> <sup>a</sup>	Pulsed Drain Current	130	Α	
E <sub>AS</sub> <sup>b</sup>	Single Pulse Avalanche Energy	169	mJ	
I <sub>AS</sub> b	Avalanche Current	26	Α	
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	95	W	
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation 5.0		W	
T <sub>STG</sub>	Storage Temperature Range -55 to 150		°C	
$T_J$	Operating Junction Temperature Range	-55 to 150	$^{\circ}$	

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>θJA</sub> <sup>c</sup>	Thermal Resistance Junction-ambient		60	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case		2.4	°C/W

Note a: Pulse width limited by max. junction temperature.

Note b: UIS tested and pulse width limited by maximum junction temperature  $150^{\circ}$ C (initial temperature  $T_j=25^{\circ}$ C).

Note c: Surface Mounted on 1in<sup>2</sup> pad area.

**N-Ch MOSFET** 

# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	100			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.0		V/℃
R <sub>DS(ON)</sub> d	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =26A		14.5	17.5	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	2.0	3.0	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient			-5		mV/℃
ı	Drain-Source Leakage Current	$V_{DS}$ =80V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}\mathrm{C}$		-	1	uA
I <sub>DSS</sub>		$V_{DS}$ =80V , $V_{GS}$ =0V , $T_J$ =55 $^{\circ}\mathrm{C}$		-	30	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20V$ , $V_{DS}$ = $0V$		-	±100	nA
R <sub>g</sub> e	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.0		Ω
Qg <sup>e</sup>	Total Gate Charge (10V)	V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , I <sub>D</sub> =26A		42	59	
Q <sub>gs</sub> e	Gate-Source Charge			12		nC
$Q_{gd}^e$	Gate-Drain Charge			12		
T <sub>d(on)</sub> e	Turn-On Delay Time	$V_{DD}$ =30V , $V_{GEN}$ =10V , $R_{G}$ =6 $\Omega$ $I_{D}$ =1A ,RL=30 $\Omega$		19	35	
T <sub>r</sub> e	Rise Time			9	17	]
T <sub>d(off)</sub> e	Turn-Off Delay Time			36	65	ns
T <sub>f</sub> e	Fall Time			22	40	
C <sub>iss</sub> e	Input Capacitance	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , f=1MHz		1800		
C <sub>oss</sub> e	Output Capacitance			215		pF
C <sub>rss</sub> e	Reverse Transfer Capacitance			42		

# **Diode Characteristics**

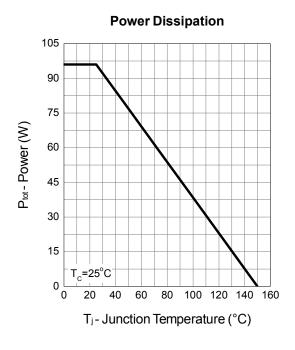
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current		-	26	Α
$V_{SD}^d$	Diode Forward Voltage	$V_{GS}$ =0V , $I_S$ =1A , $T_J$ =25 $^{\circ}$ C		-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I- 004 IVII 4004/ T 05%		44		nS
Q <sub>rr</sub>	Reverse Recovery Charge	IF=20A , dI/dt=100A/μs , T <sub>J</sub> =25℃		95		nC

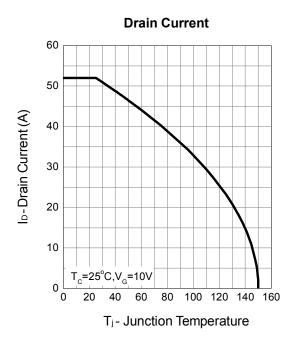
Note d : Pulse test ; pulse width $\leq 300 \mu s$ , duty cycle $\leq 2\%$ .

Note e: Guaranteed by design, not subject to production testing.

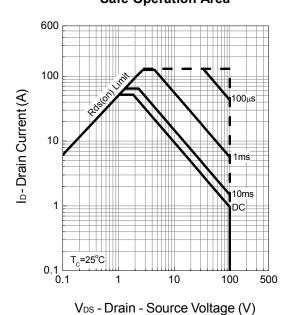


# **Typical Characteristics**

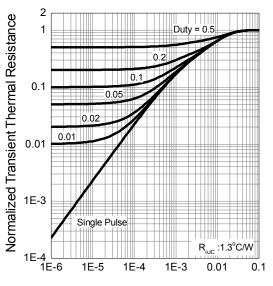




# Safe Operation Area



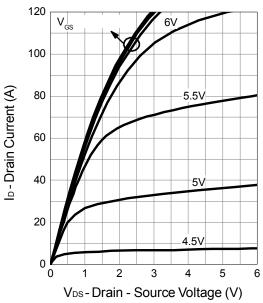
# Thermal Transient Impedance



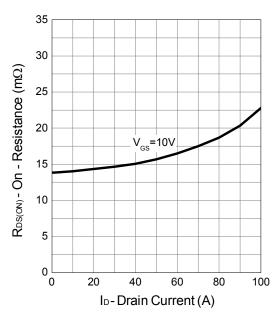
Square Wave Pulse Duration (sec)



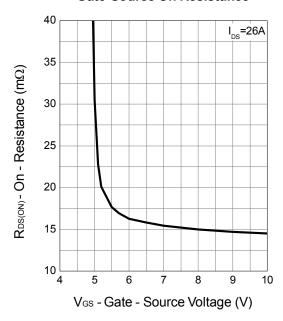
# Output Characteristics



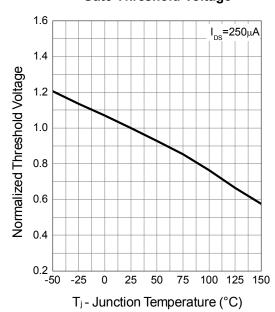
#### **Drain-Source On Resistance**



## **Gate-Source On Resistance**

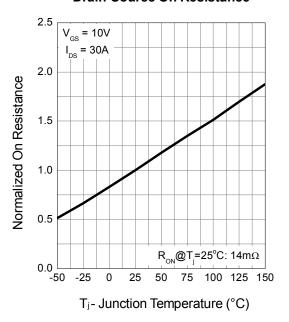


# **Gate Threshold Voltage**

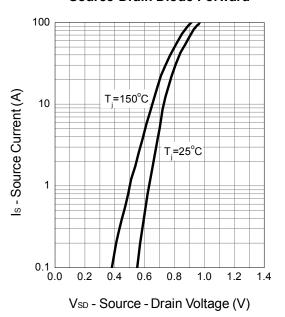


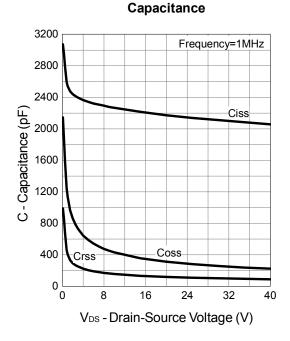


#### **Drain-Source On Resistance**

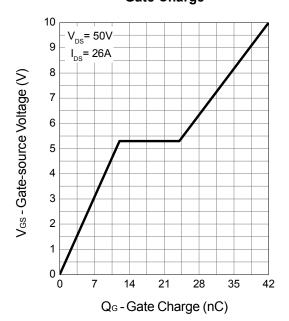


## **Source-Drain Diode Forward**





**Gate Charge** 





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