

General Description

The WSD6036DN is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent $R_{DS(on)}$ and gate charge for most of the synchronous buck converter applications.

The WSD6040DN56 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Features

- Lead Free and Green Devices Available (RoHS Compliant)
- 100% UIS + Rg Tested
- Reliable and Rugged
- Moisture Sensitivity Level MSL1 (per JED EC J-STD-020D)

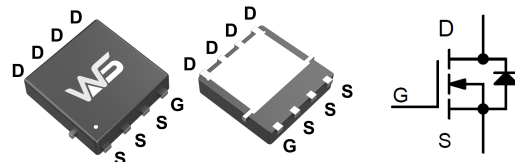
Product Summary

B_{VDSS}	$R_{DS(on)}$	I_D
60V	12m Ω	50A

Applications

- Secondary Side Synchronous Rectification
- DC-DC Converter
- Motor Control
- Load Switching

DFN3x3-8_EP1 Pin Configuration



Absolute Maximum Ratings @ $T_A=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter		Rating	Units
V_{DS}	Drain-Source Voltage		60	V
V_{GS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_c=25^\circ\text{C}$	50	A
		$T_c=100^\circ\text{C}$	30	
I_{DM}^a	Pulsed Drain Current	$T_c=25^\circ\text{C}$	90	A
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	45	W
		$T_c=100^\circ\text{C}$	18	
E_{AS}^c	Single Pulse Avalanche Energy	$L=0.1\text{mH}$	39.2	mJ
I_S	Diode Continuous Forward Current	$T_c=25^\circ\text{C}$	50	A
T_J	Maximum Junction Temperature		150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range		-55 to 150	$^\circ\text{C}$
$R_{\theta JA}^b$	Thermal Resistance Junction to ambient	Steady State	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	3.3	$^\circ\text{C}/\text{W}$

Note a: Pulse width limited by max. junction temperature.

Note b: Surface Mounted on 1in2 pad area.

Note c: UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_j=25^\circ\text{C}$).

Electrical Characteristics @ T_A=25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	60			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 48 V, V _{GS} = 0V			1	μA
		T _J =85°C			30	
I _{GSS}	Gate Leakage Current	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
On Characteristics						
V _{GS(TH)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 250μA	1	1.6	2.5	V
R _{DS(on)} ^d	Drain-Source On-state Resistance	V _{GS} = 10V, I _D = 25A		14	17.5	mΩ
		V _{GS} = 4.5V, I _D = 20A		19	22	mΩ
Switching						
Q _g	Total Gate Charge	V _{DS} =30V V _{GS} =10V I _D =25A		42		nC
Q _{gs}	Gate-Source Charge			6.4		nC
Q _{gd}	Gate-Drain Charge			9.6		nC
t _{d(on)}	Turn-on Delay Time	V _{GEN} =10V V _{DD} =30V I _D =1A R _G =6Ω R _L =30Ω		17		ns
t _r	Turn-on Rise Time			9		ns
t _{d(off)}	Turn-off Delay Time			58		ns
t _f	Turn-off Fall Time			14		ns
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		1.5		Ω
Dynamic						
C _{iss}	In Capacitance	V _{GS} =0V V _{DS} =30V f=1MHz		2100		pF
C _{oss}	Out Capacitance			140		pF
C _{rss}	Reverse Transfer Capacitance			100		pF
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Continuous Source Current	V _G =V _D =0V, Force Current			18	A
I _{SM}	Pulsed Source Current ³				35	A
V _{SD} ^d	Diode Forward Voltage	I _{SD} = 20A, V _{GS} =0V		0.8	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =25A, dI _{SD} /dt=100A/μs		27		ns
Q _{rr}	Reverse Recovery Charge				33	

Note d: Pulse test ; pulse width ≤ 300μs, duty cycle ≤ 2%.

Note e: Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

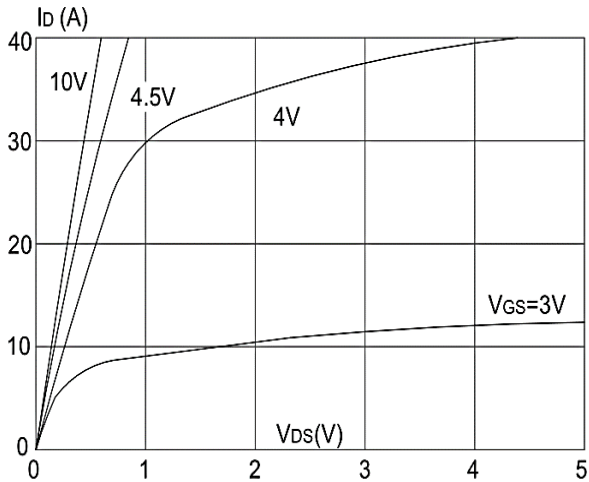


Figure 1: Output Characteristics

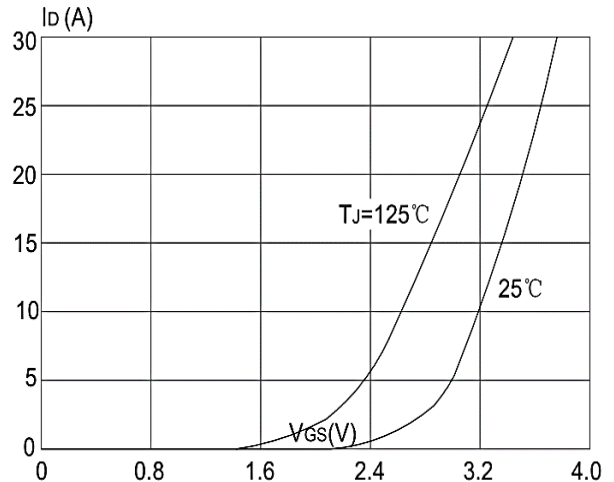


Figure 2: Typical Transfer Characteristics

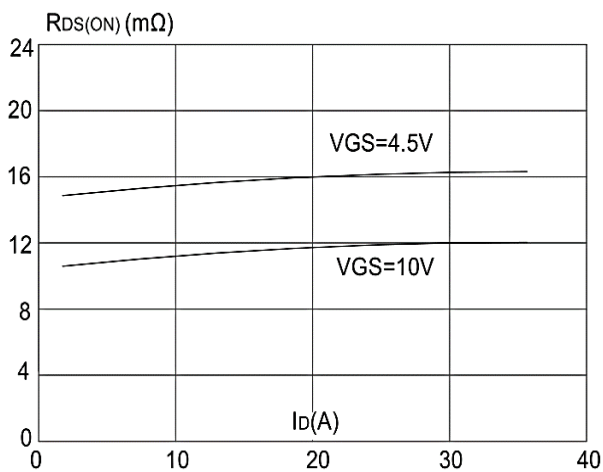


Figure 3: On-resistance vs. Drain Current

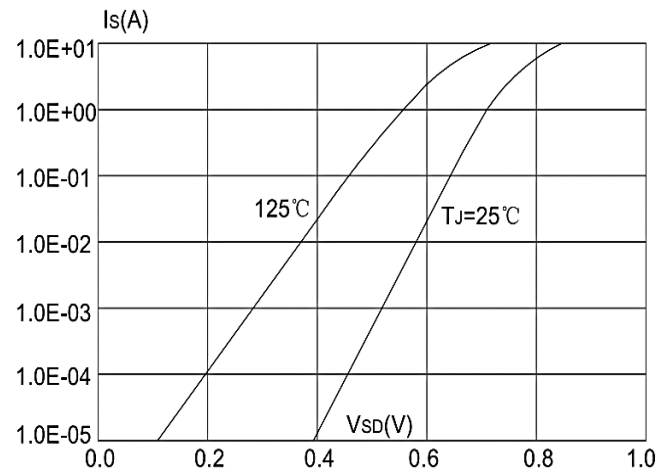


Figure 4: Body Diode Characteristics

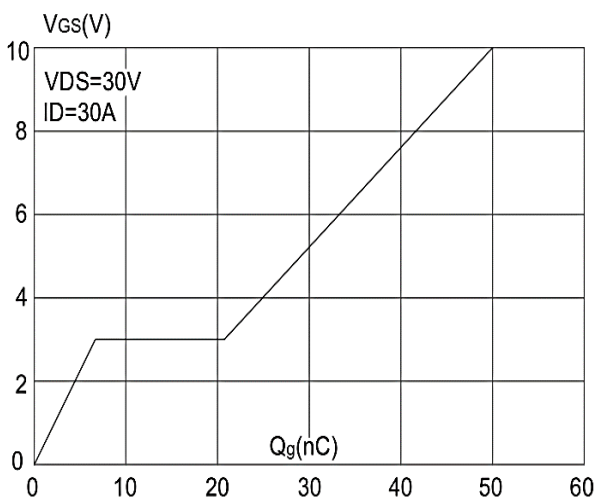


Figure 5: Gate Charge Characteristics

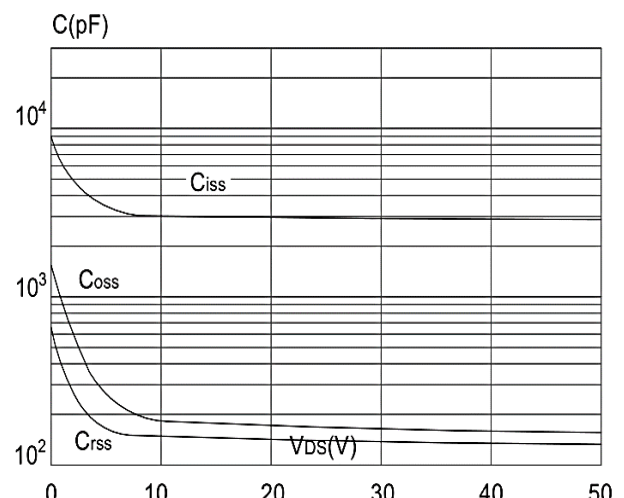


Figure 6: Capacitance Characteristics

Typical Operating Characteristics

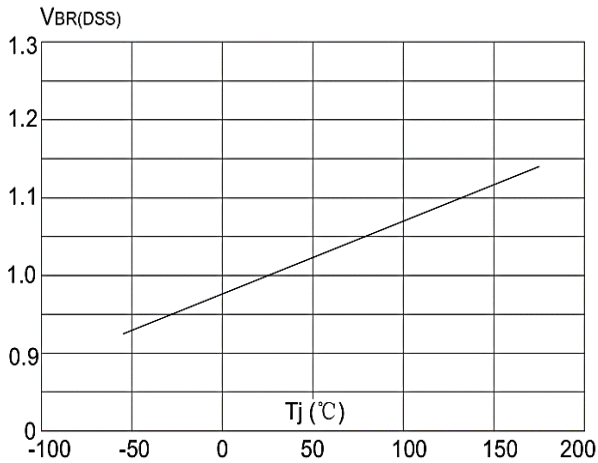


Figure 7: Normalized Breakdown Voltage vs Junction Temperature

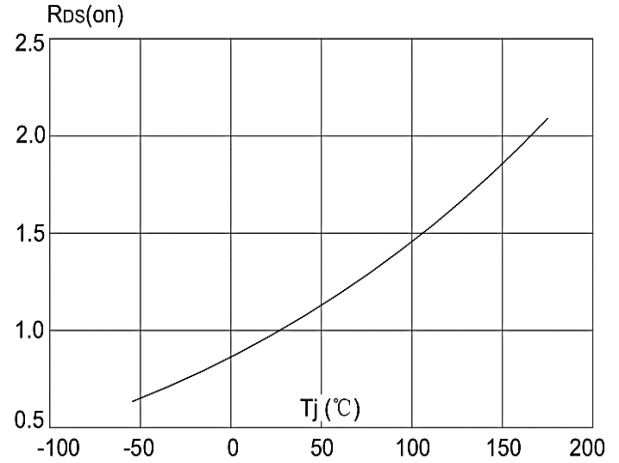


Figure 8: Normalized on Resistance vs. Junction Temperature

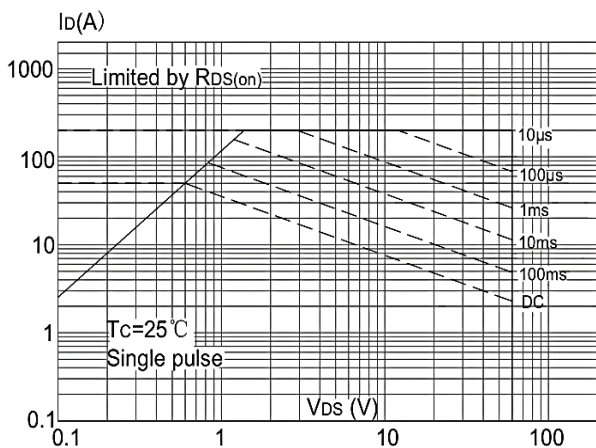


Figure 9: Maximum Safe Operating Area

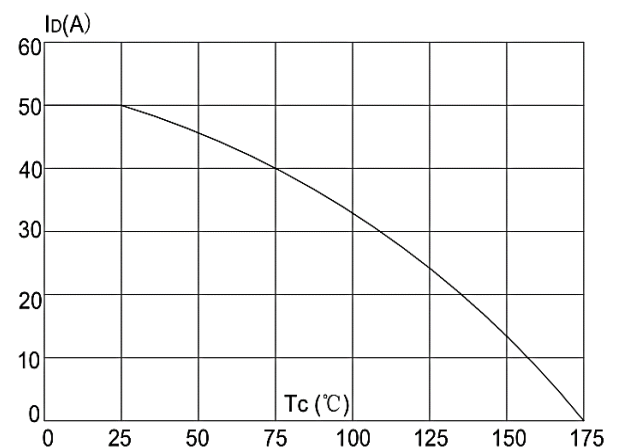


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

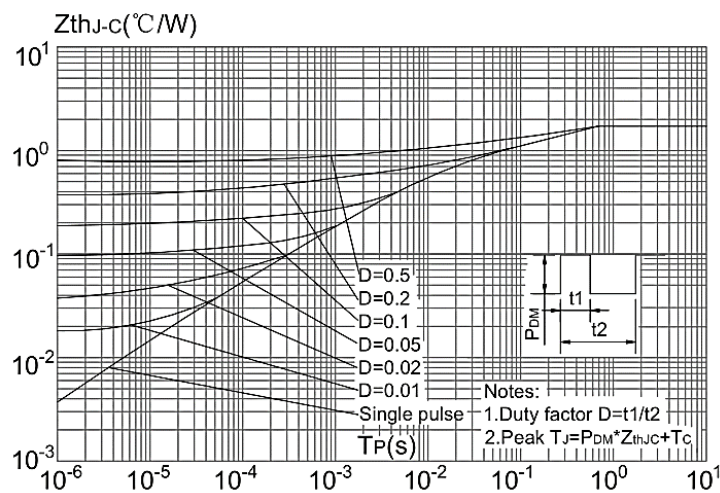


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambien



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