

**N-Ch MOSFET** 

#### **General Description**

The WSD6040DN56 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent  $R_{DSON}$  and gate charge for most of the synchronous buck converter applications .

The WSD6040DN56 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

#### **Features**

Lead Fre e an d Green Devices Available

(RoH SCom plia nt)

100% UIS + Rg Tested

Reliable and Rugged

Moistu re Sensitivity Level MSL1

(per JED EC J-STD-020D)

### **Product Summery**

Bvdss	Rdson	In
60V	17.5mΩ	36A

#### **Applications**

Secondary Side Synchronous Rectification

DC-DC Converter

Motor Control

Load Switching

## DFN5x6A-8\_EP Pin Configuration



## Absolute Maximum Ratings $@T_A=25^{\circ}\mathbb{C}$ unless otherwise noted

Symbol	Parameter			Rating	Units	
V <sub>DS</sub>	Drain-Source Voltage			60	V	
V <sub>GS</sub>	Gate-Source Voltage			±20	V	
Ib	Continuous Drain Current $\frac{Tc=2}{Tc=10}$		С	36	A	
			°C	22		
Ιp	Continuous Drain Current	T <sub>A</sub> =25°C		8.4	A	
ID			°C	6.8	А	
I <sub>DM</sub> <sup>a</sup>	Pulsed Drain Current	Tc=25°C		140	A	
PD	Maximum Power Dissipation $\frac{Tc=25}{Tc=10}$		С	37.8	W	
			°C	15.1		
D.,	Maximum Power Dissipation $\frac{T_A=25}{T_A=70}$		С	2.08	W	
PD			С	1.33	W	
I <sub>AS</sub> c	Avalanche Current, Single pulse		L=0.5mH	16	A	
Eas c	Single Pulse Avalanche Energy		L=0.5mH	64	mJ	
Is	Diode Continuous Forward Current		Tc=25°C	18	A	
TJ	Maximum Junction Temperature			150	$^{\circ}$	
Tstg	Storage Temperature Range			-55 to 150	$^{\circ}$	
R <sub>0</sub> JA <sup>b</sup>	Thermal Resistance Junction to ambient St		Steady State	60	°C/W	
Rелс	Thermal Resistance-Junction to Case Steady State		Steady State	3.3	°C/W	



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Note a: Pulse width limited by max. junction temperature.

Note b: Surface Mounted on 1in2 pad area.

Note c: UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature Tj=25°C).

## Electrical Characteristics @TA=25°C unless otherwise noted

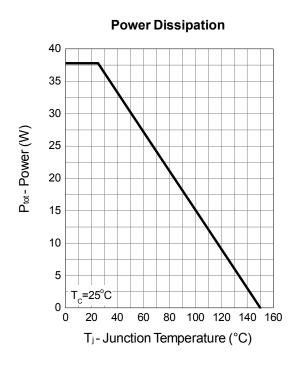
Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
Static	·						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0V$ , $I_D = 250 \mu A$		60			V
	Z. C. W. W. D. C.	$V_{DS}=48 \text{ V}, V_{GS}=0 \text{V}$				1	μА
Idss	Zero Gate Voltage Drain Current	T <sub>J</sub> =85°C				30	
Igss	Gate Leakage Current	$V_{GS} = \pm 20V, V_{SS}$	$_{\mathrm{DS}}=0\mathrm{V}$			±100	nA
On Characte	ristics						
V <sub>GS(TH)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS},I_{DS}=250\mu A$		1	1.6	2.5	V
R <sub>DS(on)</sub> <sup>d</sup>	D : C O D : .	$V_{GS} = 10V, I_D = 25A$			14	17.5	$m\Omega$
KDS(on)	Drain-Source On-state Resistance	$V_{GS} = 4.5V$ , $I_D$		19	22	$m\Omega$	
Switching							
Qg	Total Gate Charge	V <sub>DS</sub> =30V V <sub>GS</sub> =10V I <sub>D</sub> =25A			42		nC
Qgs	Gate-Sour Charge				6.4		nC
Qgd	Gate-Drain Charge				9.6		nC
td (on)	Turn-on Delay Time	$V_{\text{GEN}}$ =10V $V_{\text{DD}}$ =30V $I_{\text{D}}$ =1A $R_{\text{G}}$ =6 $\Omega$ $RL$ =30 $\Omega$			17		ns
tr	Turn-on Rise Time				9		ns
td(off)	Turn-off Delay Time				58		ns
tf	Turn-off Fall Time				14		ns
Rg	Gat resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz			1.5		Ω
Dynamic							
Ciss	In Capacitance	V <sub>GS</sub> =0V V <sub>DS</sub> =30V f=1MHz			2100		pF
Coss	Out Capacitance				140		pF
Crss	Reverse Transfer Capacitance				100		pF
Drain-Source	e Diode Characteristics and Maximum	Ratings					
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current				18	A
Isм	Pulsed Source Current3					35	A
$V_{ m SD}^{d}$	Diode Forward Voltage	$I_{SD} = 20A$ , $V_{C}$	<sub>GS</sub> =0V		0.8	1.3	V
trr	Reverse Recovery Time	- I <sub>SD</sub> =25A, dl <sub>SD</sub> /dt=100A/μs			27		ns
Qrr	Reverse Recovery Charge				33		nC

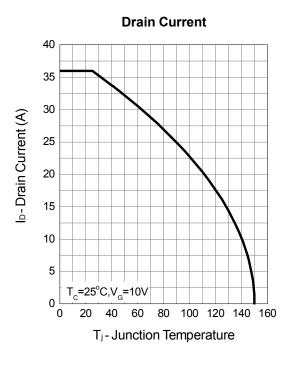
Note d: Pulse test; pulse width≤300µs, duty cycle≤2%.

Note e: Guaranteed by design, not subject to production testing.

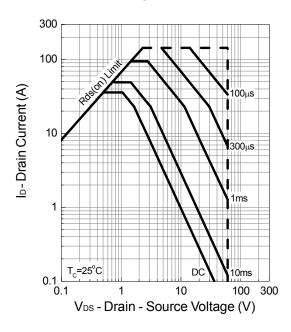


# **Typical Operating Characteristics**

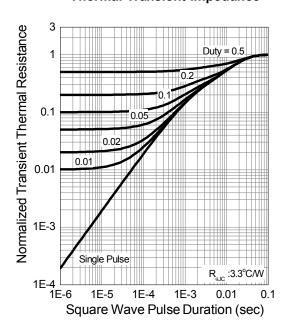




### Safe Operation Area

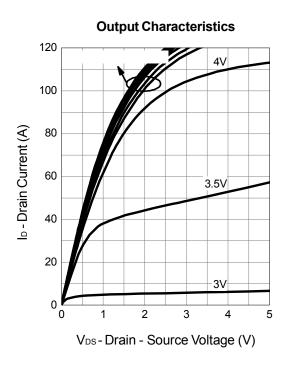


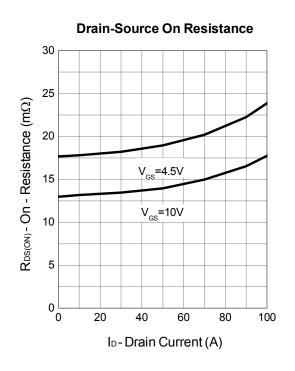
### **Thermal Transient Impedance**

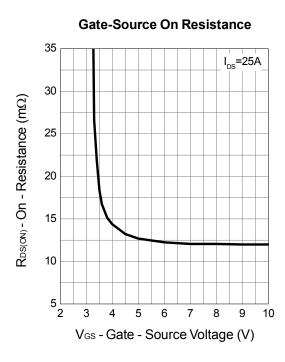


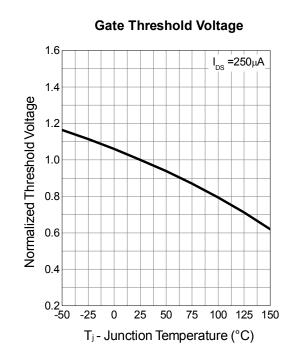


# **Typical Operating Characteristics (Cont.)**





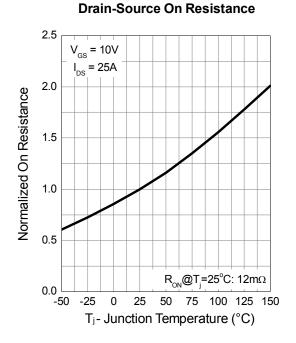




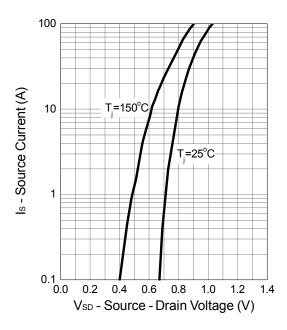


# **Typical Operating Characteristics (Cont.)**

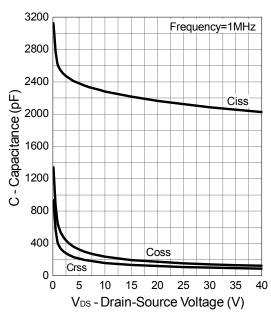
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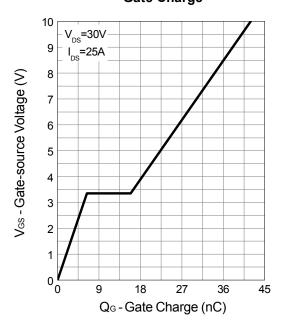
#### Source-Drain Diode Forward



## Capacitance



# **Gate Charge**





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