

General Description

The WSF10N40 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent R_{DS(on)} and gate charge for most of the synchronous buck converter applications.

Features

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

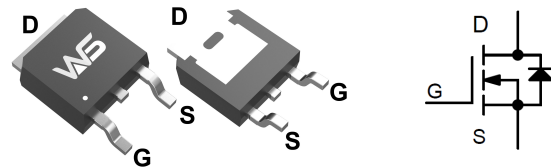
Product Summary

BVDSS	R _{DS(on)}	I _D
400V	515mΩ	10A

Applications

- DC-DC & DC-AC Converters for telecom, industrial and consumer environment
- Uninterruptible Power Supply (UPS)
- Switch Mode Low Power Supplies
- Industrial Actuators

TO-252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	400	V
V _{GS}	Gate-Source Voltage	±25	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	10.0	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	5.6	A
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	1.2	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	0.6	A
I _{DM}	Pulsed Drain Current ²	36	A
EAS	Single Pulse Avalanche Energy ³	220	mJ
I _{AS}	Avalanche Current	27	A
P _D @T _C =25°C	Total Power Dissipation ⁴	56.0	W
P _D @T _A =25°C	Total Power Dissipation ⁴	1.5	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	2.1	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	400	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25 °C, I _D = 250uA	---	0.3	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =4.5A	---	515	607	mΩ
		V _{GS} =8.0V, I _D =3A	---	1100	2000	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2.0	---	4.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.52	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =320V, V _{GS} =0V, T _J =25°C	---	---	10	uA
		V _{DS} =320V, V _{GS} =0V, T _J =55°C	---	---	100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =30V, I _D =4.5A	---	8.0	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.6	3.2	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =320V, V _{GS} =10V, I _D =9A	---	25	35	nC
Q _{gs}	Gate-Source Charge		---	4.0	6.0	
Q _{gd}	Gate-Drain Charge		---	10.5	12.1	
T _{d(on)}	Turn-On Delay Time	V _{DD} =200V, V _{GS} =10V, R _G =5Ω I _D =9A	---	12.4	---	ns
T _r	Rise Time		---	20.1	---	
T _{d(off)}	Turn-Off Delay Time		---	38.5	---	
T _f	Fall Time		---	10.8	---	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	740	---	pF
C _{oss}	Output Capacitance		---	83	---	
C _{riss}	Reverse Transfer Capacitance		---	9.0	---	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V, L=0.1mH, I _{AS} =10A	100	---	---	mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	9.0	A
I _{SM}	Pulsed Source Current ^{2,6}		---	---	35	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =4.5A	---	---	1.5	V
t _{rr}	Reverse Recovery Time	I _F =9A, dI/dt=100A/μs	---	320	---	nS
Q _{rr}	Reverse Recovery Charge		---	1345	---	nC

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=10A
- The power dissipation is limited by 150°C junction temperature
- The Min. value is 100% EAS tested guarantee.
- The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Performance Characteristics

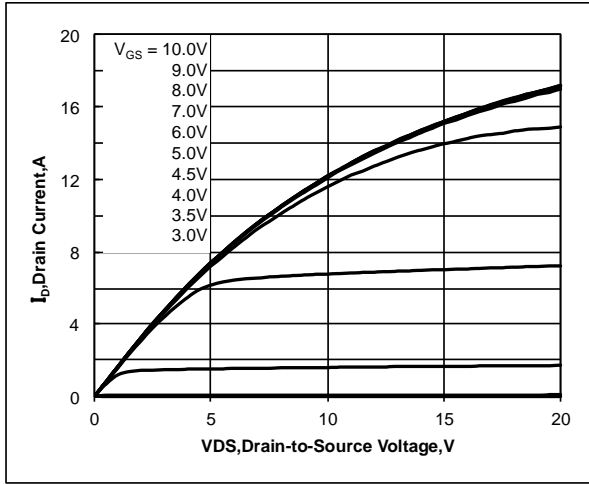


Figure 1. Output Characteristics

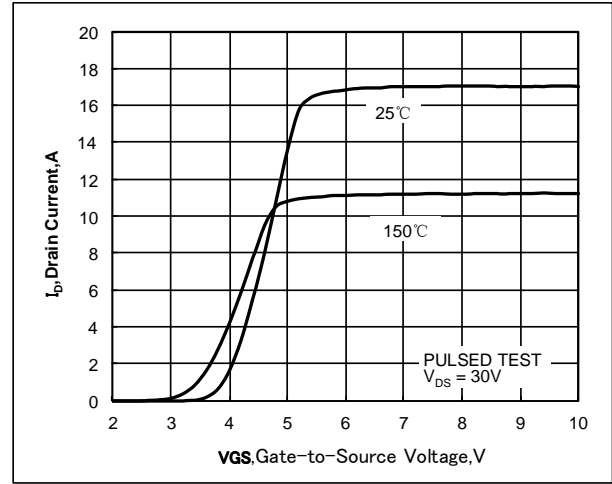


Figure 2. Transfer Characteristics

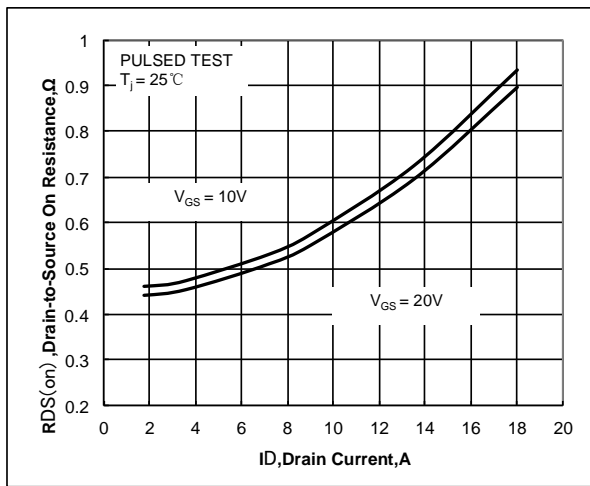


Figure 3. Drain-to-Source On Resistance vs. Drain Current and Gate Voltage

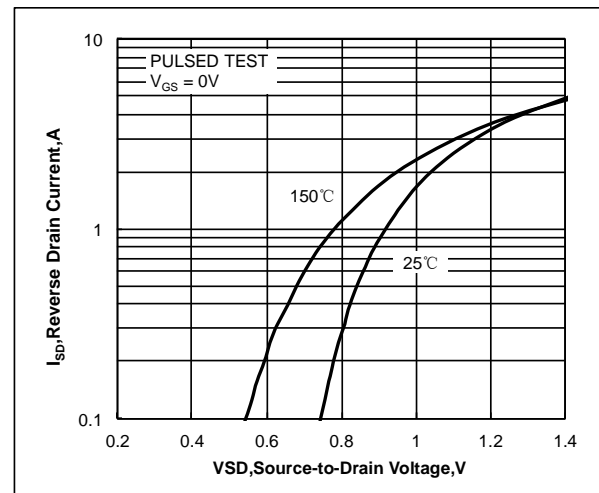


Figure 4. Body Diode Forward Voltage vs. Source Current and Temperature

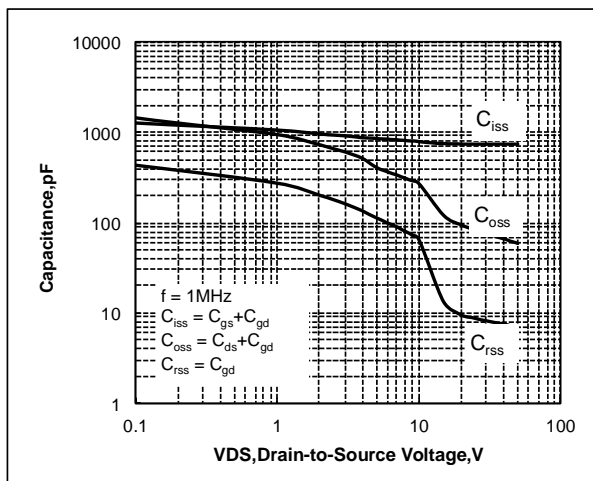


Figure 5. Capacitance Characteristics

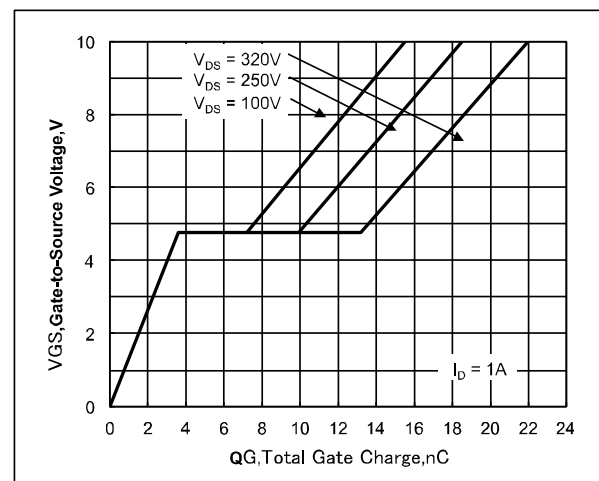


Figure 6. Gate Charge Characteristics

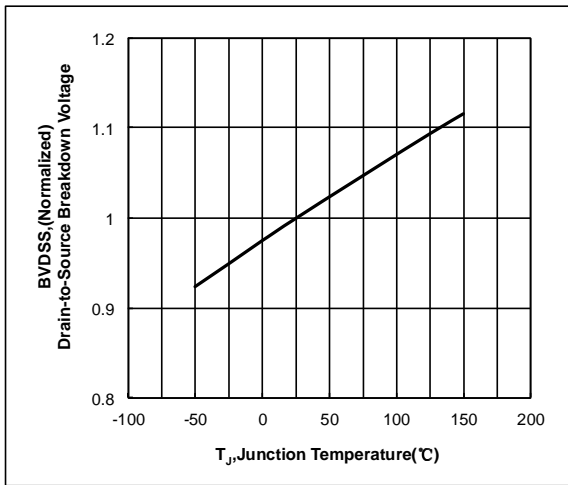


Figure 7. Normalized Breakdown Voltage vs. Junction Temperature

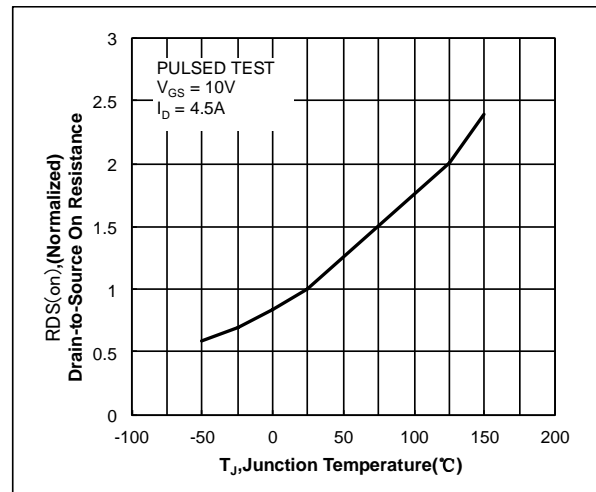


Figure 8. Normalized On Resistance vs. Junction Temperature

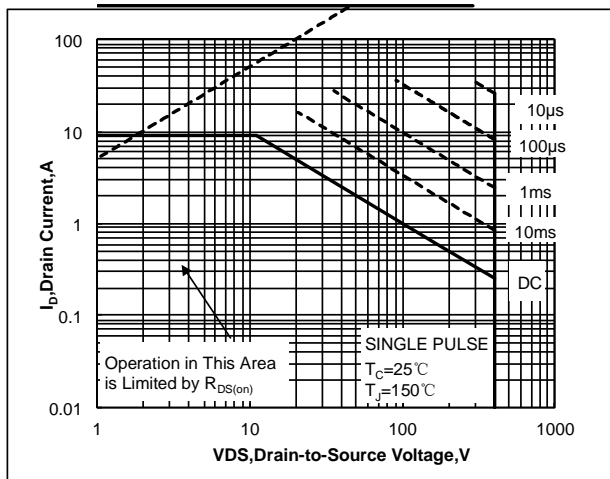


Figure 9. Maximum Safe Operating Area

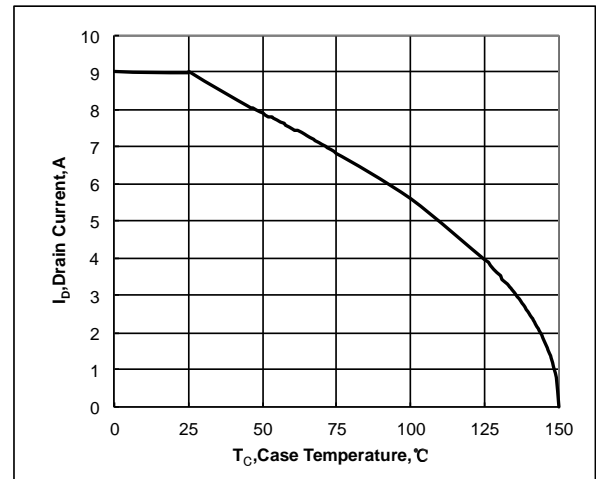


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

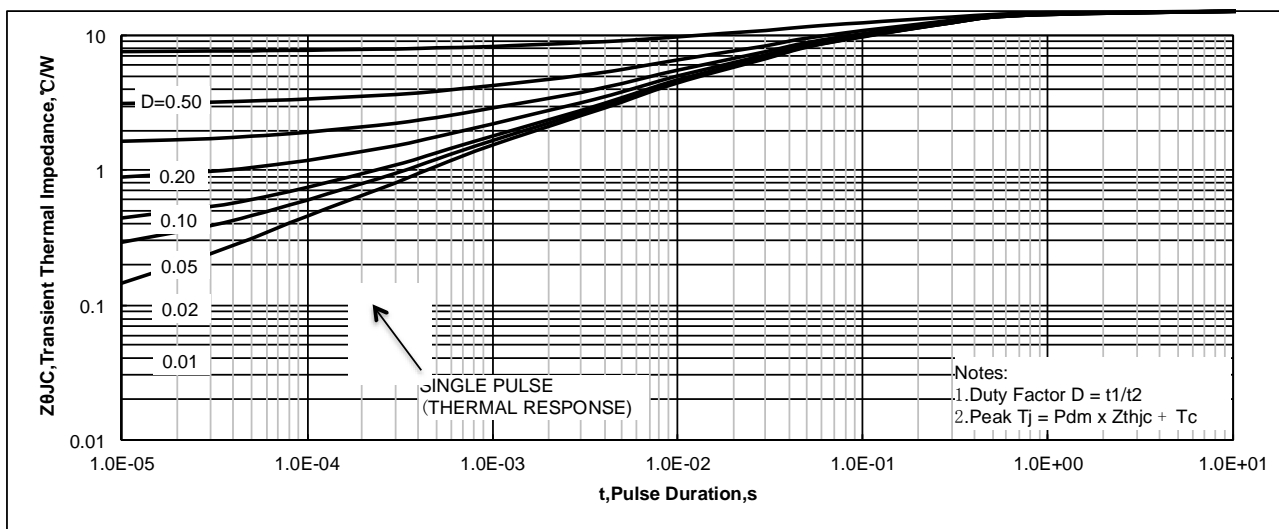


Figure 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case



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