

**N-Ch MOSFET** 

# **General Description**

The WSF20N06 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF20N06 meet the RoHS and Green Product requirement.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

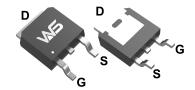
## **Product Summery**

BVDSS	RDSON	ID
60V	35mΩ	25A

#### **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- LCD/LED back light

# **TO-252 Pin Configuration**





# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	25	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	27	А
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	8	Α
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	10	А
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	100	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	38	mJ
I <sub>AS</sub>	Avalanche Current	14	Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	35	W
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>4</sup>	3.3	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C
$T_J$	Operating Junction Temperature Range -55 to 175		$^{\circ}$ C

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		75	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case <sup>1</sup>		3	°C/W



## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0 $V$ , $I_D$ =250 $u$ A	60			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25 $^{\circ}{\mathbb C}$ , ID=1mA		0.057		V/°C
D	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =16A		35	45	0
$R_{DS(ON)}$		$V_{GS}$ =5 $V$ , $I_D$ =8 $A$		40	50	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	\/ -\/   -250uA	1.0	1.6	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-5.68		mV/℃
	Drain Source Leakage Current	$V_{DS}$ =60V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			1	· uA
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =60V , $V_{GS}$ =0V , $T_J$ =125 $^{\circ}$ C			100	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 16V$ , $V_{DS}$ = $0V$			±10	nA
gfs	Forward Transconductance	V <sub>DS</sub> =25V , I <sub>D</sub> =18A		25		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.7	3.4	Ω
$Q_g$	Total Gate Charge (4.5V)	V <sub>DS</sub> =30V , V <sub>GS</sub> =10V , I <sub>D</sub> =18A		20		
$Q_{gs}$	Gate-Source Charge			7		nC
$Q_{gd}$	Gate-Drain Charge			5		
T <sub>d(on)</sub>	Turn-On Delay Time			18		
Tr	Rise Time	$V_{DD}$ =30V , $V_{GS}$ =10V , $R_G$ =6.8 $\Omega$ , $I_D$ =1A		15		no
T <sub>d(off)</sub>	Turn-Off Delay Time			60		ns
T <sub>f</sub>	Fall Time			31		
Ciss	Input Capacitance	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , f=1MHz		650		
C <sub>oss</sub>	Output Capacitance			95		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			60		

## **Guaranteed Avalanche Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy <sup>5</sup>	V <sub>DD</sub> =25V , L=0.1mH , I <sub>AS</sub> =15A	19			mJ

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			25	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>				75	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =20A , T <sub>J</sub> =25℃			1.3	V
t <sub>rr</sub>	Reverse Recovery Time	IF=20A ,dI/dt=100A/μs,TJ=25℃		65		nS
Q <sub>rr</sub>	Reverse Recovery Charge			85		nC

#### Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper,t<10sec.
- 2.The data tested by pulsed , pulse width  $\,\leq\,$  300us , duty cycle  $\,\leq\,$  2%
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH, $I_{AS}$ =15A
- 4. The power dissipation is limited by 150 °C junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



## **Typical Characteristics**

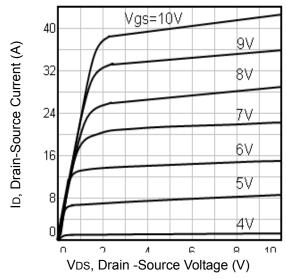


Fig1. Typical Output Characteristics

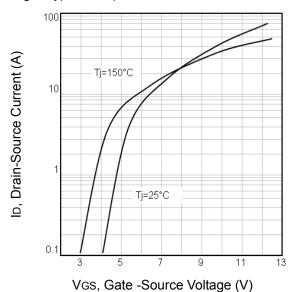


Fig3. Typical Transfer Characteristics

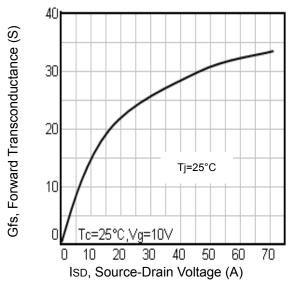
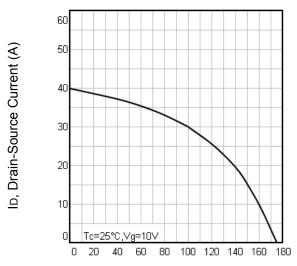


Fig5. Typical Forward Transconductance Vs. Drain Current



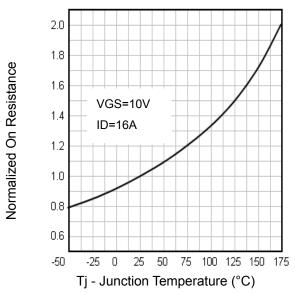


Fig4. Normalized On-Resistance Vs. Temperature

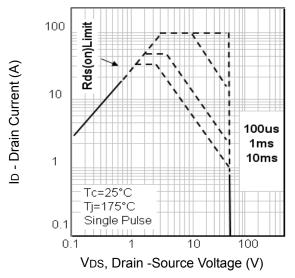


Fig6. Maximum Safe Operating Area



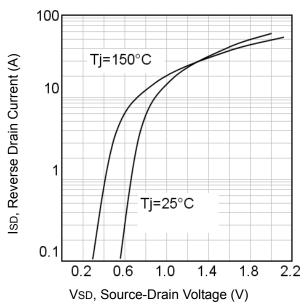


Fig7. Typical Source-Drain Diode Forward Voltage

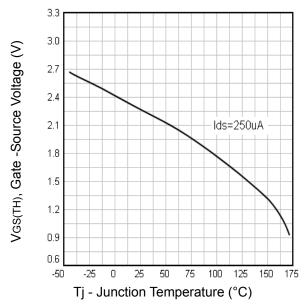


Fig9. Threshold Voltage Vs. Temperature

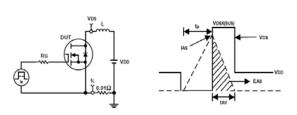


Fig11. Unclamped Inductive Test Circuit and waveforms

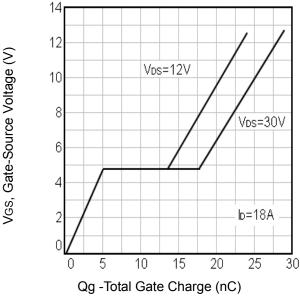


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

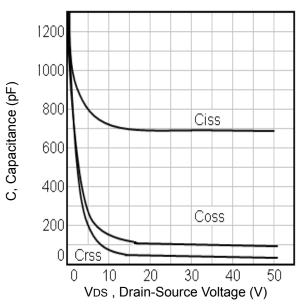


Fig10. Typical Capacitance Vs.Drain-Source Voltage

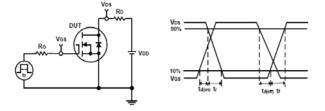


Fig12. Switching Time Test Circuit and waveforms



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