

General Description

The WSF30100D is the highest performance trench N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSF30100D meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

Advanced high cell density Trench technology Super Low Gate Charge Excellent CdV/dt effect decline 100% EAS Guaranteed Green Device Available

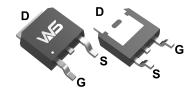
Product Summery

BVDSS	RDSON	ID
30V	$3.6 m\Omega$	100A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application

TO-252 Pin Configuration





Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter		Max.	Units
VDSS	Drain-Source Voltage		30	V
VGSS	Gate-Source Voltage		±20	V
ID	Continuous Drain Current, Vos @ 10V	C=25°C	100	Α
	Continuous Drain Current, Vos @ 10V	C=100°C	59	А
IDM	Pulsed Drain Current note1		360	А
EAS	Single Pulsed Avalanche Energy note2		95	mJ
IAS	Avalanche Current		19.5	А
Po	Total Power Dissipation ₄	C=25°C	68	W
R₀JA	Thermal Resistance Junction-ambient 1 (S	Steady State)	62	°C/W
	Thermal Resistance Junction-Ambient 1 (t	: ≤10s)	25	°C/W
R0JC	Thermal Resistance, Junction to Case		2.2	°C/W
TJ, TSTG	Operating and Storage Temperature Range		-55 to +175	${\mathbb C}$



Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	30	32	ı	V
△BVDSS/△TJ	BVDSS Temperature Coefficient	Reference to 25°C, ID=1mA		0.028		V/°C
VGS(th)	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =250µA	1.0	1.6	2.5	٧
RDS(on)	Static Drain-Source on-Resistance note3	Vgs =10V, ID =30A	-	3.6	4.5	mΩ
RDS(on)	Static Drain-Source on-Resistance note3	V _{GS} =4.5V, I _D =20A	ı	6.7	9.5	mΩ
IDSS	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} = 0V,	1	-	1.0	μΑ
IGSS	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	1	-	±100	nA
Ciss	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f = 1.0MHz	1	2100	ı	pF
Coss	Output Capacitance		-	326	1	pF
Crss	Reverse Transfer Capacitance		1	282	ı	pF
Qg	Total Gate Charge	VDS =15V, ID =30A, VGS =10V	1	45	ı	nC
Qgs	Gate-Source Charge		-	3	-	nC
Qgd	Gate-Drain("Miller") Charge		-	15	-	nC
td(on)	Turn-on Delay Time	V _{DS} =15V, I _D =30A, R _{GEN} =3Ω, V _{GS} =10V	-	21	-	ns
tr	Turn-on Rise Time		-	32	-	ns
td(off)	Turn-off Delay Time		-	59	-	ns
tr	Turn-off Fall Time		-	34	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	90	Α
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	360	Α
VSD	Drain to Source Diode Forward Voltage	Vgs = 0V, Is=30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	L_20A d1/d4_400A/:	-	15	-	ns
Qrr	Body Diode Reverse Recovery Charge	I⊧=20A,dI/dt=100A/μs	-	4	-	nC

Notes:

- 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- 2 $\scriptstyle \times$ The test condition is, VDD =15V, VG =10V, RG =25 Ω , L=0.5mH, IAS =19.5A
- 3 The data tested by pulsed Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%
- 4. The power dissipation is limited by 150 $^{\circ}\mathrm{C}$ junction temperature



Typical Characteristics

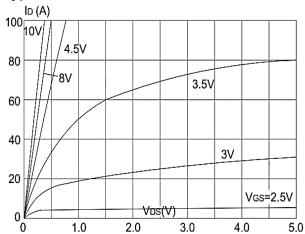


Figure1: Output Characteristics

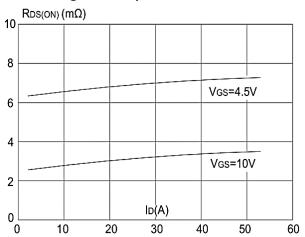


Figure 3:On-resistance vs. Drain Current

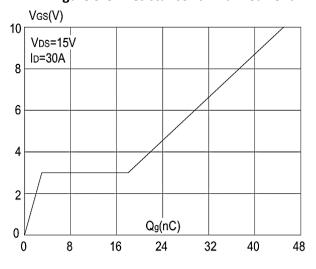


Figure 5: Gate Charge Characteristics

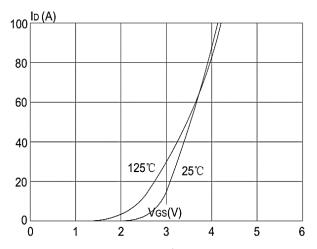


Figure 2: Typical Transfer Characteristics

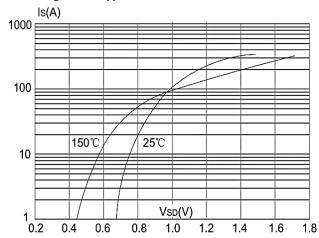


Figure 4: Body Diode Characteristics

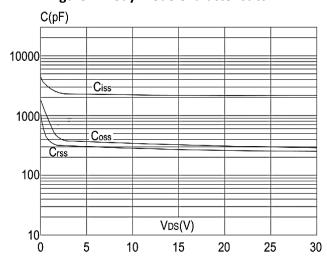


Figure 6: Capacitance Characteristics



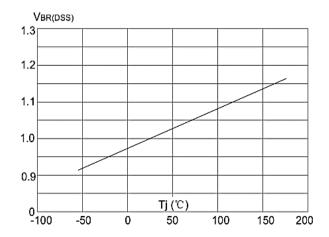


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

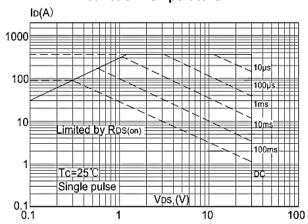


Figure 9: Maximum Safe Operating Area vs. Case Temperature

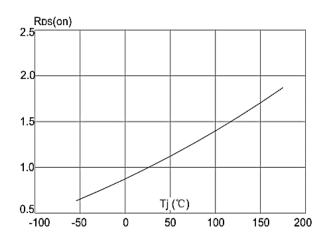


Figure 8: Normalized on Resistance vs Junction Temperature

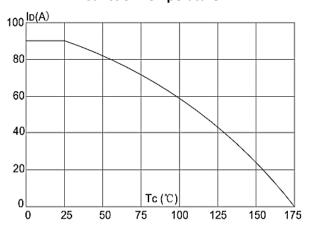


Figure 10: Maximum Continuous Drain Current

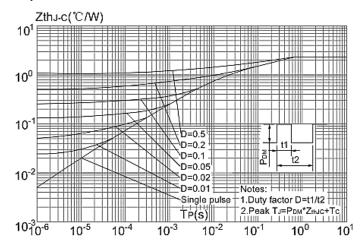


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



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