

### Description

The WSF3055 uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application

### Features

- 1,100 % U IS + Rg Tested
- 2,Re liable and Rugged
- 3,Lead Free Available (RoHS Compliant)

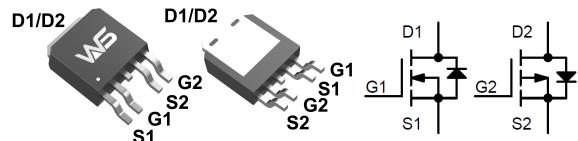
### Product Summary

VDS	RDS(ON)	ID
30	15mΩ	24A
-30	11mΩ	-19.8A

### Application

- Motor Control.
- Portable equipment application.
- Synchronous Rectification.

### TO-252 Pin Configuration



### Absolute Maximum Ratings (T= 25°C unless otherwise noted)

Symbol	Parameter	N Channel	P Channel	Unit
V <sub>DSS</sub>	Drain-Source Voltage	30	-30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	±20	
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> =25°C	24	A
		T <sub>c</sub> =100°C	15	
P <sub>D</sub>	Maximum Power Dissipation	T <sub>c</sub> =25°C	18.9	W
		T <sub>c</sub> =100°C	7.6	
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> =25°C	9	A
		T <sub>A</sub> =70°C	7.3	
I <sub>DM</sub> <sup>a</sup>	Pulsed Drain Current	T <sub>A</sub> =25°C	36	A
P <sub>D</sub>	Maximum Power Dissipation	T <sub>A</sub> =25°C	2.78	W
		T <sub>A</sub> =70°C	1.78	
I <sub>S</sub>	Diode Continuous Forward Current	T <sub>c</sub> =25°C	3	A
T <sub>J</sub>	Maximum Junction Temperature		150	°C
T <sub>STG</sub>	Storage Temperature Range		-55 to 150	
R <sub>θJC</sub>	Thermal Resistance-Junction to Case	Steady State	6.6	°C/W
R <sub>θJA</sub> <sup>b</sup>	Thermal Resistance-Junction to Ambient	t ≤ 10s	45	°C/W
		Steady State	95	°C/W
I <sub>AS</sub> <sup>c</sup>	Avalanche Current, Single pulse	L=0.1mH	13	A
E <sub>AS</sub> <sup>c</sup>	Avalanche Energy, Single pulse	L=0.1mH	8.5	mJ

Note a : Pulse width limited by max. junction temperature.

Note b : Surface mounted on 1in<sup>2</sup> pad area.

Note c : UIS tested and pulse width limited by maximum junction temperature 150°C(initial temperature T<sub>j</sub>=25°C)

**N Channel Electrical Characteristics (T= 25°C unless otherwise noted)**

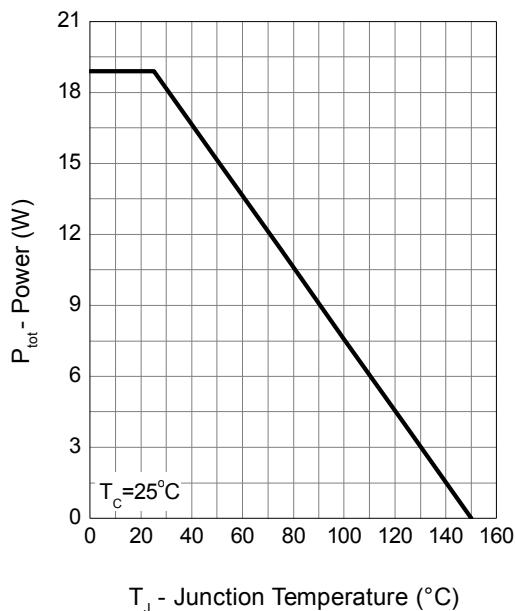
Symbol	Parameter	Test Conditions	N Channel			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V T <sub>J</sub> =85°C	-	-	50	A
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	1.3	1.8	2.3	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
R <sub>D(S(ON))d</sub>	Drain-Source On-state Resistance	V <sub>GS</sub> =10V, I <sub>DS</sub> =9A	-	15	20	mΩ
		V <sub>GS</sub> =4.5V, I <sub>DS</sub> =8A	-	18	23	mΩ
<b>Diode Characteristics</b>						
V <sub>SD</sub> <sup>d</sup>	Diode Forward Voltage	I <sub>SD</sub> =1A, V <sub>GS</sub> =0V	0.3	0.4	0.55	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> =4.0A, dI <sub>SD</sub> /dt=100A/μs	-	11	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	3.5	-	nC
<b>Dynamic Characteristics</b> e						
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	-	3.3	-	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, Frequency=1.0MHz	-	395	514	pF
C <sub>oss</sub>	Output Capacitance		-	105	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	42	-	
t <sub>d(ON)</sub>	Turn-on Delay Time	V <sub>DD</sub> =15V, R <sub>L</sub> =15Ω, I <sub>DS</sub> =1A, V <sub>GEN</sub> =10V, R <sub>G</sub> =6Ω	-	5.5	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	10.5	-	
t <sub>d(OFF)</sub>	Turn-off Delay Time		-	15	-	
t <sub>f</sub>	Turn-off Fall Time		-	3.7	-	
<b>Gate Charge Characteristics</b> e						
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>DS</sub> =4A	-	4	-	nC
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>DS</sub> =4.0A	-	8.3	12.5	
Q <sub>gs</sub>	Gate-Source Charge		-	1.1	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	1.8	-	

Note d : Pulse test ; pulse width: $\leqslant 300\mu s$ , duty cycle $\leqslant 2\%$ .

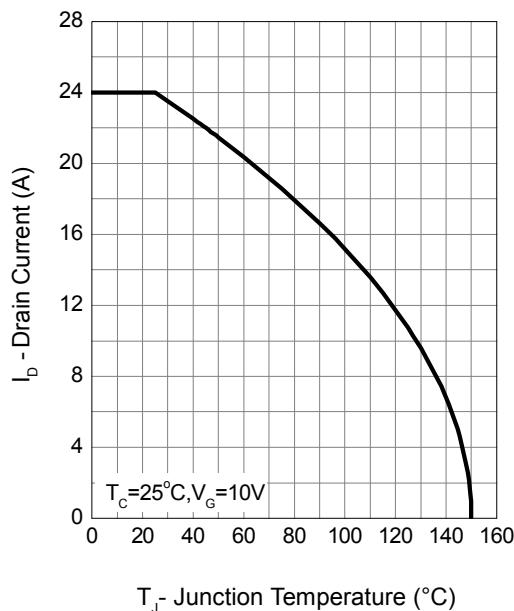
Note e : Guaranteed by design, not subject to production testing.

## N Channel Typical Operating Characteristics

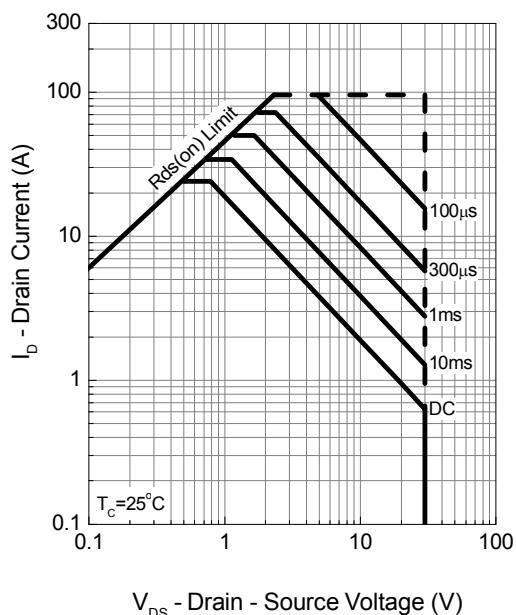
**Power Dissipation**



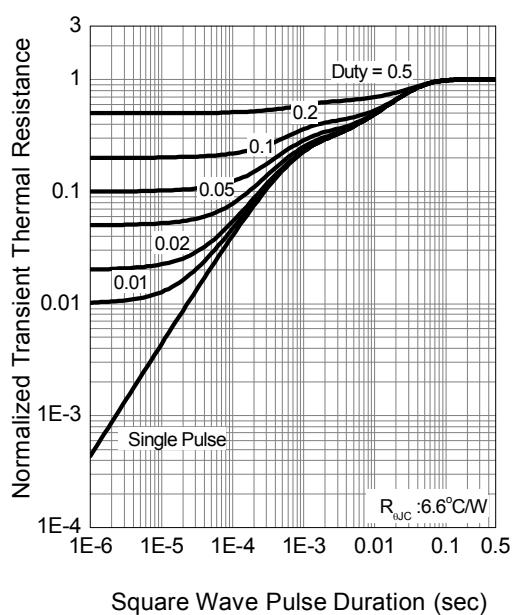
**Drain Current**



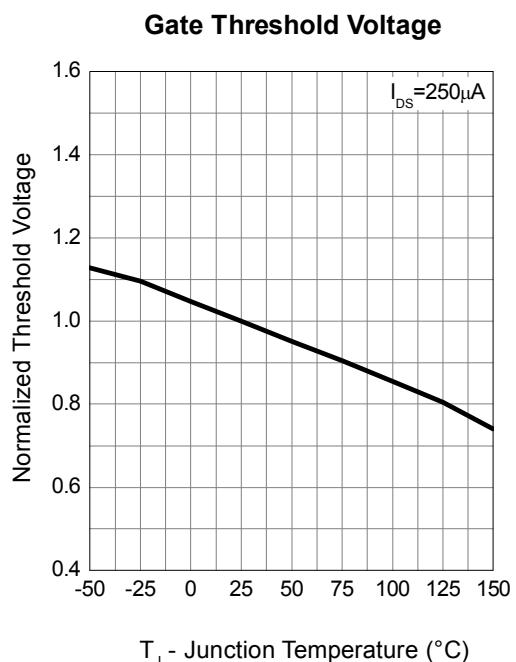
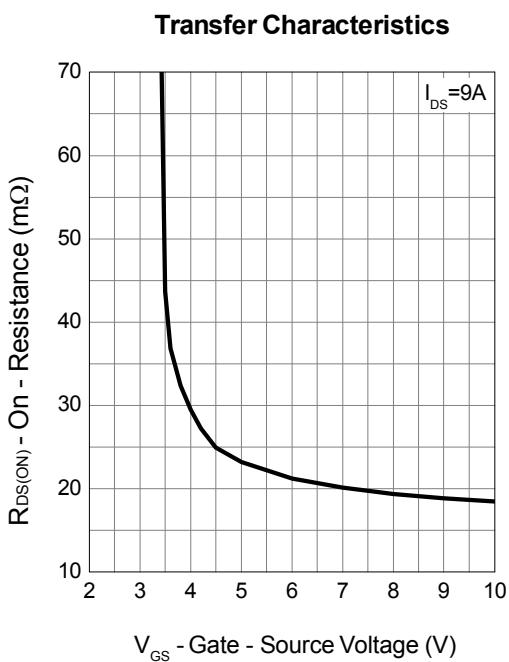
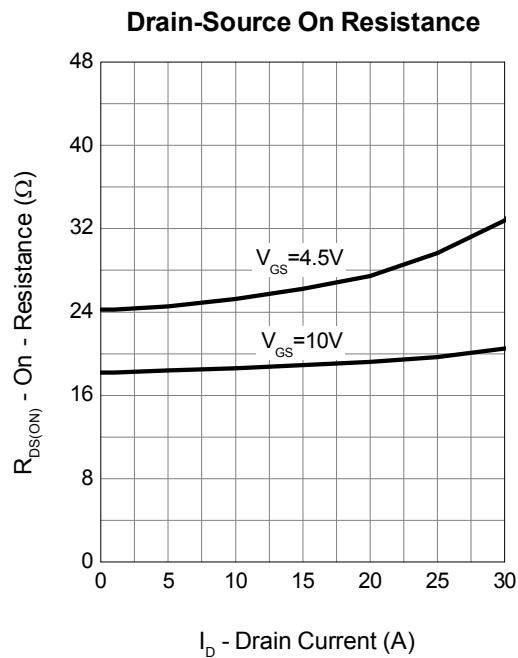
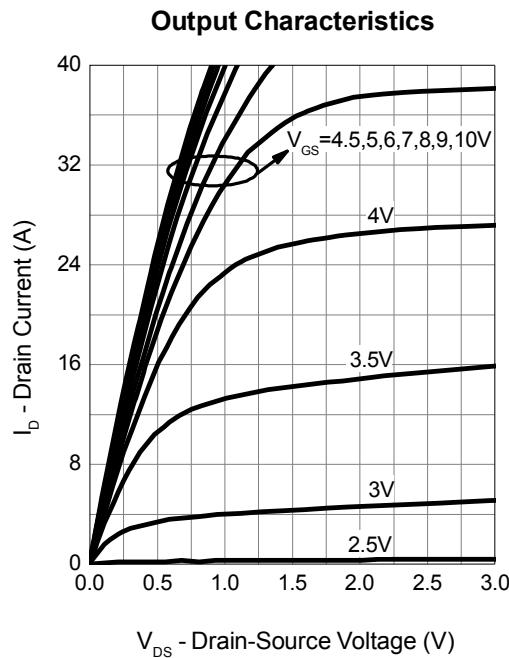
**Safe Operation Area**



**Thermal Transient Impedance**

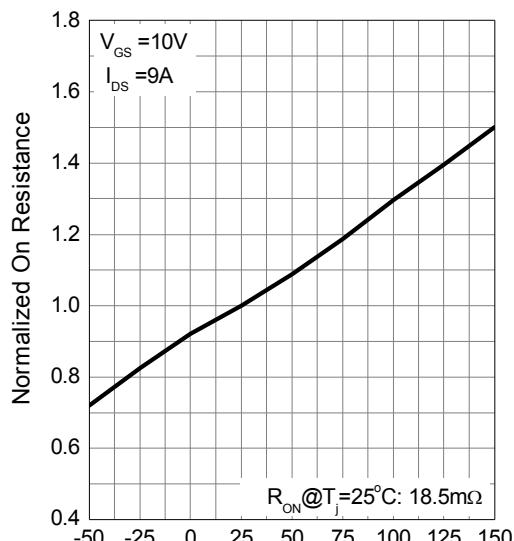


## **N Channel Typical Operating Characteristics (Cont.)**



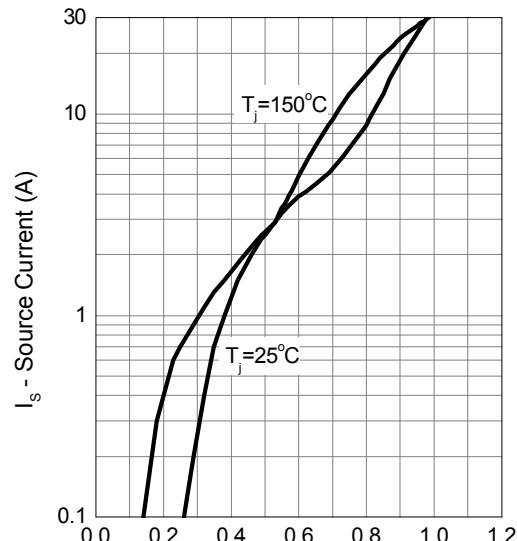
## N Channel Typical Operating Characteristics (Cont.)

**Drain-Source On Resistance**



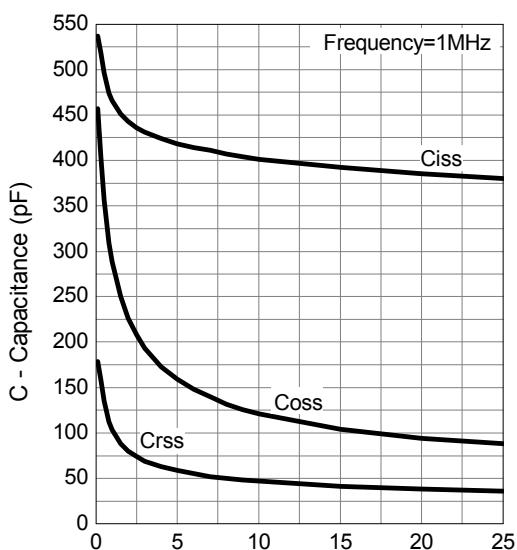
$T_j$  - Junction Temperature ( $^\circ C$ )

**Source-Drain Diode Forward**



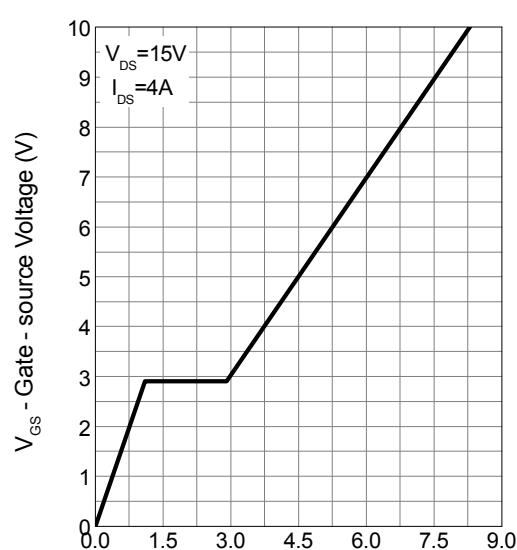
$V_{SD}$  - Source - Drain Voltage (V)

**Capacitance**



$V_{DS}$  - Drain - Source Voltage (V)

**Gate Charge**



$Q_G$  - Gate Charge (nC)

**P Channel Electrical Characteristics (T= 25°C unless otherwise noted)**

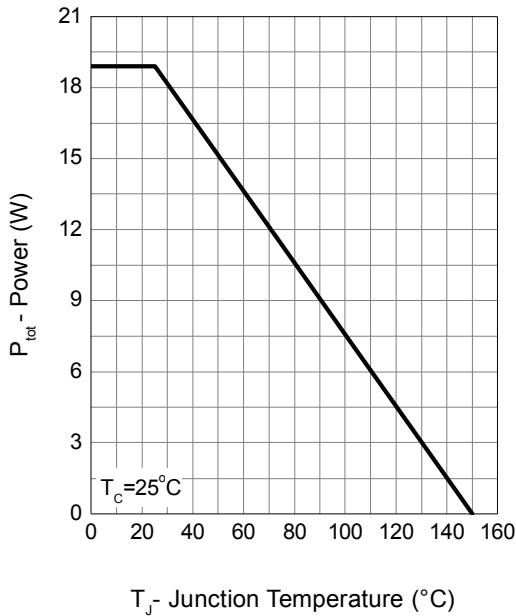
Symbol	Parameter	Test Conditions	P Channel			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =-250μA	-30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V	-	-	-1	A
			T <sub>J</sub> =85°C	-	-30	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =-250μA	-1.3	-1.8	-2.3	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
R <sub>DS(ON)<sup>d</sup></sub>	Drain-Source On-state Resistance	V <sub>GS</sub> =-10V, I <sub>DS</sub> =-7A	-	11	14	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>DS</sub> =-4A	-	15	20	mΩ
<b>Diode Characteristics</b>						
V <sub>SD<sup>d</sup></sub>	Diode Forward Voltage	I <sub>SD</sub> =-1A, V <sub>GS</sub> =0V	-	-0.75	-1	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> =-7.0A, dI <sub>SD</sub> /dt=100A/μs	-	17	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	9	-	nC
<b>Dynamic Characteristics e</b>						
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	-	12	-	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, Frequency=1.0MHz	-	750	975	pF
C <sub>oss</sub>	Output Capacitance		-	142	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	102	-	
t <sub>d(ON)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-15V, R <sub>L</sub> =15Ω, I <sub>DS</sub> =-1A, V <sub>GEN</sub> =-10V, R <sub>G</sub> =6Ω	-	9	17	ns
t <sub>r</sub>	Turn-on Rise Time		-	11	20	
t <sub>d(OFF)</sub>	Turn-off Delay Time		-	55	99	
t <sub>f</sub>	Turn-off Fall Time		-	34	62	
<b>Gate Charge Characteristics e</b>						
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>DS</sub> =-7.0A	-	8	-	nC
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, I <sub>DS</sub> =-7.0A	-	17	24	
Q <sub>gth</sub>	Threshold Gate Charge		-	1	-	
Q <sub>gs</sub>	Gate-Source Charge		-	2	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	4	-	

Note d : Pulse test ; pulse width ≤300μs, duty cycle≤2%.

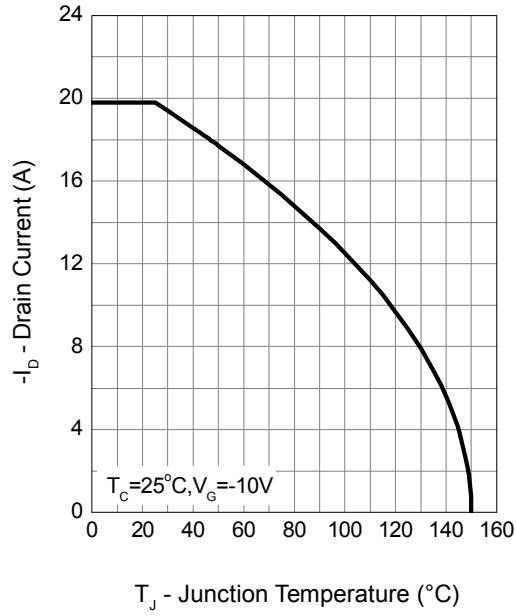
Note e : Guaranteed by design, not subject to production testing.

## P Channel Typical Operating Characteristics

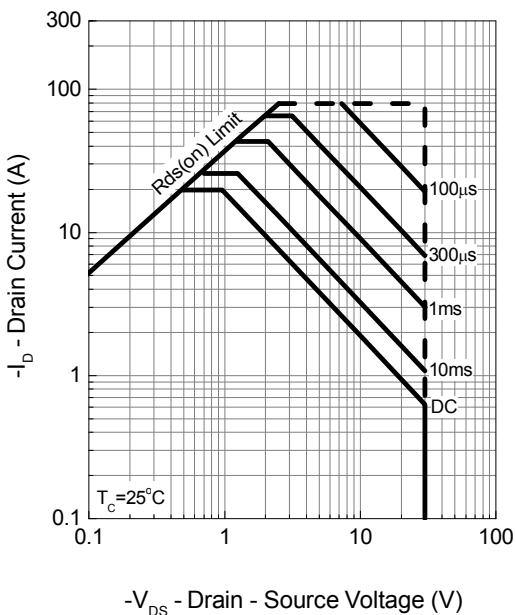
**Power Dissipation**



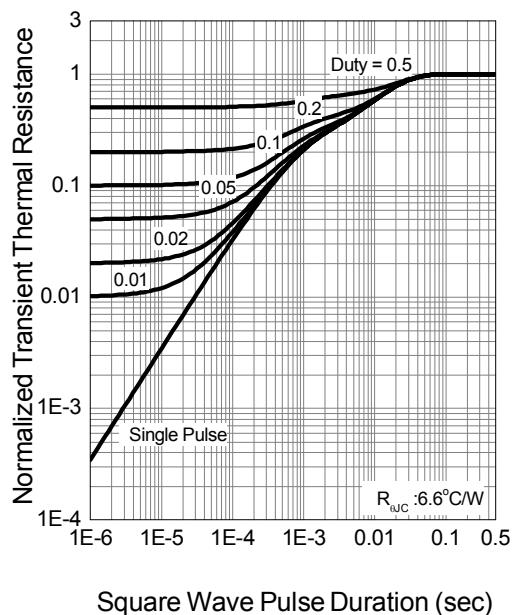
**Drain Current**



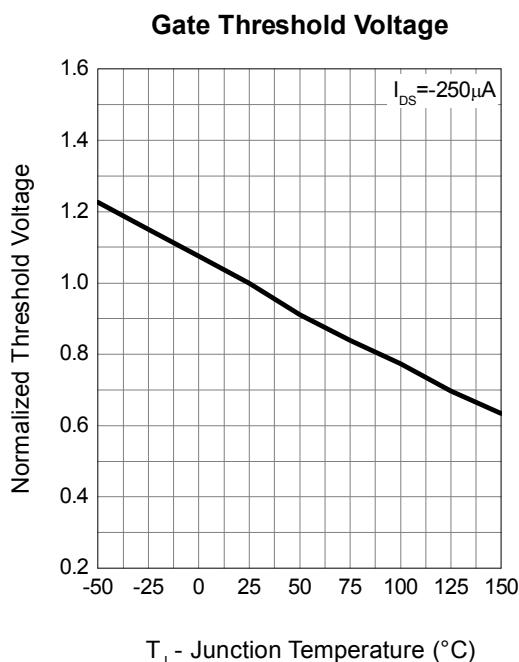
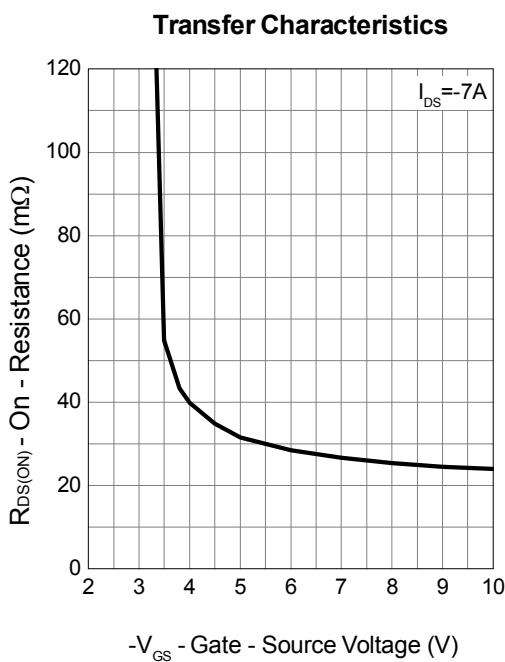
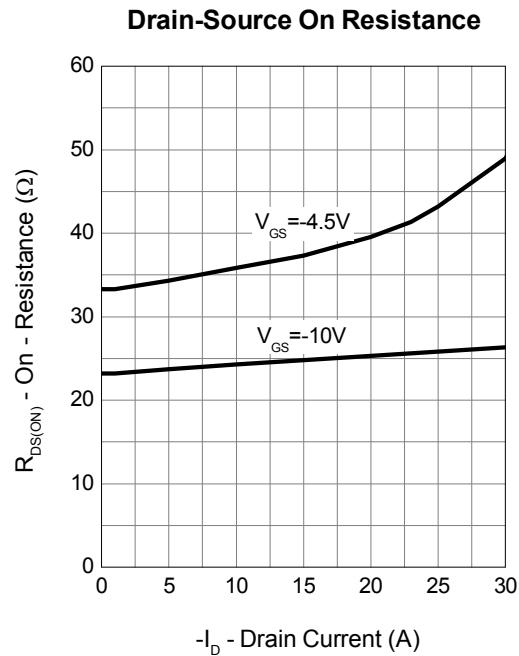
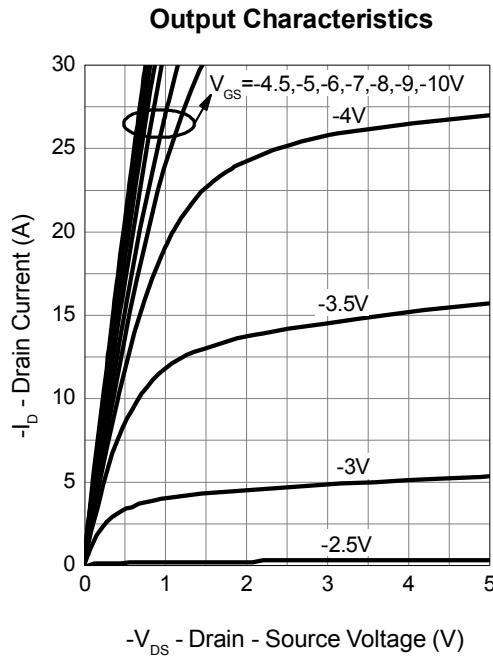
**Safe Operation Area**



**Thermal Transient Impedance**

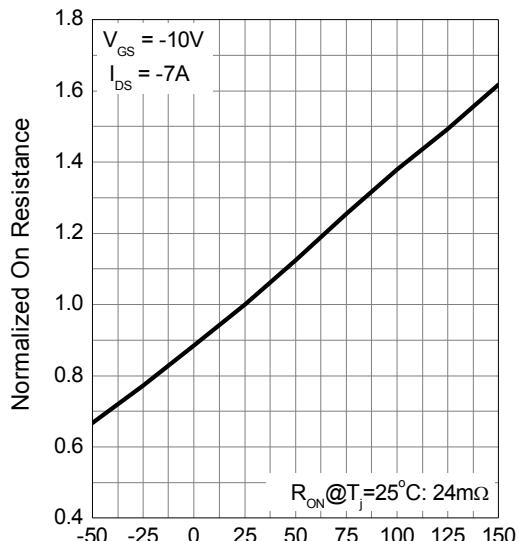


## P Channel Typical Operating Characteristics (Cont.)



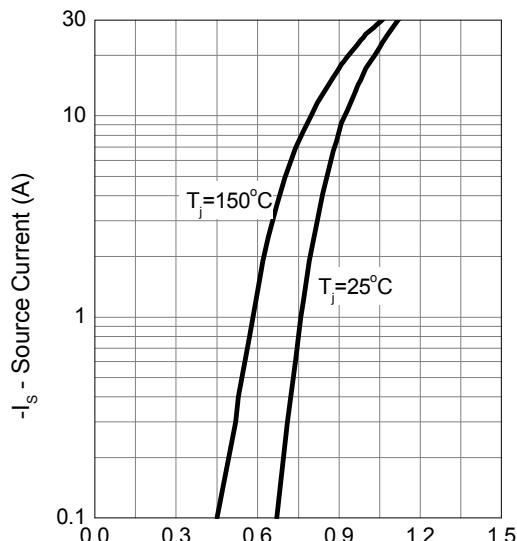
## P Channel Typical Operating Characteristics (Cont.)

**Drain-Source On Resistance**



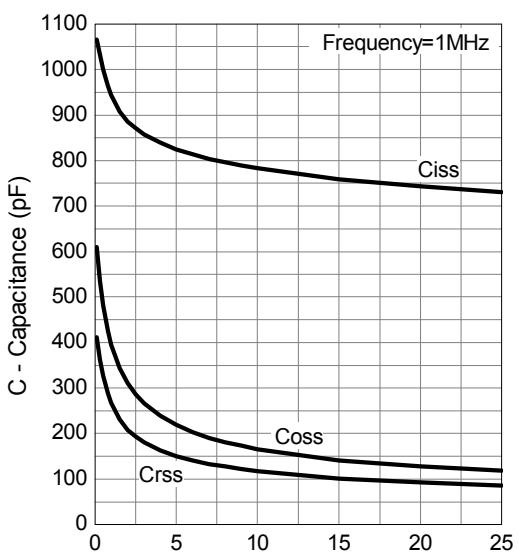
T<sub>j</sub> - Junction Temperature (°C)

**Source-Drain Diode Forward**



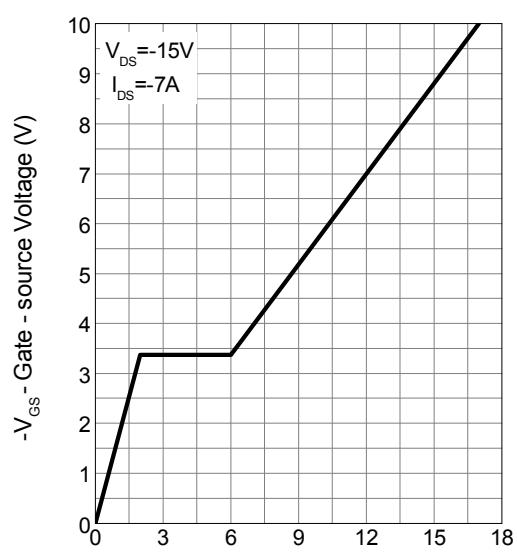
-V<sub>SD</sub> - Source - Drain Voltage (V)

**Capacitance**



-V<sub>DS</sub> - Drain - Source Voltage (V)

**Gate Charge**



Q<sub>G</sub> - Gate Charge (nC)



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