**P-Ch MOSFET** 

# **General Description**

The WSF35P06 is the highest performance trench P-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF35P06 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

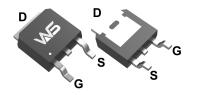
# **Product Summery**

BVDSS	RDSON	ID
-60V	80mΩ	-13.5A

# **Applications**

- Brushless motor
- Load switch
- Uninterruptible power supply

# **TO-252 Pin Configuration**





# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, -V <sub>GS</sub> @ -10V <sup>1</sup>	-13.5	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, -V <sub>GS</sub> @ -10V <sup>1</sup> -8.3		А
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup> -26		А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	29.8	mJ
I <sub>AS</sub>	Avalanche Current	-24.4	Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	31.3	W
T <sub>STG</sub>	Storage Temperature Range -55 to 150		$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

# **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient 1		62	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	Resistance Junction-Case <sup>1</sup> 4		°C/W



# P-Channel Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-60			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25℃ , I <sub>D</sub> =-1mA		-0.012		V/°C
D	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}$ =-10V , $I_D$ =-3A		80	90	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-2A		100	150	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> . In =-250uA	-1.2	-1.75	-2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> -V <sub>DS</sub> , I <sub>D</sub> 250uA		4.32		mV/℃
	Drain-Source Leakage Current	$V_{DS}$ =-32V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			1	
I <sub>DSS</sub>	Diain-Source Leakage Current	V <sub>DS</sub> =-32V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	– uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20 V$ , $V_{DS}$ = $0 V$			±100	nA
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		13	16	Ω
Qg	Total Gate Charge (-4.5V)	V <sub>DS</sub> =-20V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A		12.4		
$Q_{gs}$	Gate-Source Charge			2.2		nC
$Q_gd$	Gate-Drain Charge			6.3		
$T_{d(on)}$	Turn-On Delay Time			9.2		
T <sub>r</sub>	Rise Time	$V_{DD}$ =-15V , $V_{GS}$ =-10V , $R_{G}$ =3.3 $\Omega$ ,		20.1		ns
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =-1A		46.7		115
T <sub>f</sub>	Fall Time			9.4		
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		1137		
C <sub>oss</sub>	Output Capacitance			76		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			50		

#### **Guaranteed Avalanche Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy <sup>5</sup>	V <sub>DD</sub> =-25V , L=0.1mH , I <sub>AS</sub> =-24A	20			mJ

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-13	Α
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃			-1.2	V

#### Note:

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%
- 3. The EAS data shows Max. rating . The test condition is V DD =-25V,V GS =-10V,L=0.1mH,IAS =-24A
- 4. The power dissipation is limited by 150 ℃ junction temperature
- 5. The data is theoretically the same as I D and I DM, in real applications, should be limited by total power dissipation.



# **P-Channel Typical Characteristics**

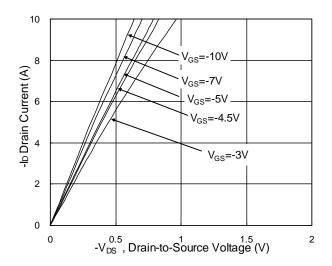


Fig.1 Typical Output Characteristics

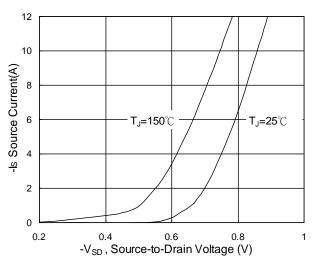


Fig.3 Forward Characteristics of Reverse

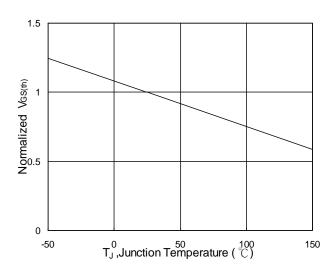


Fig.5 Normalized V<sub>GS(th)</sub> v.s T<sub>J</sub>

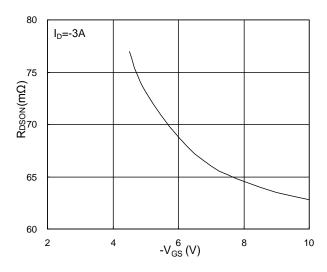


Fig.2 On-Resistance v.s Gate-Source

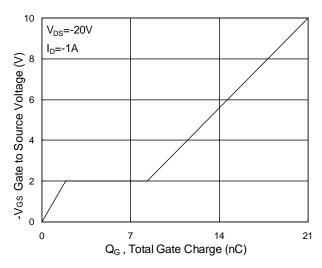


Fig.4 Gate-Charge Characteristics

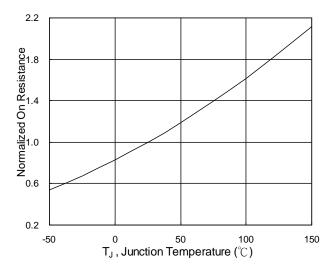
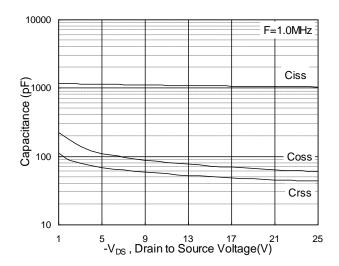


Fig.6 Normalized R<sub>DSON</sub> v.s T<sub>J</sub>





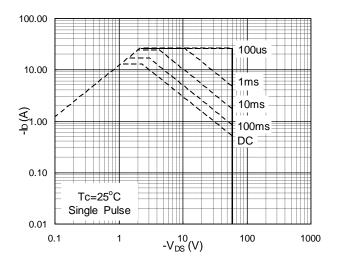


Fig.7 Capacitance

Fig.8 Safe Operating Area

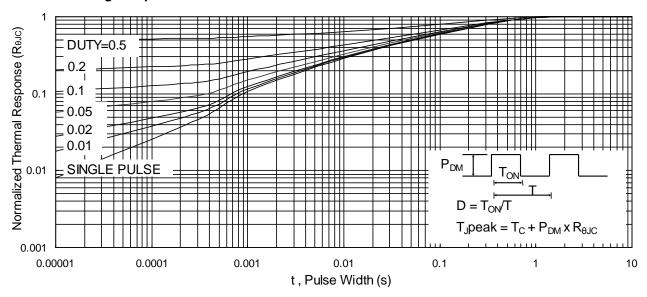
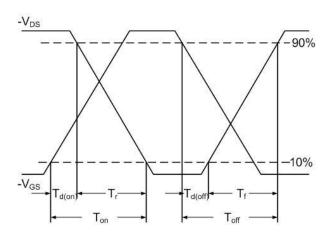
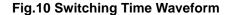


Fig.9 Normalized Maximum Transient Thermal Impedance





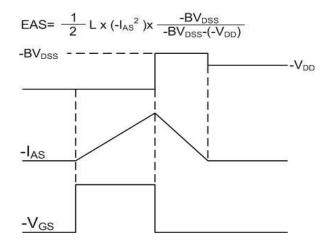


Fig.11 Unclamped Inductive Switching Waveform



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