General Description

The WSF40130is the highest performance trench MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF40130 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

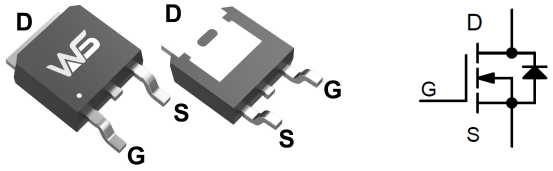
Product Summery

BVDSS	RDSON	ID
40V	2.4mΩ	120A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application

TO-252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V	120	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V	79	A
I_{DM}	Pulsed Drain Current[a]	360	A
EAS	Single Pulse Avalanche Energy[b]	400	mJ
I_{AS}	Avalanche Current	40	A
P_D@T_c=25℃	Total Power Dissipation	125	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	62	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	1.0	℃/W

Notes：

* Avalanche single pulse test and avalanche period time tav≦100μs, duty<1% . **
Avalanche test condition: T_J=25°C, L=0.5mH, I_{AS}=40A, V_{DD}=20V, and V_{GS} =10V. ***
Current limited by bond wire.

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	40	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.043	---	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=20A	---	2.4	3.5	mΩ
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=4.5V , I_D=10A	---	3.2	5.0	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.3	1.8	2.6	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-6.94	---	mV/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=32V , V_{GS}=0V , T_J=25℃	---	---	2	uA
		V_{DS}=32V , V_{GS}=0V , T_J=55℃	---	---	10	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=5V , I_D=20A	40	---	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	1.5	2	Ω
Q_g	Total Gate Charge (10V)	V_{DS}=20V , V_{GS}=10V , I_D=40A	---	59	---	nC
Q_{gs}	Gate-Source Charge		---	9.8	---	
Q_{gd}	Gate-Drain Charge		---	9.5	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=20V , V_{GEN}=10V , R_G=1Ω, I_D=1A ,RL=15Ω.	---	12	---	ns
T_r	Rise Time		---	6	---	
$T_{d(off)}$	Turn-Off Delay Time		---	38	---	
T_f	Fall Time		---	9	---	
C_{iss}	Input Capacitance	V_{DS}=20V , V_{GS}=0V , f=1MHz	---	3495	---	pF
C_{oss}	Output Capacitance		---	1045	---	
C_{rss}	Reverse Transfer Capacitance		---	62	---	

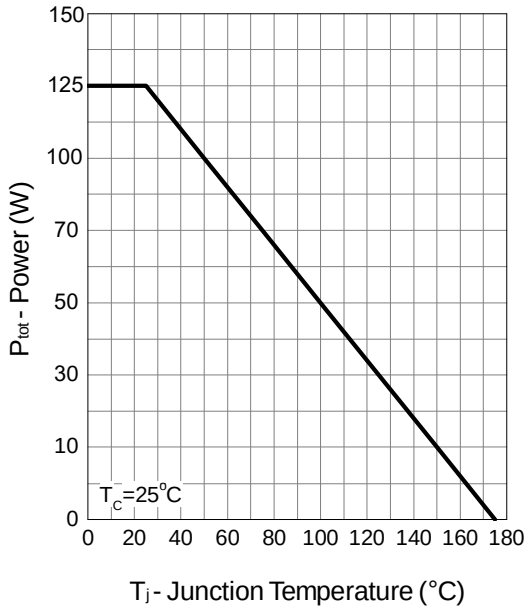
Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,6]	V_G=V_D=0V , Force Current	---	---	55	A
I_{SM}	Pulsed Source Current[2,6]		---	---	160	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=20A , T_J=25℃	---	0.8	1.2	V

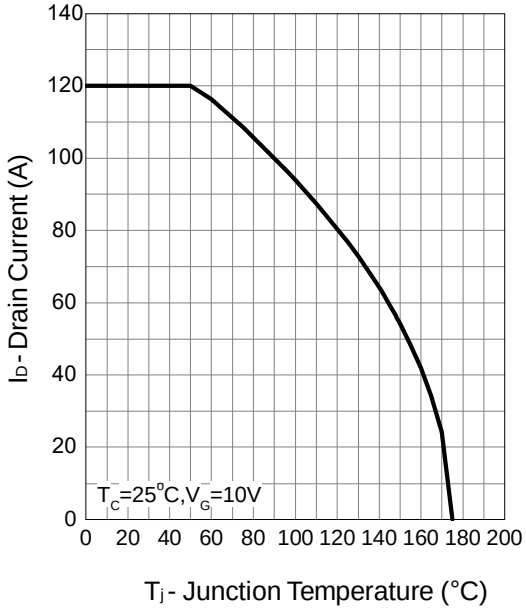
Note :
1. The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper,t<10sec .
2. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3. The EAS data shows Max. rating . The test condition is V_{DD}=20V,V_{GS}=10V,L=0.5mH,I_{AS}=40A
4. The power dissipation is limited by 150℃ junction temperature
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
7. Package limitation current is 60A.

Typical Characteristics

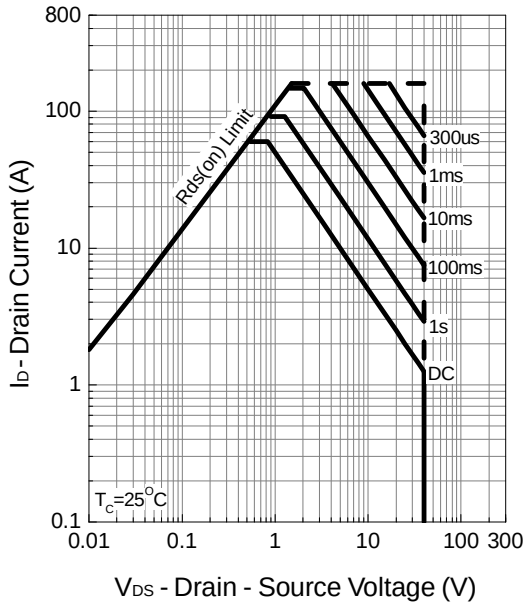
Power Dissipation



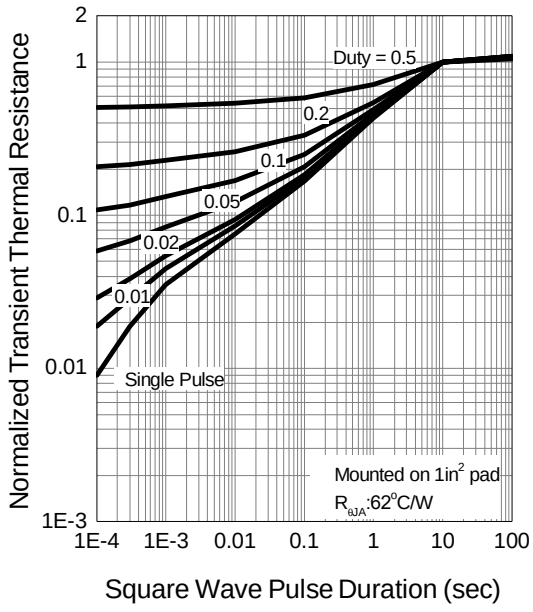
Drain Current



Safe Operation Area

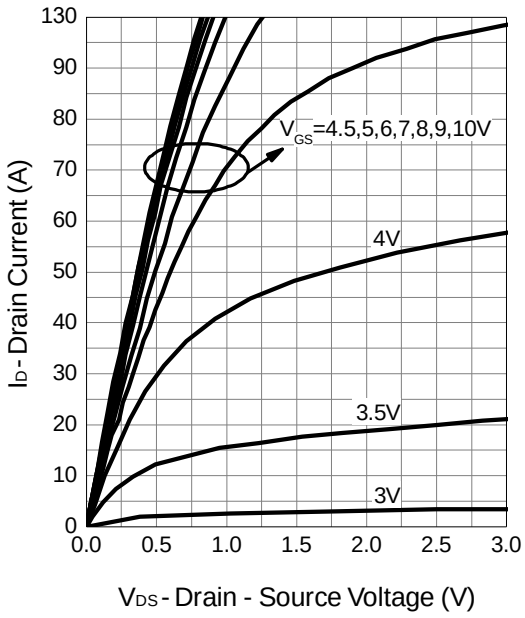


Thermal Transient Impedance

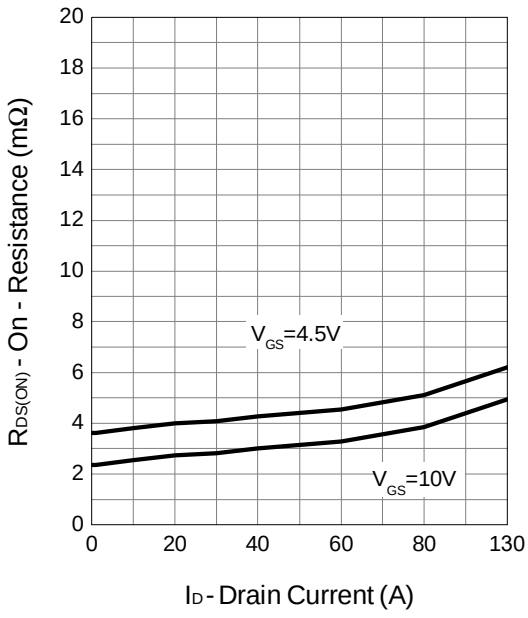


Typical Characteristics

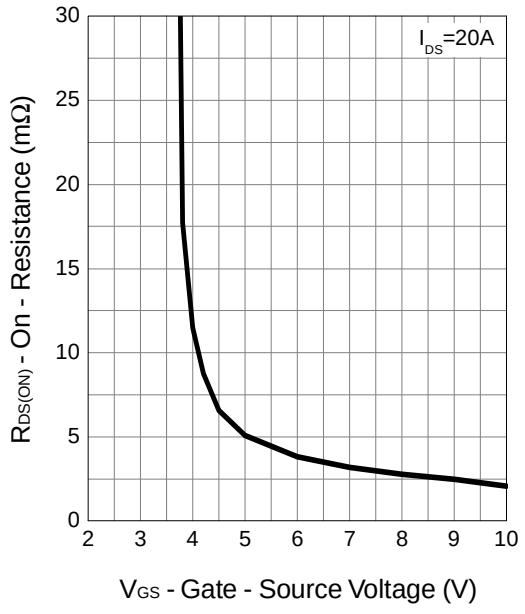
Output Characteristics



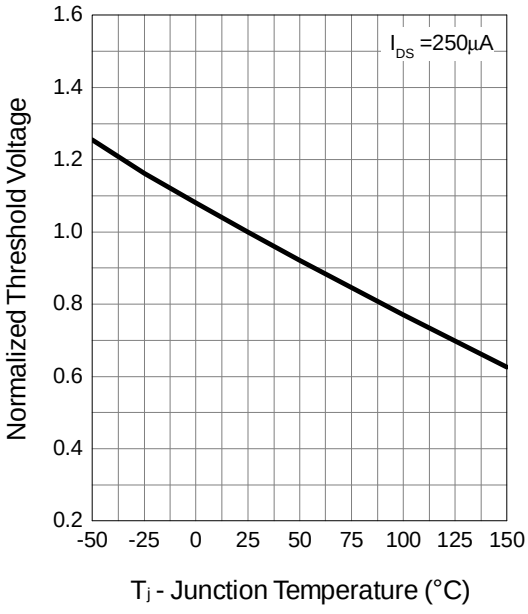
Drain-Source On Resistance



Gate-Source On Resistance

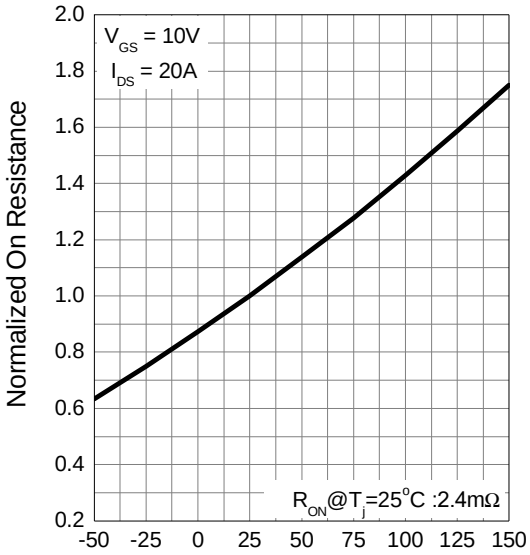


Gate Threshold Voltage

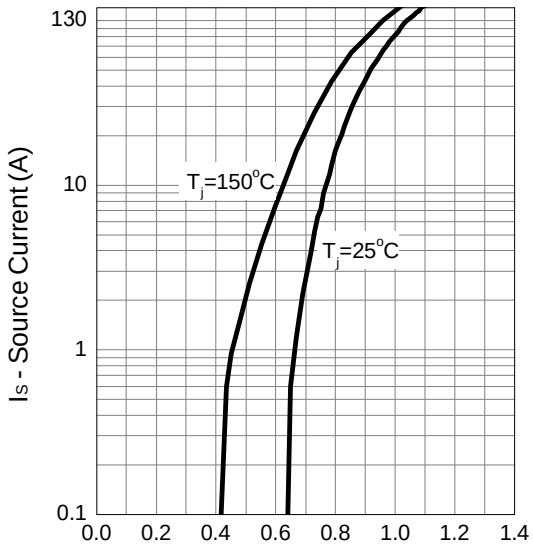


Typical Characteristics

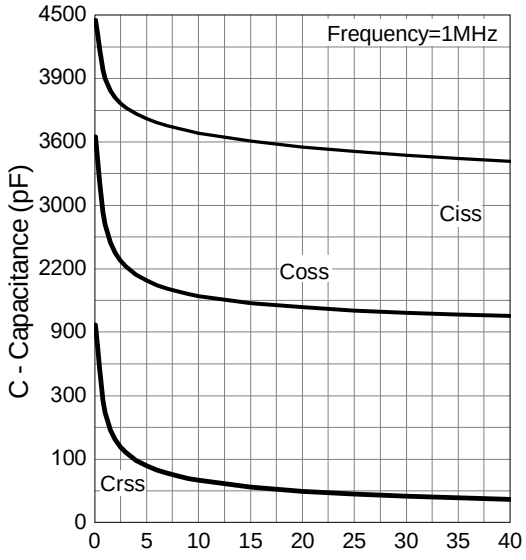
Drain-Source On Resistance



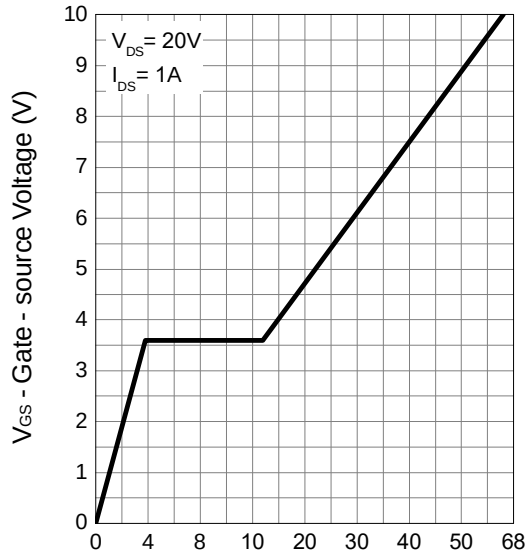
Source-Drain Diode Forward



Capacitance



Gate Charge



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