

### General Description

The WSF40P06 is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent R<sub>DS(on)</sub> and gate charge for most of the synchronous buck converter applications.

The WSF40P06 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

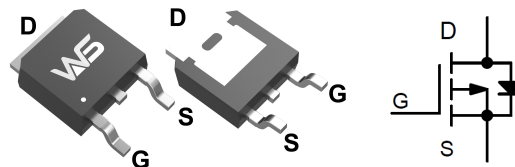
### Product Summary

BVDSS	R <sub>DS(on)</sub>	ID
-60V	62mΩ	-17A

### Applications

- Brushless motor
- Load switch
- Uninterruptible power supply

### TO-252 Pin Configuration



### ABSOLUTE MAXIMUM RATINGS (T<sub>c</sub> = 25 °C Unless Otherwise Noted)

SYMBOL	PARAMETERS/TEST CONDITIONS	LIMITS	UNIT	
V <sub>DS</sub>	Drain - Source Voltage	-60	V	
V <sub>GS</sub>	Gate-Source Voltage	±20		
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> = 25 °C	-17	A
		T <sub>c</sub> = 100 °C	-11	
I <sub>DM</sub>	Pulsed Drain Current <sub>1</sub>	-60		
I <sub>AS</sub>	Avalanche Current	-12		
E <sub>AS</sub>	Avalanche Energy	L = 0.1mH	7.2	mJ
E <sub>AR</sub>	Repetitive Avalanche Energy <sub>2</sub>	L = 0.05mH	3.6	
P <sub>D</sub>	Power Dissipation	T <sub>c</sub> = 25 °C	27	W
		T <sub>c</sub> = 100 °C	8	
T <sub>J</sub> , T <sub>stg</sub>	Operating Junction & Storage Temperature Range	-55 to 150	°C	

### THERMAL RESISTANCE RATINGS

SYMBOL	THERMAL RESISTANCE	MAXIMUM	UNIT
R <sub>θJC</sub>	Junction-to-Case	4.5	°C / W
R <sub>θJA</sub>	Junction-to-Ambient	85	

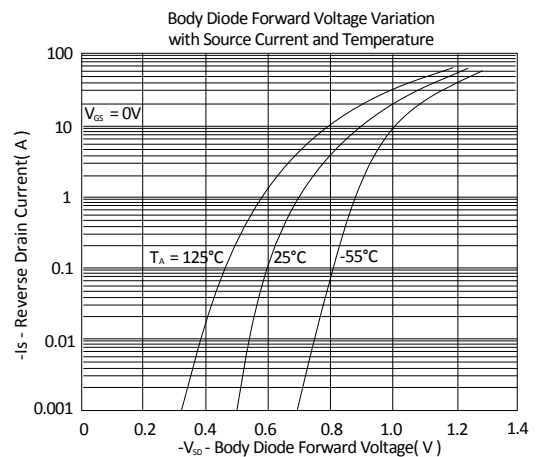
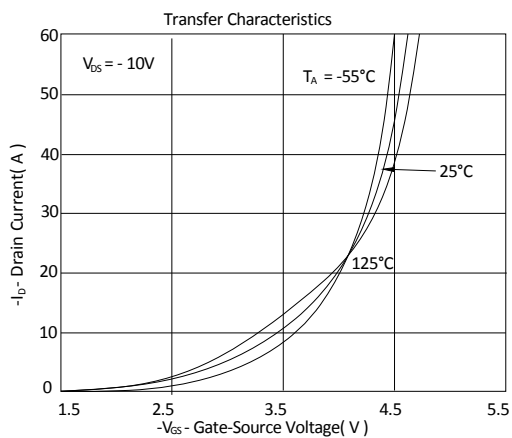
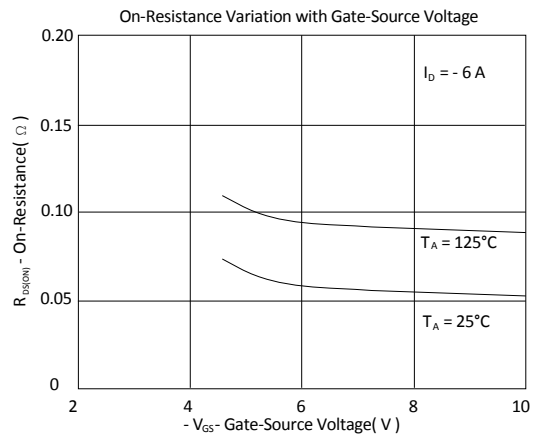
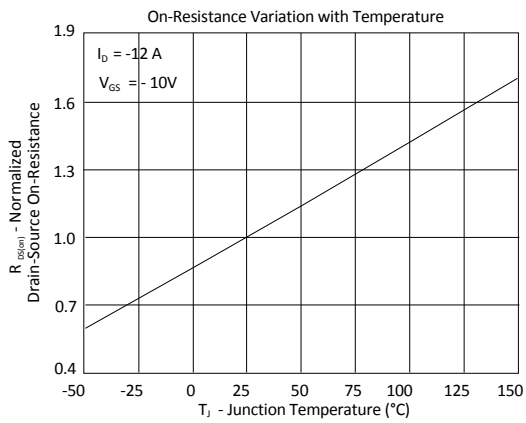
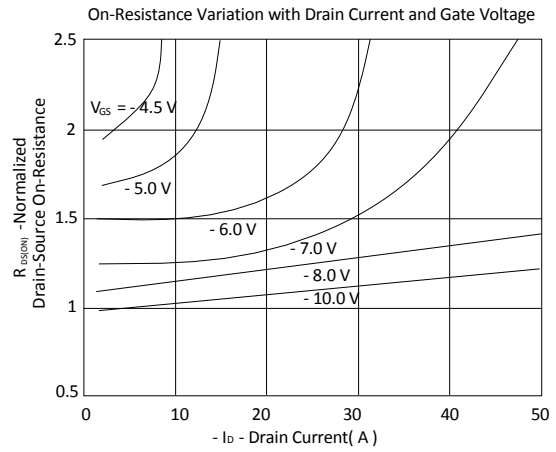
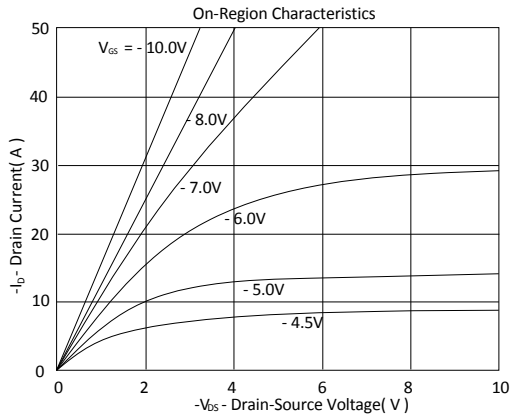
**P-Channel Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**

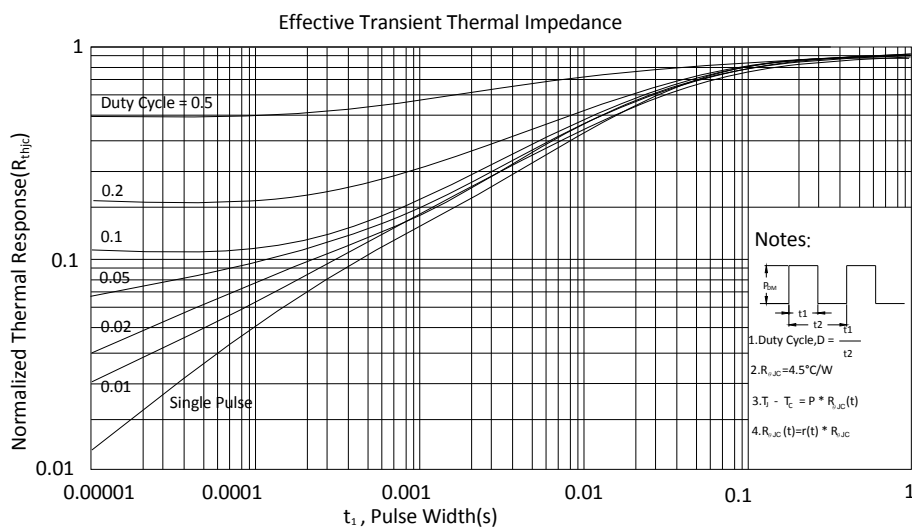
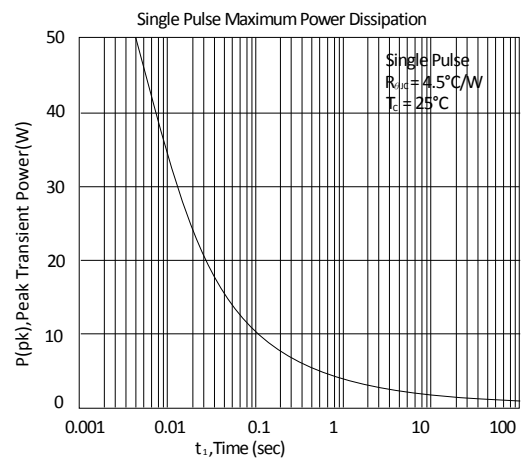
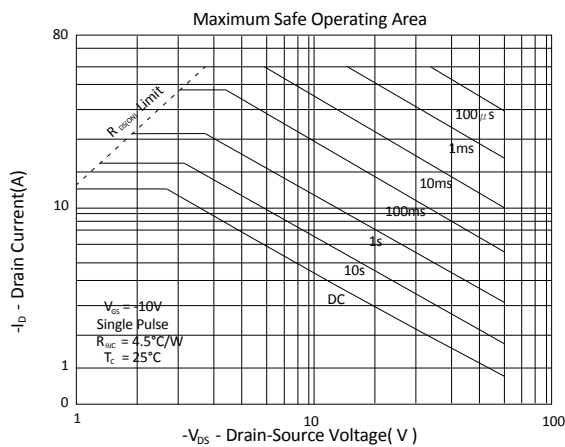
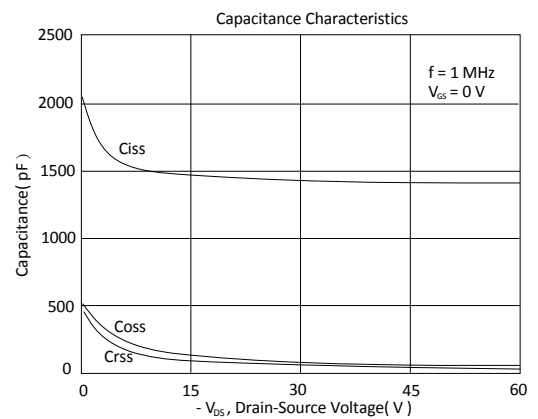
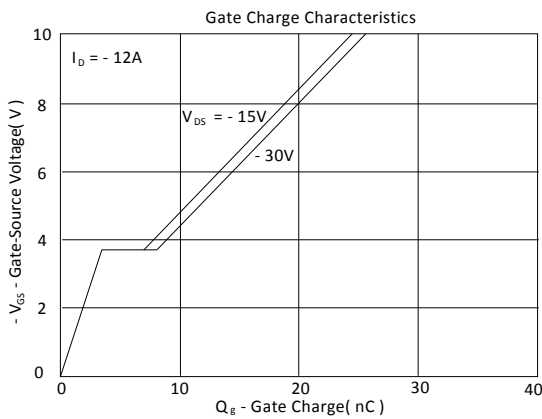
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = -250\mu A$	-60	---	---	V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	-1.8	-3.0	
$I_{GSS}$	Gate-Body Leakage	$V_{DS} = 0V, V_{GS} = \pm 20V$	---	---	$\pm 100$	nA
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = -48V, V_{GS} = 0V$	---	---	-1	$\mu A$
		$V_{DS} = -40V, V_{GS} = 0V, T_J = 125\text{ }^\circ\text{C}$	---	---	-25	
$I_{D(ON)}$	On-State Drain Current <sup>1</sup>	$V_{DS} = -5V, V_{GS} = -4.5V$	-17	---	---	A
$R_{DS(ON)}$	Drain-Source On-State Resistance <sup>1</sup>	$V_{GS} = -10V, I_D = -15A$	---	55	62	m $\Omega$
		$V_{GS} = -4.5V, I_D = -7A$	---	75	90	
$g_{fs}$	Forward Transconductance <sup>1</sup>	$V_{DS} = -5V, I_D = -15A$	---	12	---	S
<b>DYNAMIC</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0V, V_{DS} = -25V, f = 1MHz$	---	1485	---	pF
$C_{oss}$	Output Capacitance		---	93	---	
$C_{rss}$	Reverse Transfer Capacitance		---	81	---	
$R_g$	Gate Resistance	$V_{GS} = 15mV, V_{DS} = 0V, f = 1MHz$	---	7.0	---	$\Omega$
$Q_g$	Total Gate Charge <sup>1,2</sup>	$V_{DS} = -30V, V_{GS} = -10V, I_D = -10A$	---	25.3	---	nC
$Q_{gs}$	Gate-Source Charge <sup>1,2</sup>		---	3.2	---	
$Q_{gd}$	Gate-Drain Charge <sup>1,2</sup>		---	4	---	
$t_{d(on)}$	Turn-On Delay Time <sup>1,2</sup>	$V_{DS} = -10V, I_D = -1A, V_{GS} = -10V, R_{GS} = 6\Omega$	---	12	---	nS
$t_r$	Rise Time <sup>1,2</sup>		---	24	---	
$t_{d(off)}$	Turn-Off Delay Time <sup>1,2</sup>		---	45	---	
$t_f$	Fall Time <sup>1,2</sup>		---	60	---	
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_C = 25\text{ }^\circ\text{C}</math>)</b>						
$I_S$	Continuous Current		---	---	-17	A
$I_{SM}$	Pulsed Current <sup>3</sup>		---	---	-60	
$V_{SD}$	Forward Voltage <sup>1</sup>	$I_F = I_S, V_{GS} = 0V$	---	---	1.3	V
$t_{rr}$	Reverse Recovery Time	$I_F = -5A, di_F/dt = 100A/\mu S$	---	12	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	9	---	nC

<sup>1</sup>Pulse test : Pulse Width  $\leq 300\mu sec$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

**TYPICAL CHARACTERISTICS**







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