



General Description

The WSF50P10 is the highest performance trench P-ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF50P10 meet the RoHS and Green Product requirement,100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

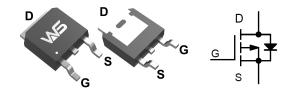
Product Summery

BVDSS	RDSON	ID
-100V	40mΩ	-34A

Applications

 Power Management for Industrial DC / DC Converters.

TO-252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25℃	Continuous Drain Current, -V _{GS} @ -10V	-34	Α
I _D @T _C =100°C	Continuous Drain Current, -V _{GS} @ -10V	-22	А
I _{DM}	Pulsed Drain Current	-136ª	А
E _{AS} c	Single Pulse Avalanche Energy	182	mJ
I _{AS} c	Avalanche Current	-27	А
P _D @T _C =25℃	Total Power Dissipation	96	W
T _{STG}	Storage Temperature Range -55 to 150		$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$ C

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{θJA} b	Thermal Resistance Junction-Ambient		60	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case		1.3	°C/W

Note a: Pulse width is limited by max. junction temperature.

Note b: Surface Mounted on 1in² pad area.

Note c: UIS tested and pulse width are limited by maximum junction temperature 150°C(initial temperature T_J=25°C).



Electrical Characteristics (T $_{J}$ =25 $\,$ $\,$ $\,$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-100			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25℃ , I _D =-1mA		-0.021		V/°C
R _{DS(ON)} d	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-18A		32	40	mΩ
TUS(ON)		V _{GS} =-4.5V , I _D =-10A		38	51	
$V_{GS(th)}$	Gate Threshold Voltage		-1.0	-2.0	-3.0	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	VGS-VDS , ID250UA		4.08		mV/℃
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-80V , V _{GS} =0V , T _J =25°C			-1	uA
		V _{DS} =-80V , V _{GS} =0V , T _J =85°C			-30	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, V_{DS} =0V			±100	nA
Qg ^e	Total Gate Charge			56		nC
Q _{gs} ^e	Gate-Source Charge	V _{DS} =-30V , V _{GS} =-10V , I _D =-18A		9.5		
Q _{gd} e	Gate-Drain Charge			14.5		
T _{d(on)} e	Turn-On Delay Time			17		
T _r e	Rise Time	V_{DD} =-30V , V_{GS} =-10V ,		9		
T _{d(off)} e	Turn-Off Delay Time	R_G =6Ω, I_D =-1A ,RL=30Ω.		83		ns
T _f e	Fall Time			34		
C _{iss} e	Input Capacitance	V _{DS} =-50V , V _{GS} =0V , f=1MHz		2480	3207	
C _{oss} e	Output Capacitance			268		pF
C _{rss} e	Reverse Transfer Capacitance			126		

Diode Characteristics

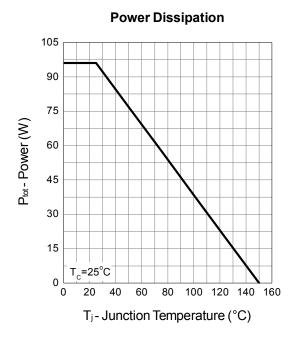
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current	VG=VD=0V, Force Current			-18	Α
V_{SD}	Diode Forward Voltage	V_{GS} =0 V , I_{S} =-18 A , T_{J} =25 $^{\circ}$ C			-1.2	V

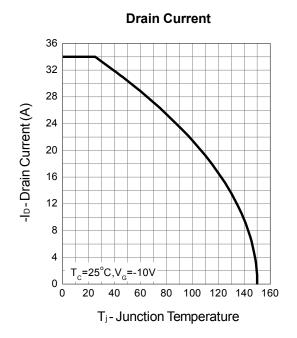
Note d : Pulse test ; pulse width \leq 300 μ s, duty cycle \leq 2%.

Note e: Guaranteed by design, not subject to production testing.

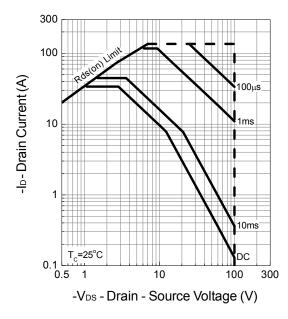


Typical Characteristics

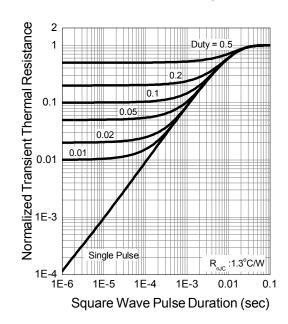




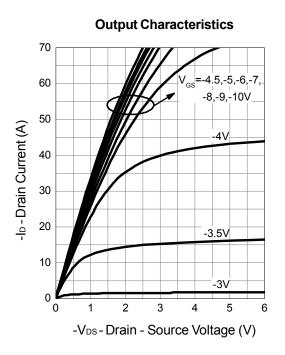
Safe Operation Area

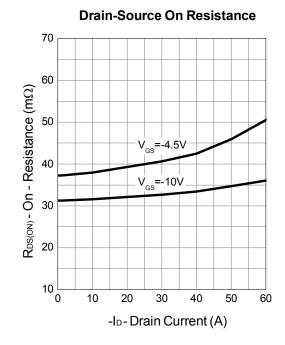


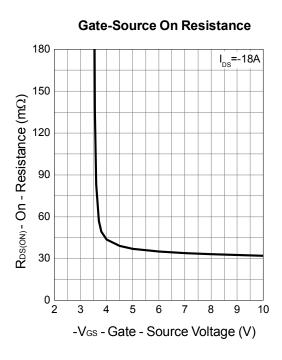
Thermal Transient Impedance

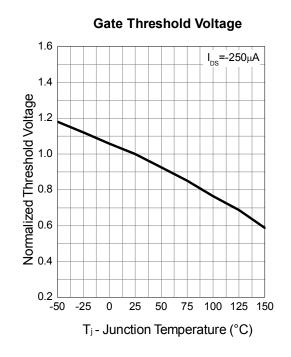






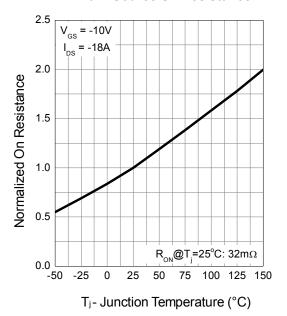




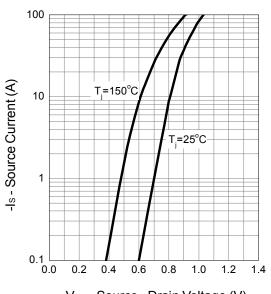




Drain-Source On Resistance

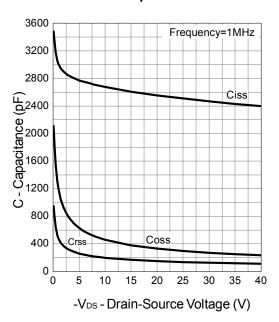


Source-Drain Diode Forward

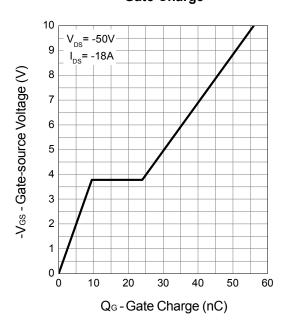


-Vsp - Source - Drain Voltage (V)

Capacitance

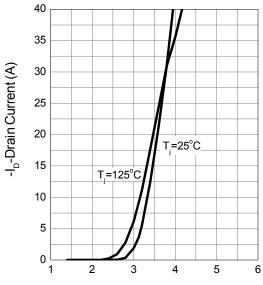


Gate Charge





Transfer Characteristics



-V_{GS} - Gate-Source Voltage



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