

N-Ch MOSFET

#### **General Description**

The WSF90N10 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSF90N10 meet the RoHS and Green Product requirement,100% EAS guaranteed with full function reliability approved.

#### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

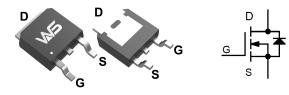
### **Product Summery**

BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
100V	6mΩ	90A

#### Applications

- Power Management in TV Converter.
- DC-DC Converter
- LED TV Back Light

### **TO-252 Pin Configuration**



Symbol	Parameter	Rating	Units	
V <sub>DS</sub>	Drain-Source Voltage 100		V	
V <sub>GS</sub>	Gate-Source Voltage ±20		V	
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	90	А	
I₀@T₀=100°C	Continuous Drain Current, $V_{GS}$ @ $10V^1$ 50		А	
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	13	А	
I₀@T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup> 10		A	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup> ,T <sub>C</sub> =25°C 300		A	
EAS	Avalanche Energy, Single pulse,L=0.5mH 180		mJ	
I <sub>AS</sub>	Avalanche Current, Single pulse,L=0.5mH	27	A	
P₀@T₀=25℃	Total Power Dissipation <sup>4</sup> 104		W	
P <sub>D</sub> @T <sub>C</sub> =100℃	Total Power Dissipation <sup>4</sup>	42	W	
T <sub>STG</sub>	Storage Temperature Range -55 to 150		°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		50	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>		1.2	°C/W

## **Absolute Maximum Ratings**



N-Ch MOSFET

### Electrical Characteristics (T<sub>J</sub>=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	100			V
$\triangle BV_{DSS} / \triangle T_J$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25 $^\circ\!\!{\rm C}$ , I_D=1mA		0.096		V/℃
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =15A		6	9	mΩ
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		10	15	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage		1.0	1.6	2.5	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_{D}=250 \text{uA}$		-5.5		mV/℃
	Drain-Source Leakage Current	V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	
I <sub>DSS</sub>		V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =30A		40		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.1	1.8	Ω
Qg	Total Gate Charge (10V)	V <sub>DS</sub> =30V , V <sub>GS</sub> =10V , I <sub>D</sub> =15A		43		
Q <sub>gs</sub>	Gate-Source Charge			9.5		nC
Q <sub>gd</sub>	Gate-Drain Charge			9.0		
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =50V , V <sub>GS</sub> =10V , R <sub>G</sub> =3Ω, I <sub>D</sub> =1A		18		
Tr	Rise Time			11		
T <sub>d(off)</sub>	Turn-Off Delay Time			55		ns
T <sub>f</sub>	Fall Time			70		
Ciss	Input Capacitance	V <sub>DS</sub> =50V , V <sub>GS</sub> =0V , f=1MHz		2400		
C <sub>oss</sub>	Output Capacitance			355		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			60		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	$V_G = V_D = 0V$ , Force Current			40	А
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>				160	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =15A , TJ=25℃			1.3	V
t <sub>rr</sub>	Reverse Recovery Time	IF=15A,dI/dt=100A/µs,Tյ=25℃		48		nS
Q <sub>rr</sub>	Reverse Recovery Charge			70		nC

Note :

- 4. The power dissipation is limited by 150  $^\circ\!\mathrm{C}$  junction temperature
- 5. The Min. value is 100% EAS tested guarantee.

6.The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

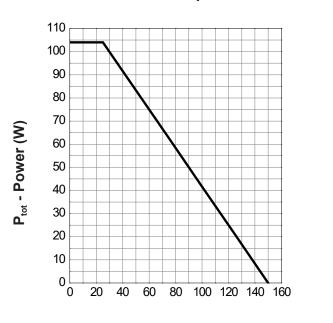
<sup>1</sup>.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,t<10sec. 2.The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2% 3.The EAS data shows Max. rating . The test condition is V<sub>DS</sub>=50V,V<sub>GS</sub>=10V,L=0.5mH,I<sub>AS</sub>=27A



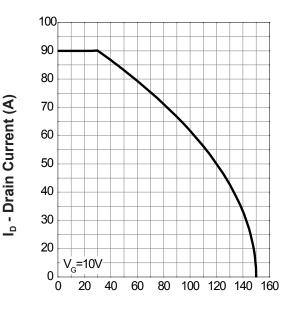
**N-Ch MOSFET** 

# **Typical Operating Characteristics**

**Power Dissipation** 



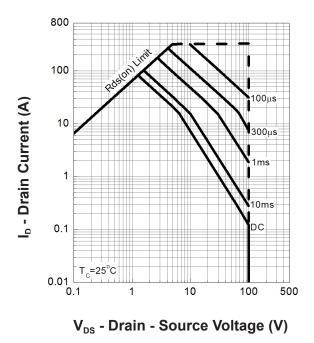
T<sub>c</sub> - Case Temperature (°C)



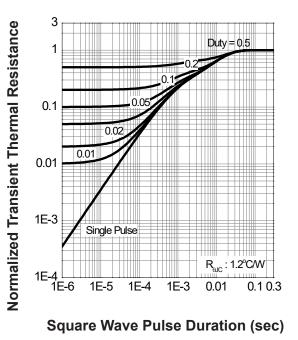
**Drain Current** 

T<sub>c</sub> - Case Temperature (°C)

Safe Operation Area



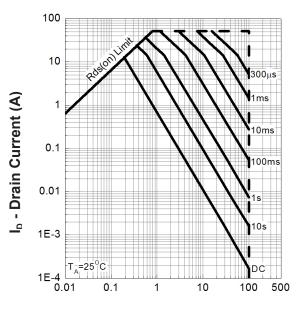
**Thermal Transient Impedance** 





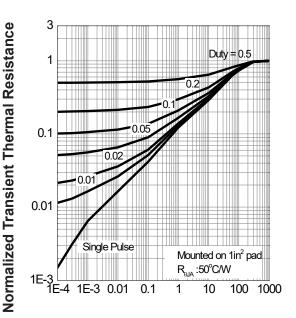
**Typical Operating Characteristics** 

Safe Operation Area



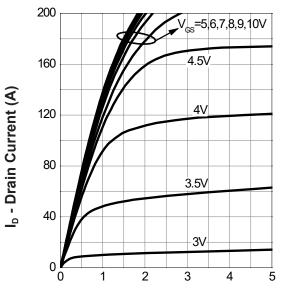
V<sub>DS</sub> - Drain - Source Voltage (V)

# Thermal Transient Impedance



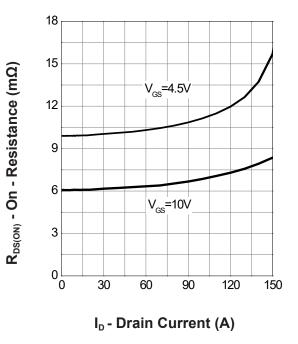
**Square Wave Pulse Duration (sec)** 

**Output Characteristics** 



V<sub>DS</sub> - Drain - Source Voltage (V)

**Drain-Source On Resistance** 



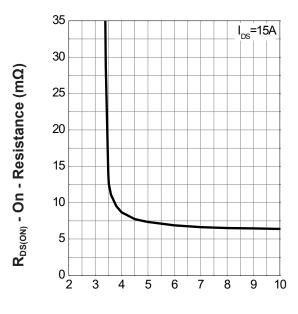
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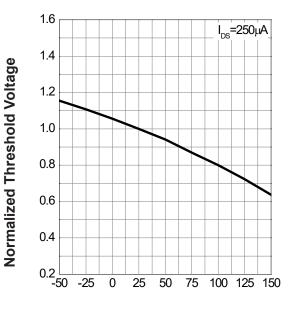
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# **Typical Operating Characteristics**

**Gate-Source On Resistance** 

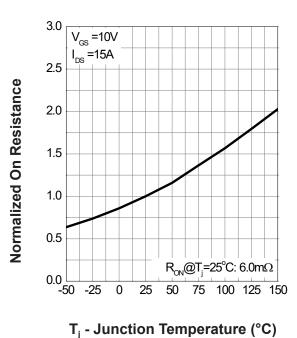


 $V_{\mbox{\scriptsize GS}}$  - Gate - Source Voltage (V)

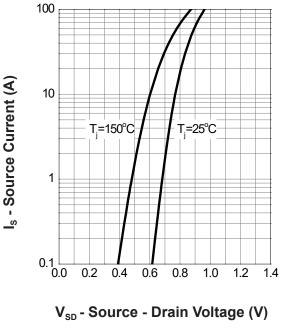


T<sub>j</sub> - Junction Temperature (°C)

**Drain-Source On Resistance** 



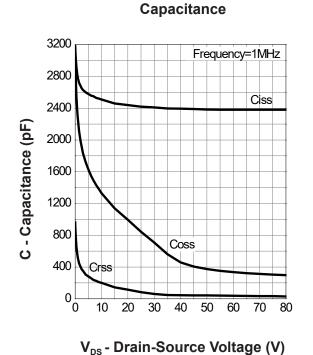
Source-Drain Diode Forward

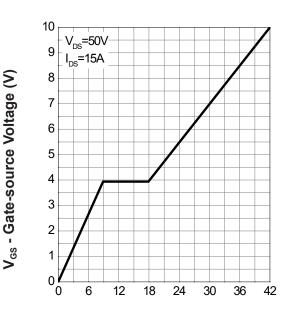




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# **Typical Operating Characteristics**

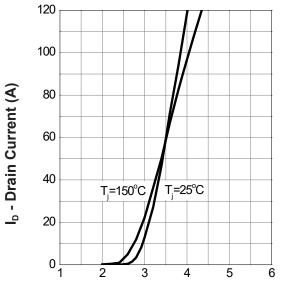




**Gate Charge** 

**Q**<sub>G</sub> - Gate Charge (nC)

**Transfer Characteristics** 



V<sub>gs</sub> - Gate-Source Voltage (V)



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