

General Description

The WSF90P03 is the highest performance trench P-Ch MOSFET with extreme high cell density , which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications .

The WSF90P03 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

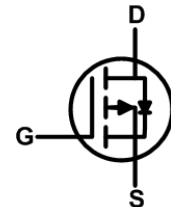
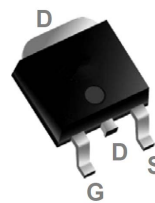
Product Summary

| BVDSS | RDS(on) | ID |
|-------|---------|------|
| -30V | 5mΩ | -85A |

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

TO-252 Pin Configuration



Absolute Maximum Ratings

| Symbol | Parameter | Rating | | Units |
|---------------------------------------|---|------------|--------------|-------|
| | | 10s | Steady State | |
| V _{DS} | Drain-Source Voltage | -30 | | V |
| V _{GS} | Gate-Source Voltage | ±20 | | V |
| I _D @T _C =25°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -85 | | A |
| I _D @T _C =100°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -78 | | A |
| I _D @T _A =25°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -25.8 | -21.3 | A |
| I _D @T _A =70°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -23.2 | -18 | A |
| I _{DM} | Pulsed Drain Current ² | -240 | | A |
| EAS | Single Pulse Avalanche Energy ³ | 408 | | mJ |
| I _{AS} | Avalanche Current | -55.4 | | A |
| P _D @T _C =25°C | Total Power Dissipation ⁴ | 52.1 | | W |
| P _D @T _A =25°C | Total Power Dissipation ⁴ | 5 | 2 | W |
| T _{STG} | Storage Temperature Range | -55 to 150 | | °C |
| T _J | Operating Junction Temperature Range | -55 to 150 | | °C |

Thermal Data

| Symbol | Parameter | Typ. | Max. | Unit |
|------------------|--|------|------|------|
| R _{θJA} | Thermal Resistance Junction-Ambient ¹ | --- | 62 | °C/W |
| R _{θJA} | Thermal Resistance Junction-Ambient ¹ (t ≤ 10s) | --- | 25 | °C/W |
| R _{θJC} | Thermal Resistance Junction-Case ¹ | --- | 2.4 | °C/W |

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------------|--|---|------|--------|------|-------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =-250uA | -30 | --- | --- | V |
| ΔBV _{DSS} /ΔT _J | BV _{DSS} Temperature Coefficient | Reference to 25°C, I _D =-1mA | --- | -0.018 | --- | V/°C |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =-10V, I _D =-30A | --- | 5 | 6 | mΩ |
| | | V _{GS} =-4.5V, I _D =-15A | --- | 6 | 8 | |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _D =-250uA | -1.0 | -1.6 | -2.5 | V |
| ΔV _{GS(th)} | V _{GS(th)} Temperature Coefficient | | --- | 5.04 | --- | mV/°C |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =-24V, V _{GS} =0V, T _J =25°C | --- | --- | 1 | uA |
| | | V _{DS} =-24V, V _{GS} =0V, T _J =55°C | --- | --- | 5 | |
| I _{GSS} | Gate-Source Leakage Current | V _{GS} =±20V, V _{DS} =0V | --- | --- | ±100 | nA |
| g _{fs} | Forward Transconductance | V _{DS} =-5V, I _D =-30A | --- | 26.4 | --- | S |
| Q _g | Total Gate Charge (-4.5V) | V _{DS} =-15V, V _{GS} =-4.5V, I _D =-15A | --- | 37 | --- | nC |
| Q _{gs} | Gate-Source Charge | | --- | 23 | --- | |
| Q _{gd} | Gate-Drain Charge | | --- | 14 | --- | |
| T _{d(on)} | Turn-On Delay Time | V _{DD} =-15V, V _{GS} =-10V, R _G =3.3Ω, I _D =-15A | --- | 15 | --- | ns |
| T _r | Rise Time | | --- | 22 | --- | |
| T _{d(off)} | Turn-Off Delay Time | | --- | 85 | --- | |
| T _f | Fall Time | | --- | 47 | --- | |
| C _{iss} | Input Capacitance | V _{DS} =-15V, V _{GS} =0V, f=1MHz | --- | 4448 | --- | pF |
| C _{oss} | Output Capacitance | | --- | 808 | --- | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 521 | --- | |

Guaranteed Avalanche Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|--------|--|---|------|------|------|------|
| EAS | Single Pulse Avalanche Energy ⁵ | V _{DD} =-25V, L=0.1mH, I _{AS} =-30A | 120 | --- | --- | mJ |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| I _S | Continuous Source Current ^{1,6} | V _G =V _D =0V, Force Current | --- | --- | -20 | A |
| I _{SM} | Pulsed Source Current ^{2,6} | | --- | --- | -180 | A |
| V _{SD} | Diode Forward Voltage ² | V _{GS} =0V, I _S =-1A, T _J =25°C | --- | --- | -1.2 | V |
| t _{rr} | Reverse Recovery Time | IF=-15A, dI/dt=100A/μs, T _J =25°C | --- | 34 | --- | nS |
| Q _{rr} | Reverse Recovery Charge | | --- | 19 | --- | nC |

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The EAS data shows Max. rating. The test condition is V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-30A
4. The power dissipation is limited by 150°C junction temperature
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

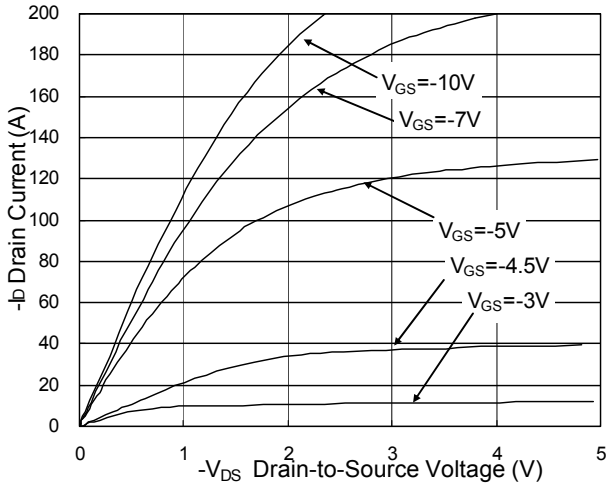


Fig.1 Typical Output Characteristics

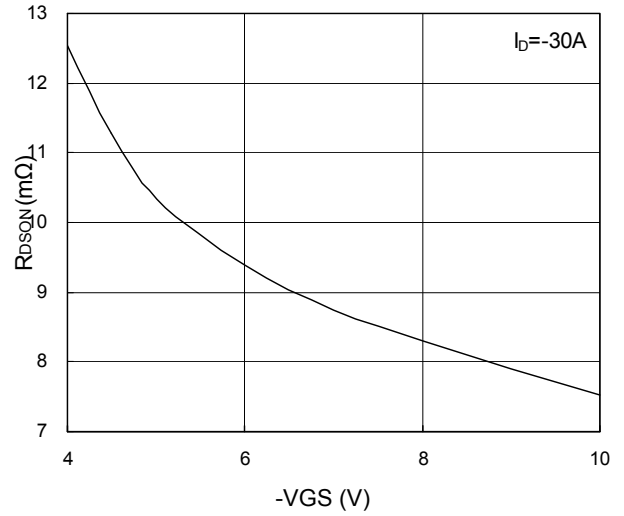


Fig.2 On-Resistance v.s Gate-Source

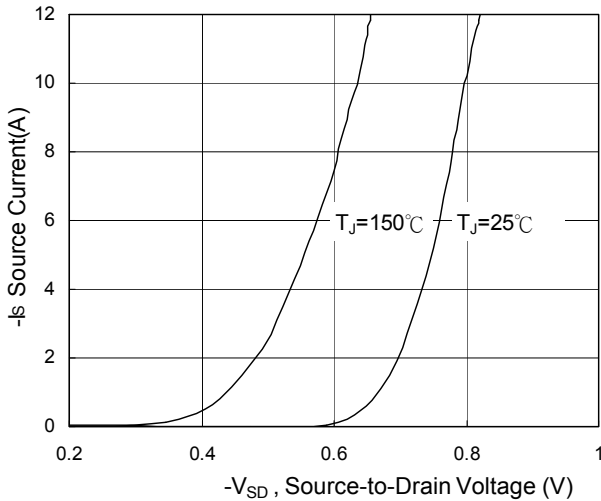


Fig.3 Forward Characteristics Of Reverse

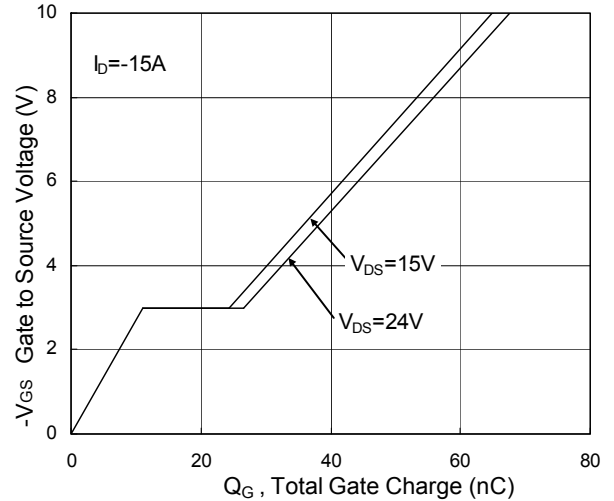


Fig.4 Gate-Charge Characteristics

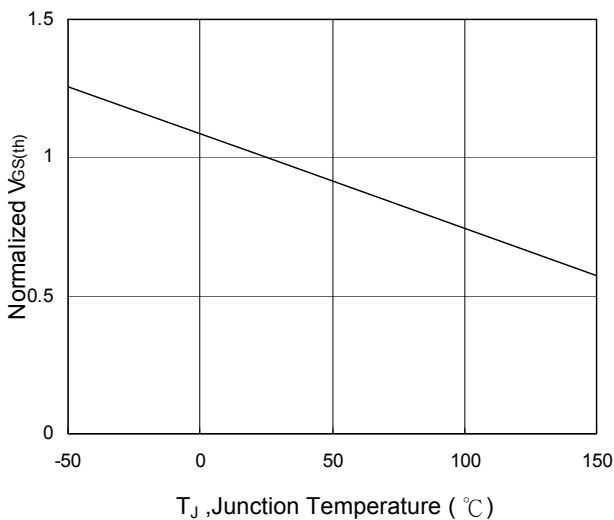


Fig.5 Normalized V_{GS(th)} v.s T_J

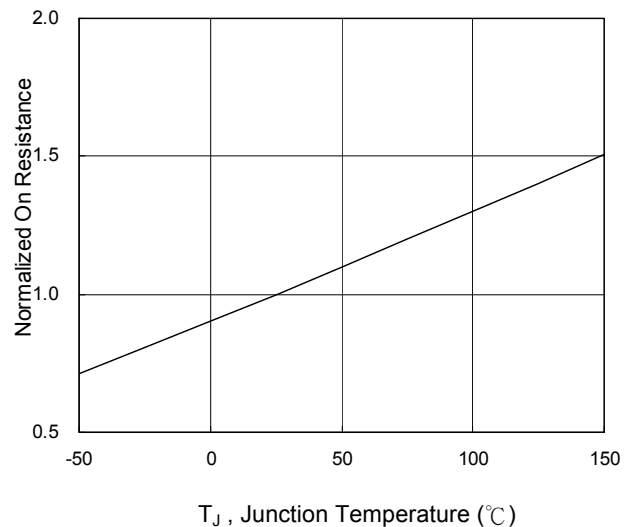


Fig.6 Normalized R_{DS(on)} v.s T_J

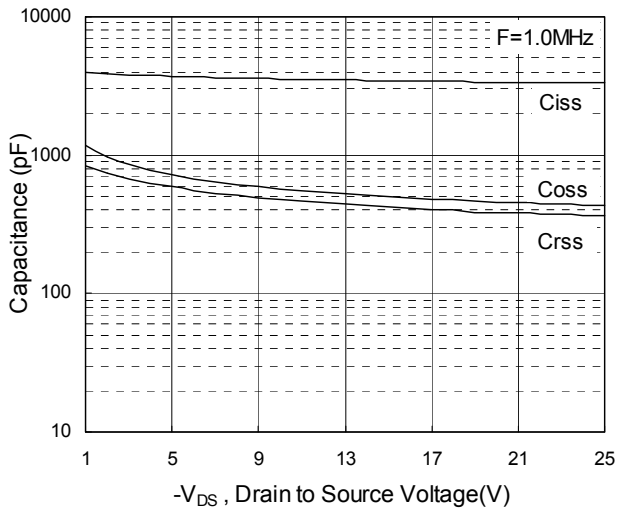


Fig.7 Capacitance

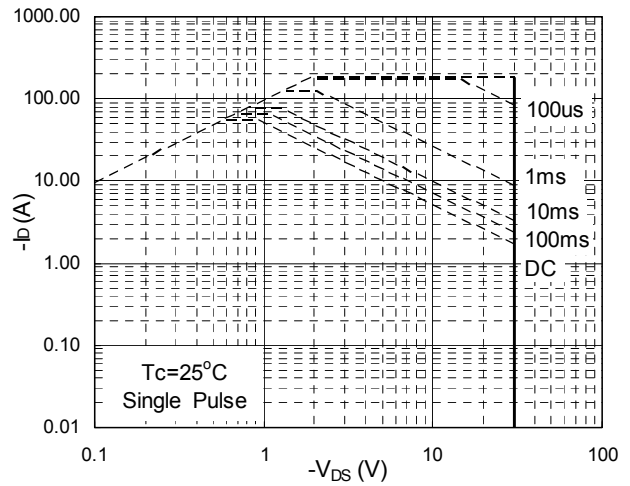


Fig.8 Safe Operating Area

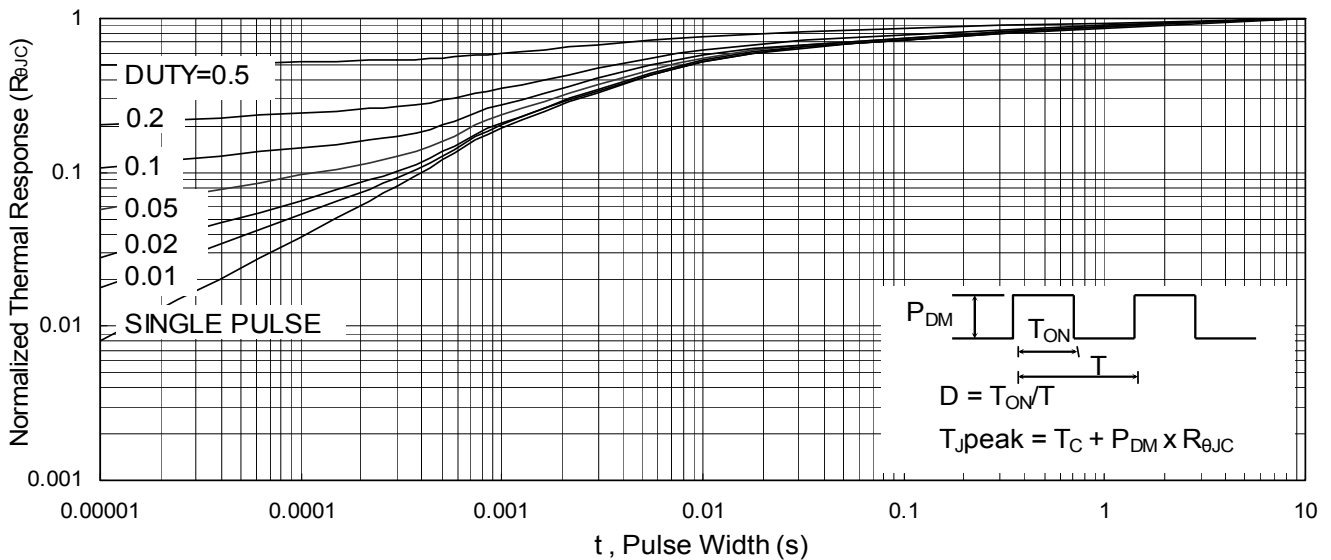


Fig.9 Normalized Maximum Transient Thermal Impedance

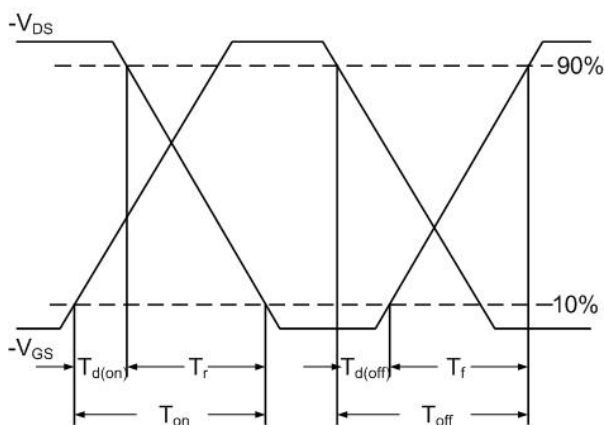


Fig.10 Switching Time Waveform

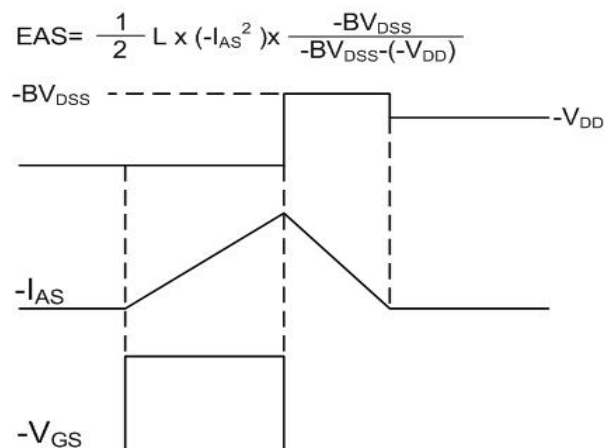


Fig.11 Unclamped Inductive Switching Waveform



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