

## General Description

The WSK100P06 is the highest performance trench P-Ch MOSFET with extreme high cell density, which provide excellent  $R_{DSON}$  and gate charge for most of the synchronous buck converter applications.

The WSK100P06 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

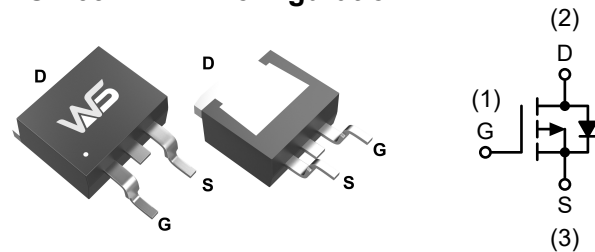
## Product Summary

$BV_{DSS}$	$R_{DSON}$	$I_D$
-60V	5.5m $\Omega$	-100A

## Applications

- Power Management in Desktop Computer or DC/DC Converters

## TO-263-2L Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	$\pm 25$	V
$I_D$	Continuous Drain Current, $V_{GS} @ -10V; T_C=25^\circ C$	-100	A
	Continuous Drain Current, $V_{GS} @ -10V; T_C=100^\circ C$	-58	A
$I_S$	Diode Continuous Forward Current	-80	A
$I_{AS}$	Avalanche Energy, Single pulse ;L=1mH	49	A
$E_{AS}$	Avalanche Energy, Single pulse;;L=1mH	1200	mJ
$I_{DP}$	Pulse Drain Current Tested ; $T_C=25^\circ C$	-264	A
$P_D$	Maximum Power Dissipation; $T_C=25^\circ C$	250	W
	Maximum Power Dissipation; $T_C=100^\circ C$	100	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	55	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup> (t $\leq$ 10s)	---	20	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	0.5	$^\circ C/W$

Note : \*Current limited by bond wire.

Note a : UIS tested and pulse width limited by maximum junction temperature 150  $^\circ C$  (initial temperature  $T_J=25^\circ C$ ).

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1\text{mA}$	---	-0.018	---	V/ $^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-10V, I_D=-20A$	---	5.5	6.8	m $\Omega$
		$V_{GS}=-4.5V, I_D=-10A$	---	9.0	12	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.3	-1.8	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	5.04	---	mV/ $^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-48V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=-48V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=-5V, I_D=-20A$	---	26.4	---	S
$Q_g$	Total Gate Charge (-4.5V)	$V_{DS}=-30V, V_{GS}=-4.5V, I_D=-20A$	---	136	---	nC
$Q_{gs}$	Gate-Source Charge		---	20	---	
$Q_{gd}$	Gate-Drain Charge		---	33	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-15V, V_{GS}=-10V,$ $R_G=3.3\Omega, I_D=-20A$	---	18	---	ns
$T_r$	Rise Time		---	20	---	
$T_{d(off)}$	Turn-Off Delay Time		---	200	---	
$T_f$	Fall Time		---	120	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V, f=1\text{MHz}$	---	6095	---	pF
$C_{oss}$	Output Capacitance		---	1080	---	
$C_{rss}$	Reverse Transfer Capacitance		---	430	---	

**Diode Characteristics**

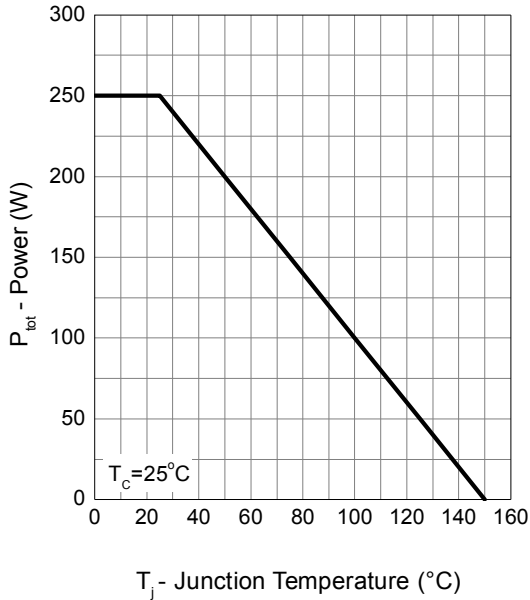
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current <sup>1,6</sup>	$V_G=V_D=0V$ , Force Current	---	---	-80	A
$I_{SM}$	Pulsed Source Current <sup>2,6</sup>		---	---	-160	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_S=-1A, T_J=25^\circ\text{C}$	---	---	-1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=-20A, dI/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	30	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	20	---	nC

Note :

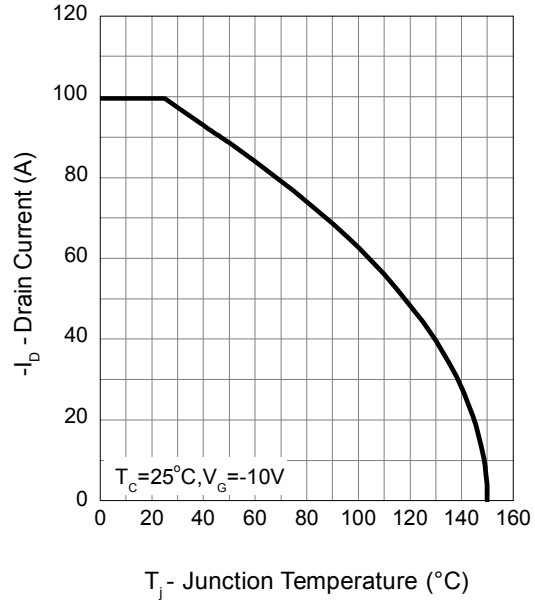
- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,  $t < 10\text{sec}$ .
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}=-25V, V_{GS}=-10V, L=0.1\text{mH}, I_{AS}=-30A$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

**Typical Characteristics**

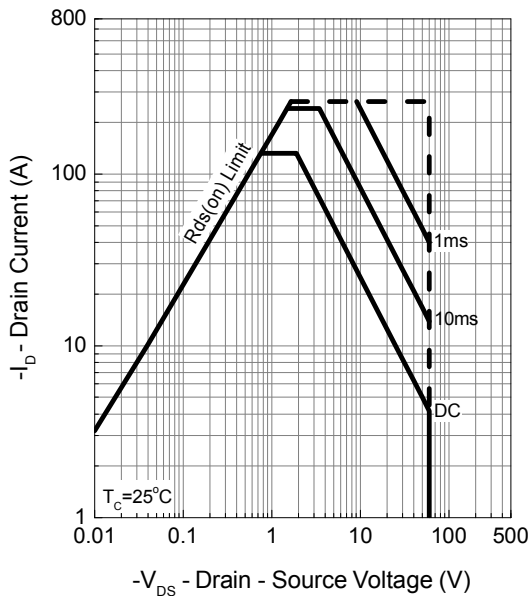
**Power Dissipation**



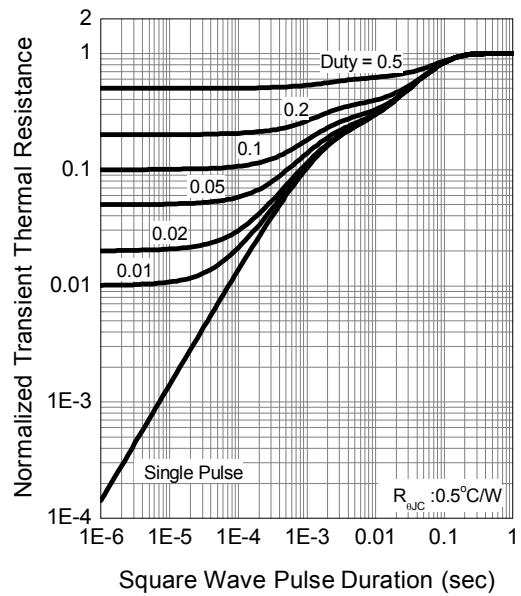
**Drain Current**



**Safe Operation Area**

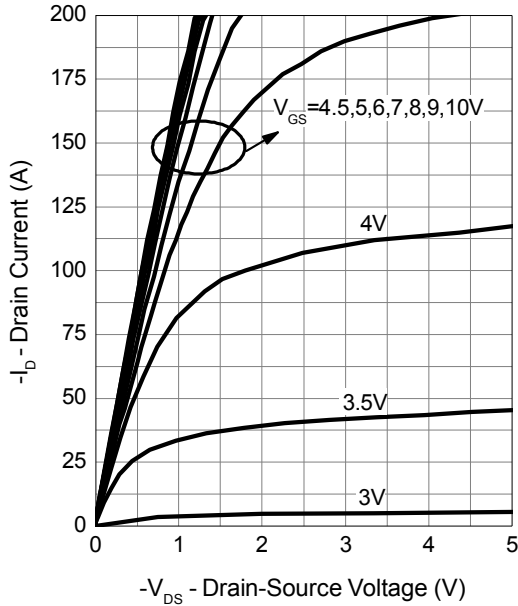


**Thermal Transient Impedance**

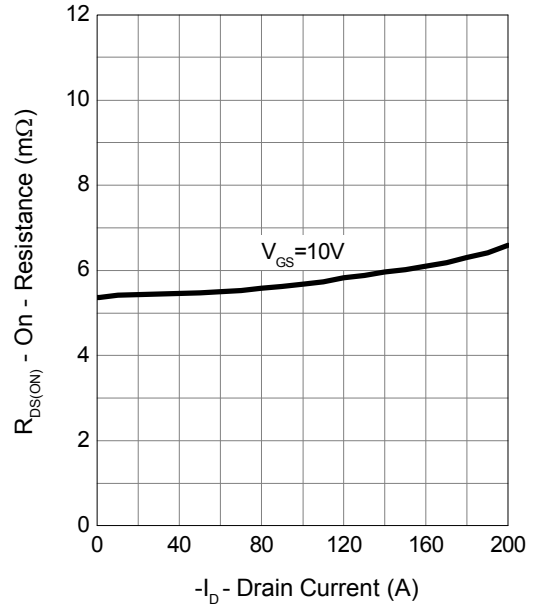


**Typical Characteristics**

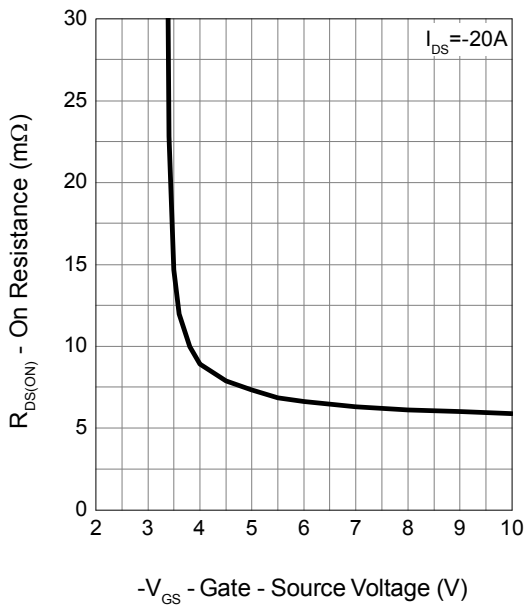
**Output Characteristics**



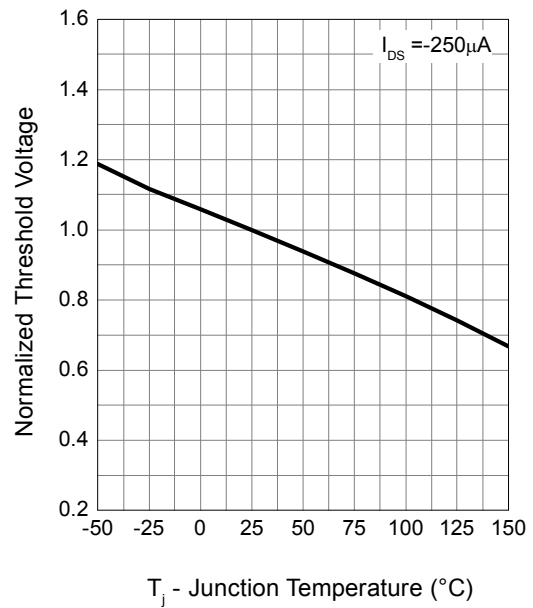
**Drain-Source On Resistance**



**Gate-Source On Resistance**

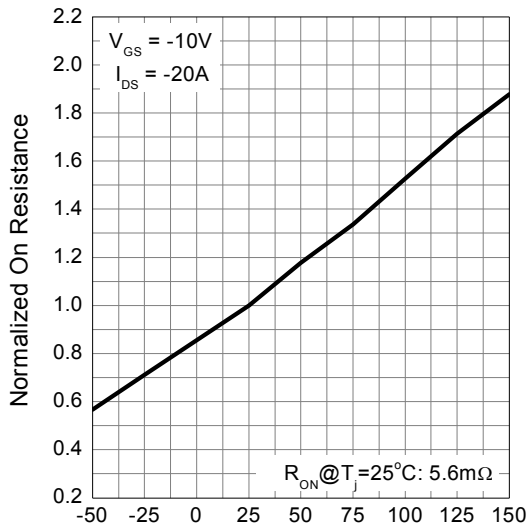


**Gate Threshold Voltage**



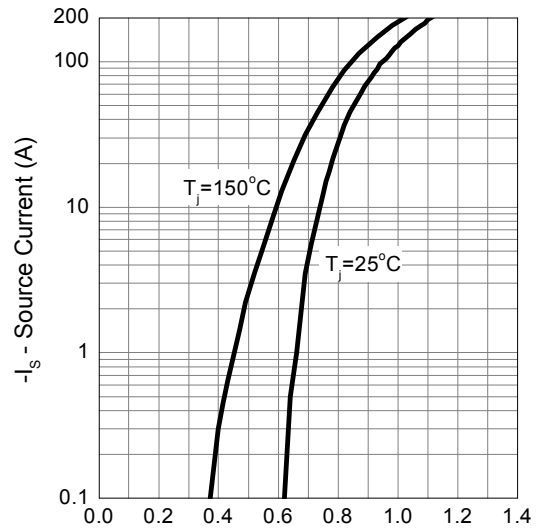
**Typical Characteristics**

**Drain-Source On Resistance**



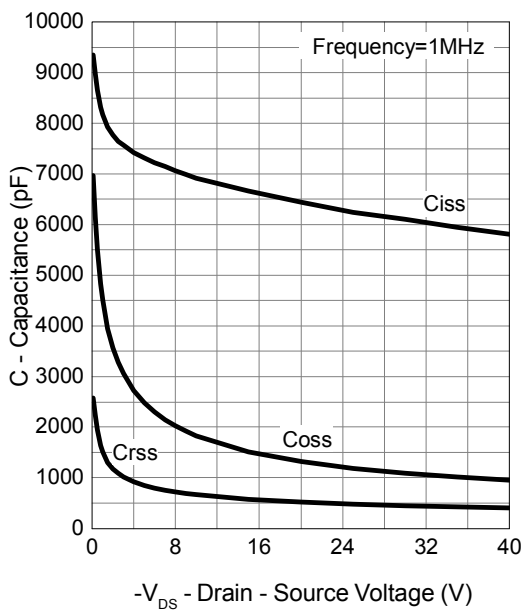
$T_j$  - Junction Temperature ( $^{\circ}C$ )

**Source-Drain Diode Forward**



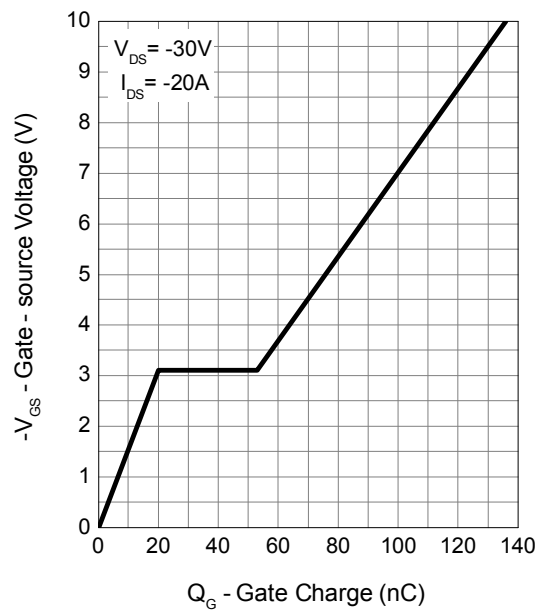
$-V_{SD}$  - Source - Drain Voltage (V)

**Capacitance**

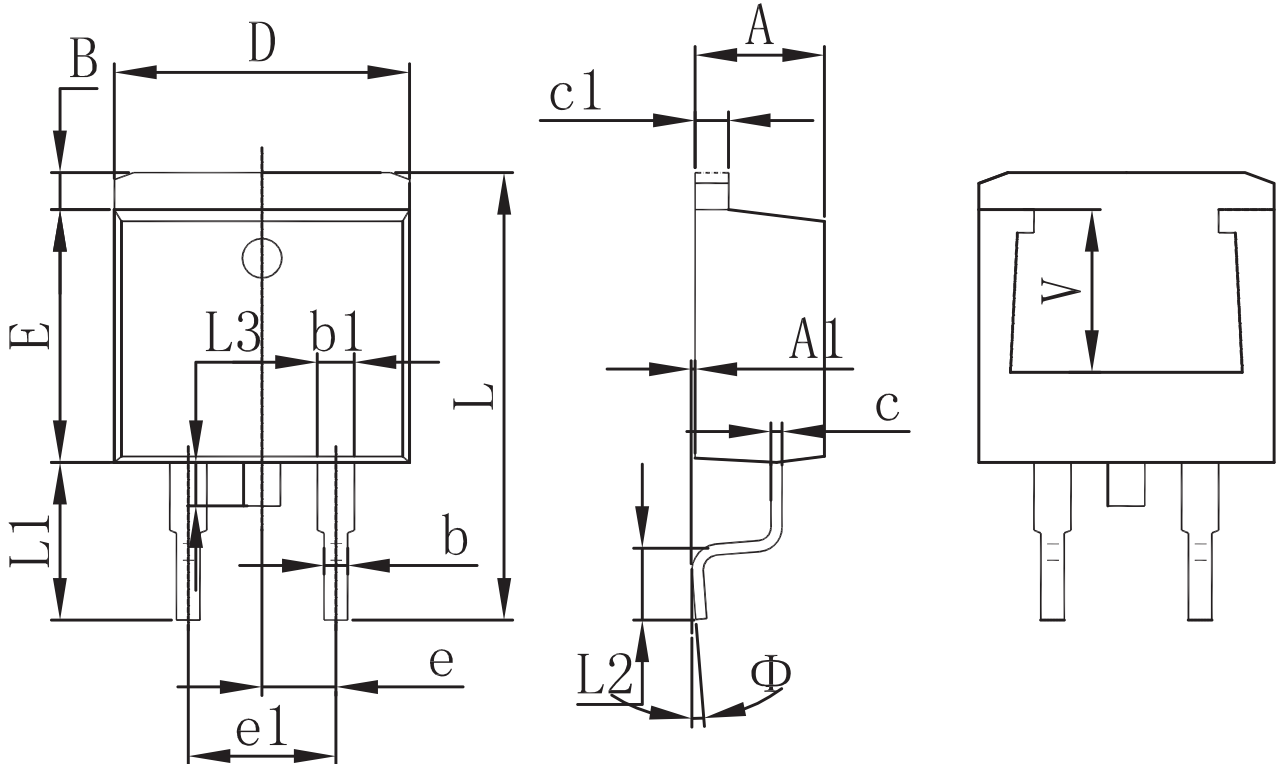


$-V_{DS}$  - Drain - Source Voltage (V)

**Gate Charge**



$Q_G$  - Gate Charge (nC)

**Packaging information**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.120	1.420	0.044	0.056
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
L	14.940	15.500	0.588	0.610
L1	4.950	5.450	0.195	0.215
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
$\Phi$	0°	8°	0°	8°
V	5.600 REF.		0.220REF.	



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