

# **General Description**

The WSP4068 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSP4068 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

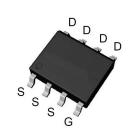
### **Product Summery**

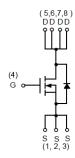
BVDSS	RDSON	ID
40V	16.5mΩ	10A

#### **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

# **S0P-8 Pin Configuration**





# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	10	Α
I <sub>D</sub> @T <sub>C</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	9	А
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	30	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	26	mJ
I <sub>AS</sub>	Avalanche Current	23	Α
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation⁴	2.08	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>		65	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case <sup>1</sup>		20	°C/W

**N-Ch MOSFET** 

# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃ , I <sub>D</sub> =1mA		0.024		V/°C
В	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =12A		13.5	16.5	
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		19	24.5	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V -V 1 -250A	1.5	1.8	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-5.07		mV/℃
	Drain Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	uA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			30	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20 V$ , $V_{DS}$ =0 $V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =8A		31		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.1	1.8	Ω
$Q_g$	Total Gate Charge (4.5V)	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =12A		9.4		
Q <sub>gs</sub>	Gate-Source Charge			3.9		nC
Q <sub>gd</sub>	Gate-Drain Charge			3.0		
T <sub>d(on)</sub>	Turn-On Delay Time			12	14	
Tr	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$		10	17	
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =1A ,R <sub>L</sub> =15Ω		23	42	ns
T <sub>f</sub>	Fall Time			6	12	
Ciss	Input Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		1125		
C <sub>oss</sub>	Output Capacitance			132		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			70		

#### **Guaranteed Avalanche Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy <sup>5</sup>	V <sub>DD</sub> =25V , L=0.1mH , I <sub>AS</sub> =23A		26		mJ

#### **Diode Characteristics**

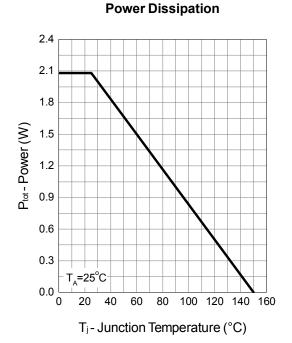
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	V =V =0V Force Current			8	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			38	Α
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.1	V
t <sub>rr</sub>	Reverse Recovery Time			15		nS
Q <sub>rr</sub>	Reverse Recovery Charge	IF=12A , dI/dt=100A/ $\mu$ s , T $_{J}$ =25 $^{\circ}$ C		9.5		nC

#### Note

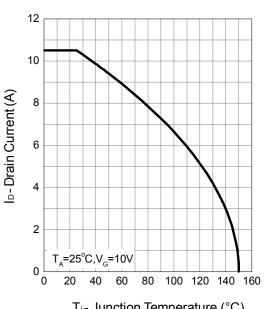
- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, t<10 sec.
- 2.The data tested by pulsed , pulse width  $\,\leq\,300\text{us}$  , duty cycle  $\,\leq\,2\%$
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH, $I_{AS}$ =23A
- 4.The power dissipation is limited by 150  $^{\circ}\mathrm{C}\,$  junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



# **Typical Characteristics**

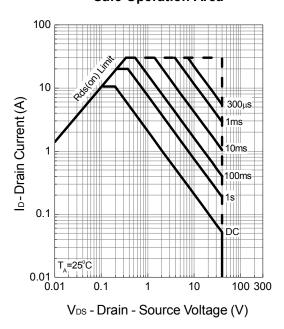


#### **Drain Current**

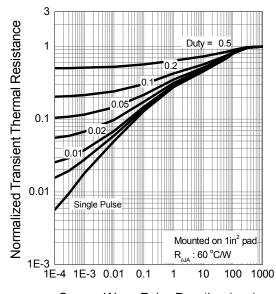


T<sub>j</sub>- Junction Temperature (°C)

# Safe Operation Area



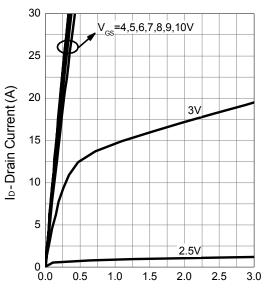
# **Thermal Transient Impedance**



Square Wave Pulse Duration (sec)

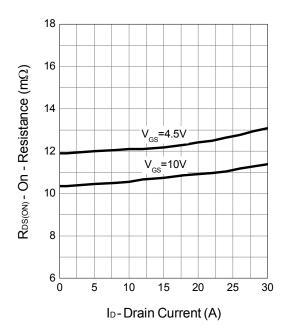


# **Output Characteristics**

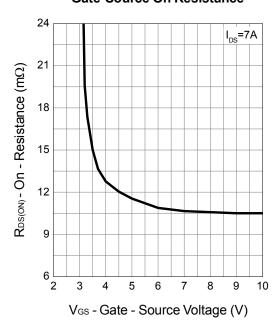


V<sub>DS</sub> - Drain - Source Voltage (V)

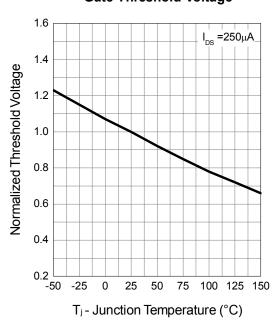
#### **Drain-Source On Resistance**



**Gate-Source On Resistance** 



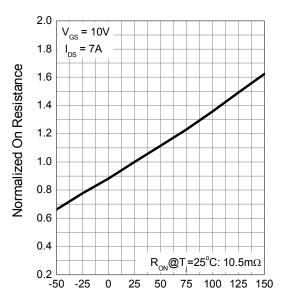
**Gate Threshold Voltage** 



V

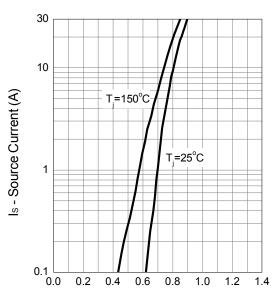


#### **Drain-Source On Resistance**



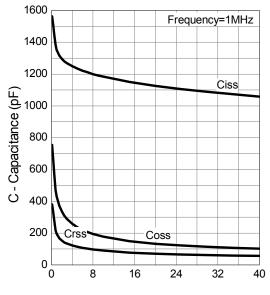
T<sub>j</sub>-Junction Temperature (°C)

#### **Source-Drain Diode Forward**



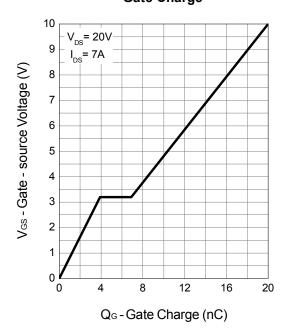
Vsp - Source - Drain Voltage (V)

# Capacitance



V<sub>DS</sub> - Drain - Source Voltage (V)

# **Gate Charge**



V



#### **Attention**

- 1, Any and all Winsok power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Winsok power representative nearest you before using any Winsok power products described or contained herein in such applications.
- 2, Winsok power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Winsok power products described or contained herein.
- 3, Specifications of any and all Winsok power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- 4, Winsok power Semiconductor CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- 5,In the event that any or all Winsok power products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- 6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Winsok power Semiconductor CO., LTD.
- 7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Winsok power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- 8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Winsok power product that you Intend to use.
- 9, this catalog provides information as of Sep.2014. Specifications and information herein are subject to change without notice.

# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for MOSFET category:

Click to view products by Winsok manufacturer:

Other Similar products are found below:

614233C 648584F MCH3443-TL-E MCH6422-TL-E FDPF9N50NZ NTNS3A92PZT5G IRFD120 IRFF430 JANTX2N5237 2N7000
AOD464 2SK2267(Q) 2SK2545(Q,T) 405094E 423220D MIC4420CM-TR VN1206L 614234A 715780A 751625C
IPS70R2K0CEAKMA1 BSF024N03LT3 G PSMN4R2-30MLD TK31J60W5,S1VQ(O 2SK2614(TE16L1,Q) DMN1017UCP3-7
EFC2J004NUZTDG FCAB21350L1 P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 NTE2969 NTE6400A DMC2700UDMQ-7
DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 SSM6P54TU,LF DMP22D4UFO-7B IPS60R3K4CEAKMA1
DMN1006UCA6-7 DMN16M9UCA6-7 STF5N65M6 IRF40H233XTMA1 IPSA70R950CEAKMA1 IPSA70R2K0CEAKMA1 STU5N65M6
C3M0021120D DMN6022SSD-13