General Description

The WSP4606C is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSP4606C meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Product Summery

BVDSS	RDSON	ID
20V	28mΩ	6.5A
-20V	55mΩ	-5.8A

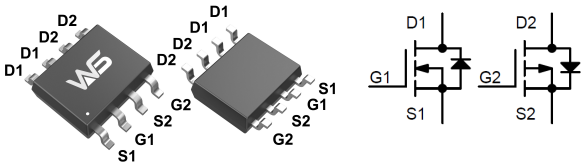
Applications

- MB/NB/UMPC/VGA
- DC-DC Power System
- Inverter

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

SOP-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	20	-20	V
V_{GS}	Gate-Source Voltage	±10	±12	V
I_D@T_A=25℃	Continuous Drain Current	6.5	-5.8	A
I_D@T_A=70℃	Continuous Drain Current	3.8	-3.5	A
I_{DM}	Pulsed Drain Current	28	-28	A
P_D@T_A=25℃	Total Power Dissipation	1.5	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R_{thJA}	Thermal Resistance Junction-Ambient	---	62.5	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	20	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=4.5V , I_D=3A	---	28	35	mΩ
		V_{GS}=2.5V , I_D=2A	---	32	40	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	0.4	0.72	1.2	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=20V , V_{GS}=0V .	---	---	1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
Q_g	Total Gate Charge	V_{DS}=15V,V_{GS}=4.5V,I_D=3A.	---	4.6	---	nC
Q_{gs}	Gate-Source Charge		---	0.7	---	
Q_{gd}	Gate-Drain Charge		---	1.5	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=10V,V_{GS}=4.5V,R_G=3.3 Ω, I_D=1A .	---	1.6	---	ns
T_r	Rise Time		---	42	---	
$T_{d(off)}$	Turn-Off Delay Time		---	14	---	
T_f	Fall Time		---	7	---	
C_{iss}	Input Capacitance	V_{DS}=15V,V_{GS}=0V,f=1MHz .	---	310	---	pF
C_{oss}	Output Capacitance		---	49	---	
C_{rss}	Reverse Transfer Capacitance		---	35	---	
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	3.6	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A.	---	---	1.2	V

P-Channel Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-20	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-4.5V , I_D=-5A	---	55	80	mΩ
		V_{GS}=-2.5V , I_D=-3A	---	75	100	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-0.4	-0.60	-1.2	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-20V , V_{GS}=0V , T_J=25℃	---	---	1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±12V , V_{DS}=0V	---	---	±100	nA
Q_g	Total Gate Charge	V_{DS}=-10V,V_{GS}=-4.5V,I_D=-3A.	---	10.1	---	nC
Q_{gs}	Gate-Source Charge		---	1.21	---	
Q_{gd}	Gate-Drain Charge		---	2.46	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=-10V,V_{GS}=-4.5V ,R_G=3Ω , I_D=-3A .	---	5.6	---	ns
T_r	Rise Time		---	32.2	---	
$T_{d(off)}$	Turn-Off Delay Time		---	45.6	---	
T_f	Fall Time		---	29.2	---	
C_{iss}	Input Capacitance	V_{DS}=-10V,V_{GS}=0V , f=1MHz.	---	677	---	pF
C_{oss}	Output Capacitance		---	82	---	
C_{rss}	Reverse Transfer Capacitance		---	73	---	
I_S	Continuous Source Current	T_A =25°C	---	---	-3.0	A
V_{SD}	Diode Forward Voltage	I_{SD}= -1A , V_{GS}=0V.	---	---	-1.2	V

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.

N-Channel Typical Characteristics

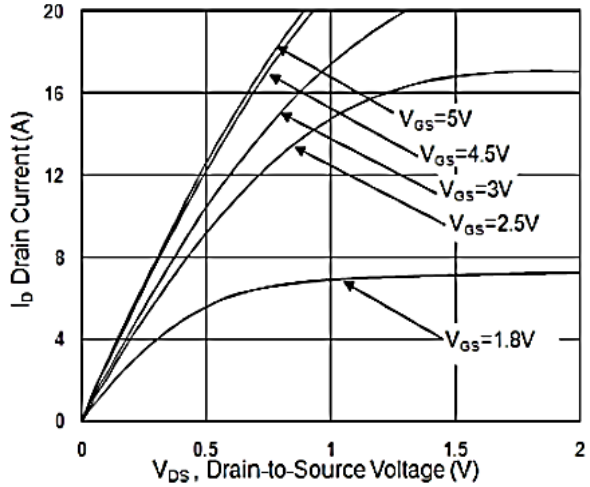


Fig.1 Typical Output Characteristics

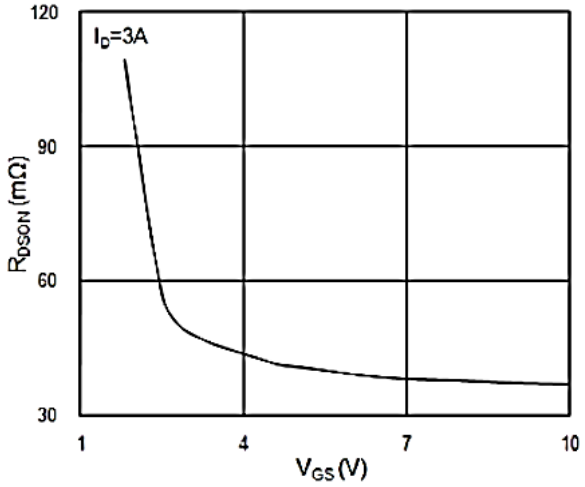


Fig.2 On-Resistance vs. G-S Voltage

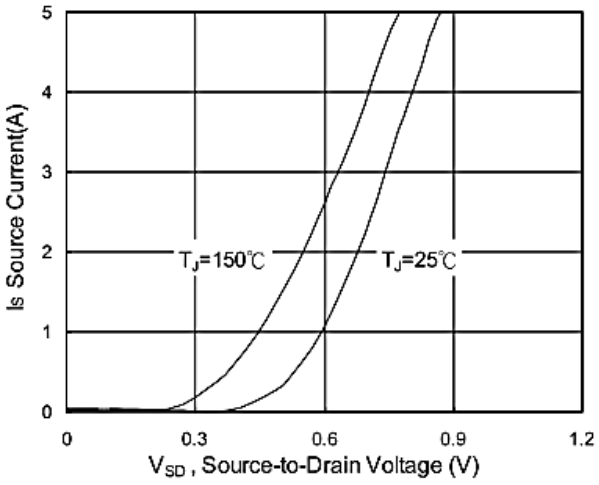


Fig.3 Source Drain Forward Characteristics

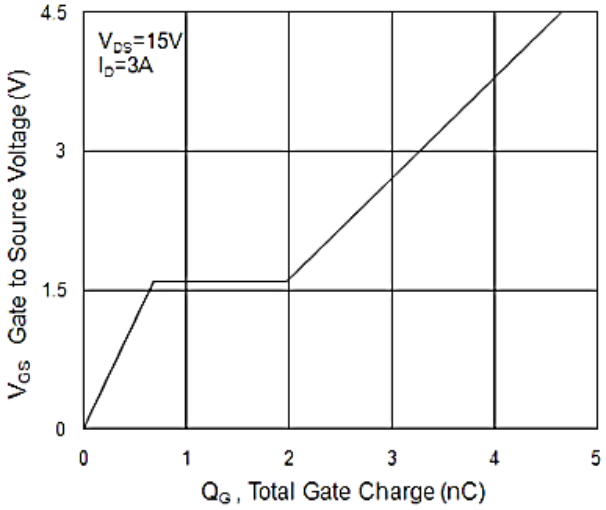


Fig.4 Gate-Charge Characteristics

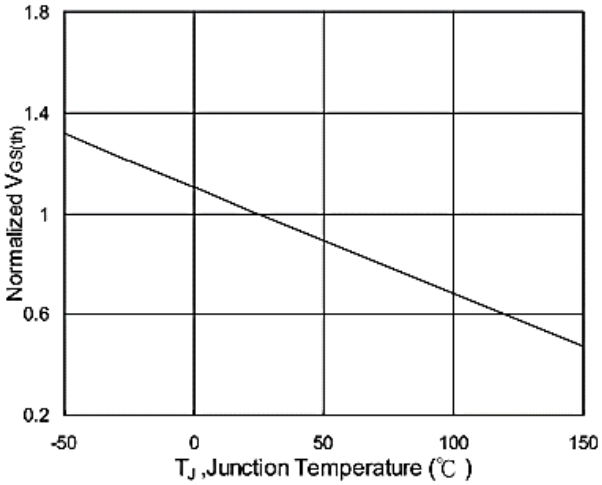


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

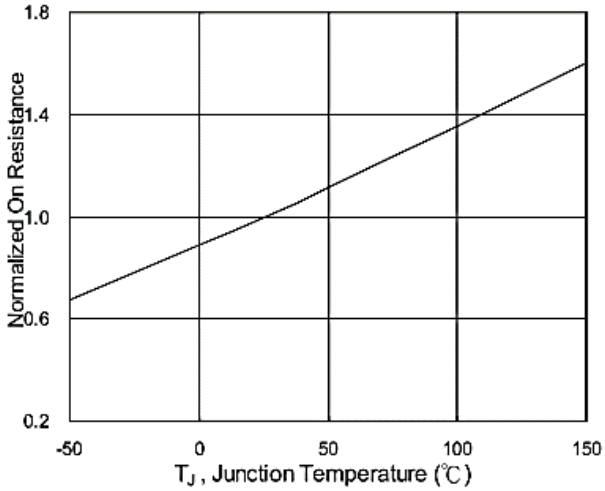


Fig.6 Normalized R_{DSON} vs. T_J

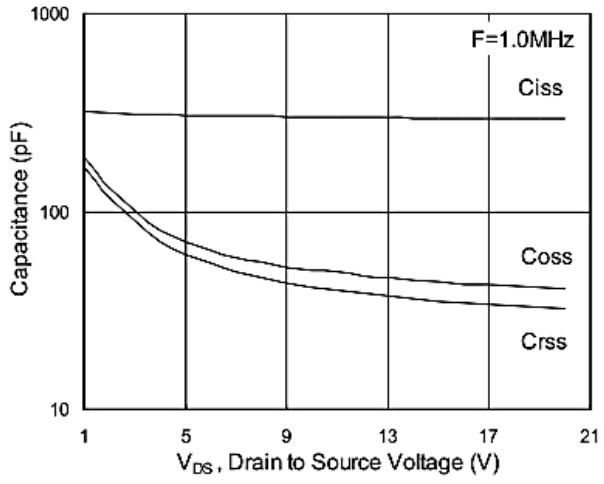


Fig.7 Capacitance

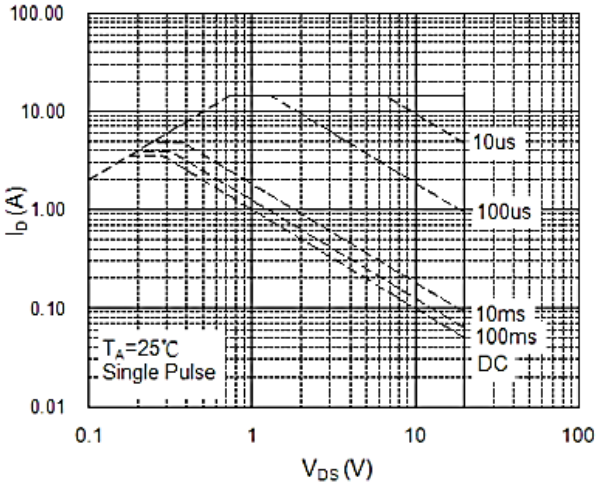


Fig.8 Safe Operating Area

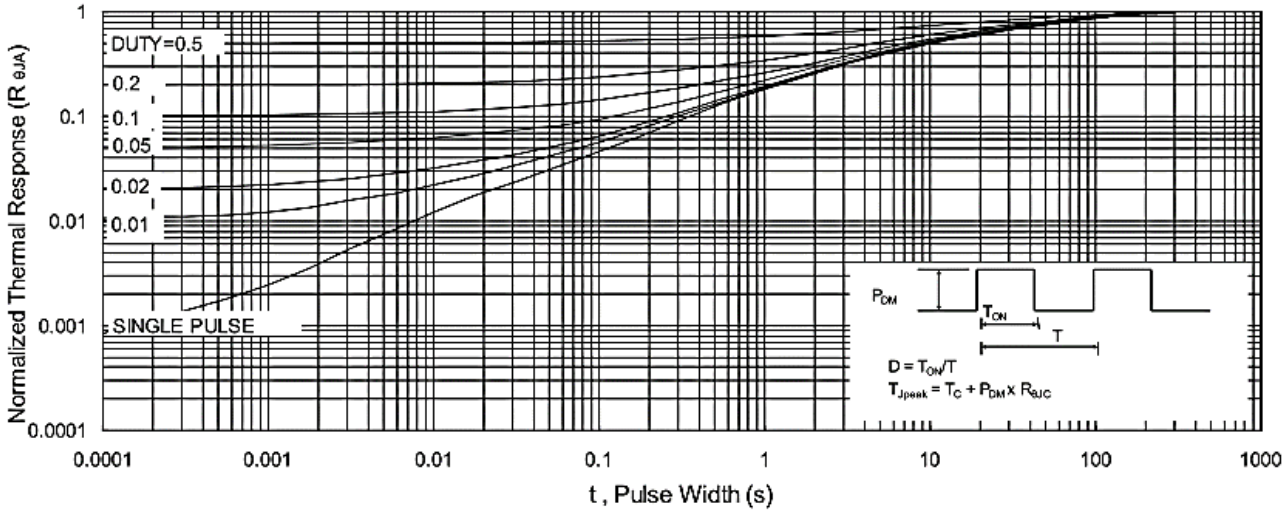


Fig.9 Normalized Maximum Transient Thermal Impedance

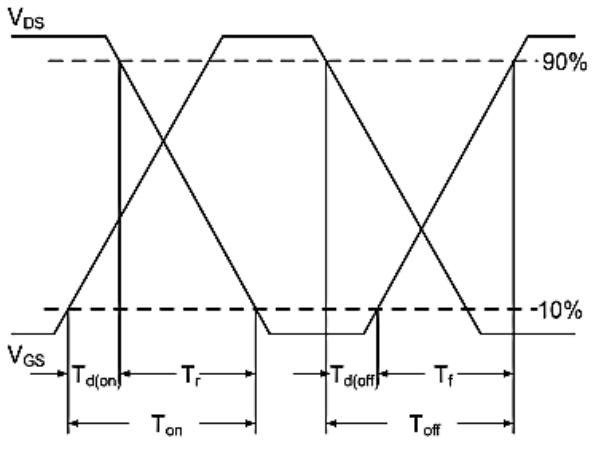


Fig.10 Switching Time Waveform

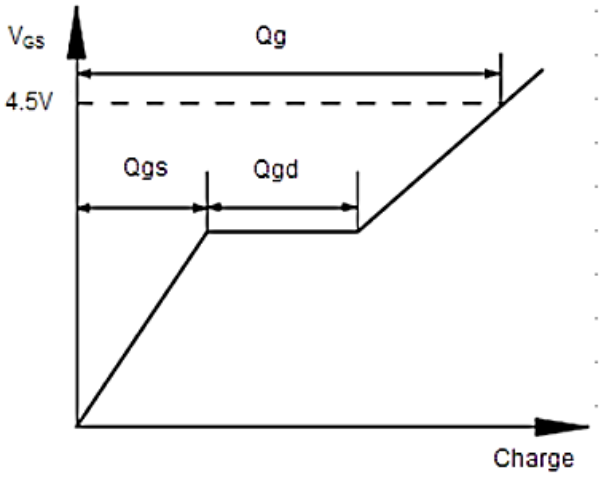


Fig.11 Gate Charge Waveform

P-Channel Typical Characteristics

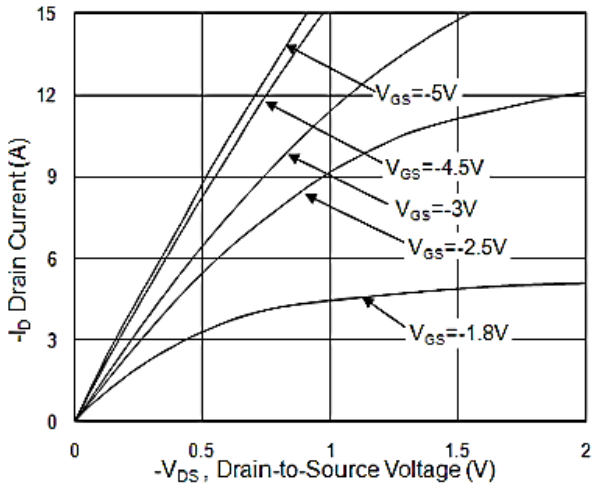


Fig.1 Typical Output Characteristics

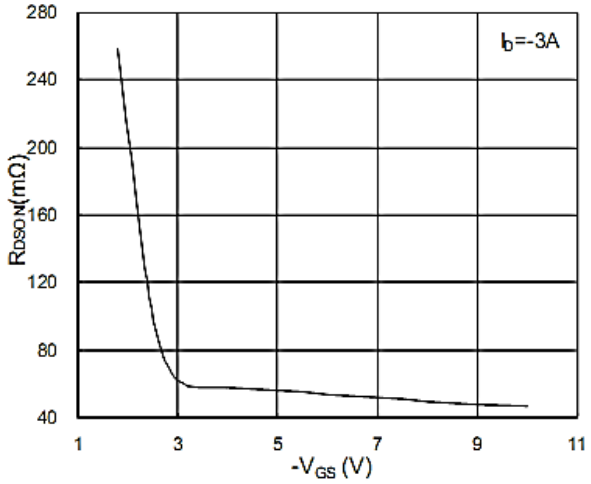


Fig.2 On-Resistance vs. Gate-Source

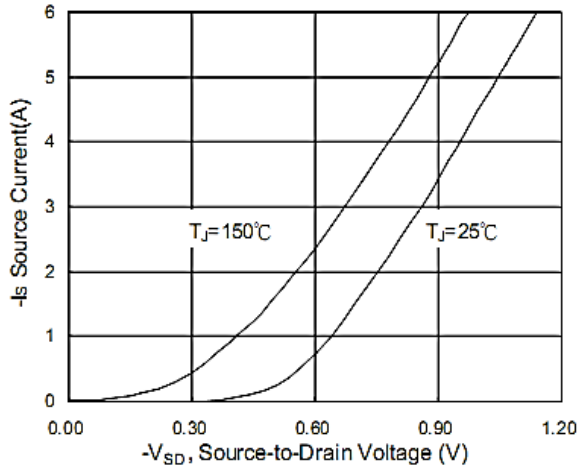


Fig.3 Forward Characteristics Of Reverse

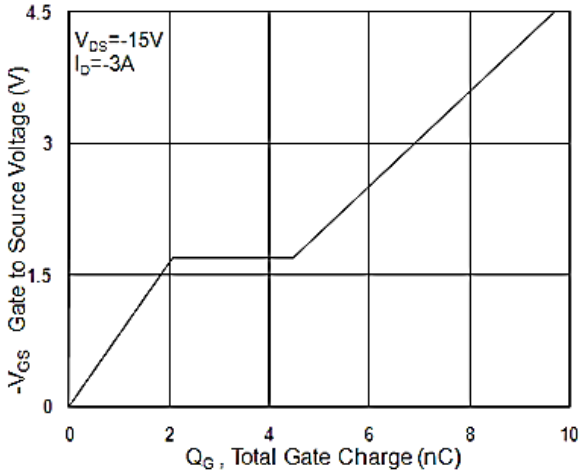


Fig.4 Gate-Charge Characteristics

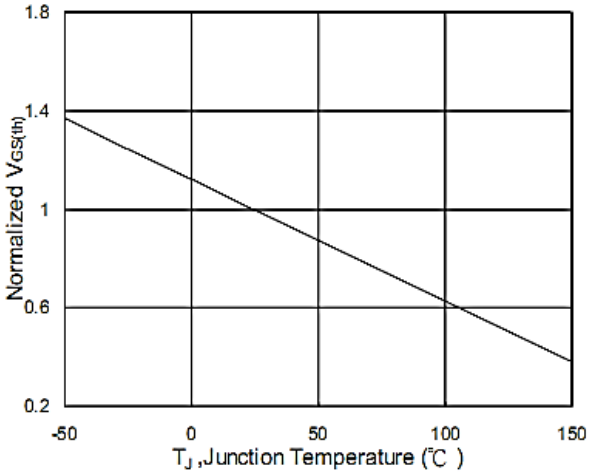


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

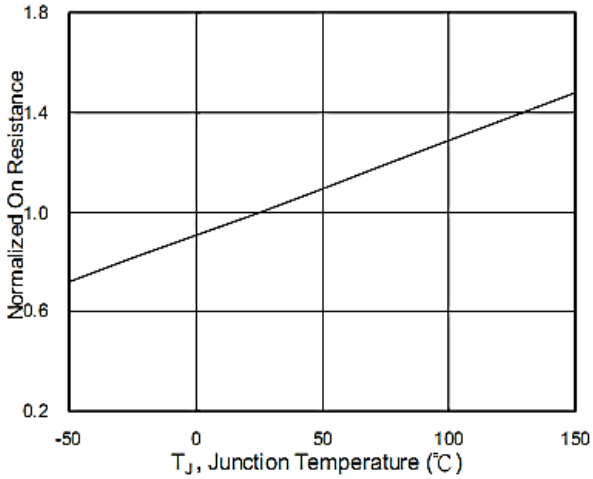


Fig.6 Normalized R_{DSON} vs. T_J

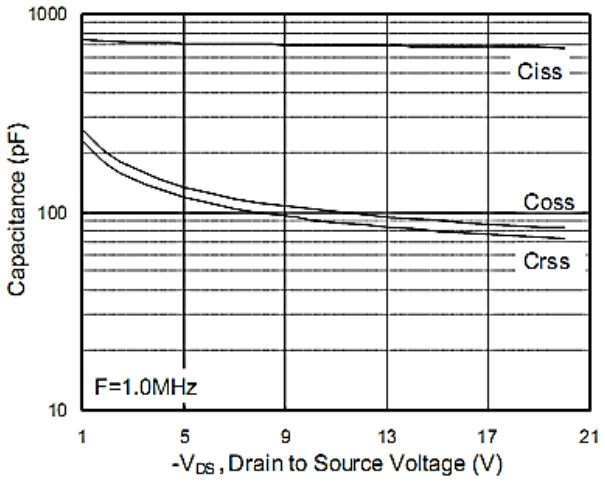


Fig.7 Capacitance

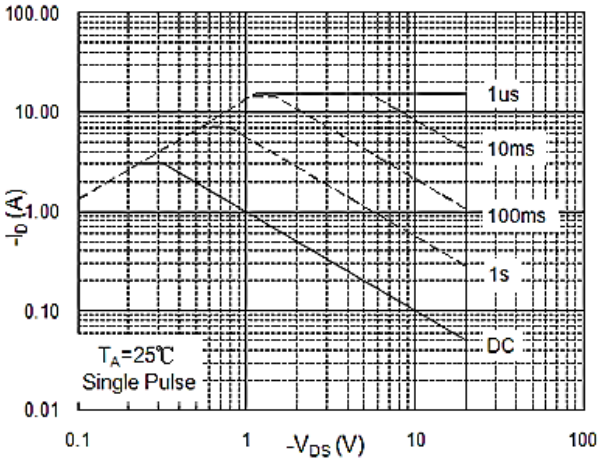


Fig.8 Safe Operating Area

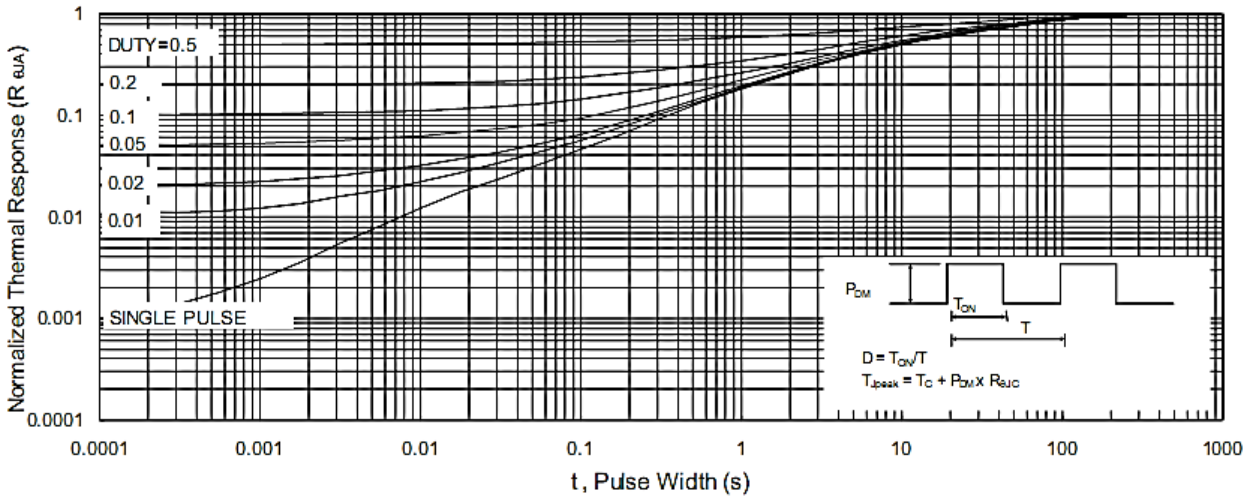


Fig.9 Normalized Maximum Transient Thermal Impedance

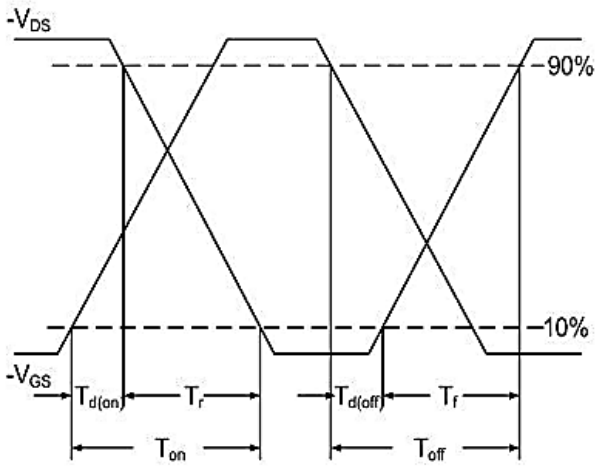


Fig.10 Switching Time Waveform

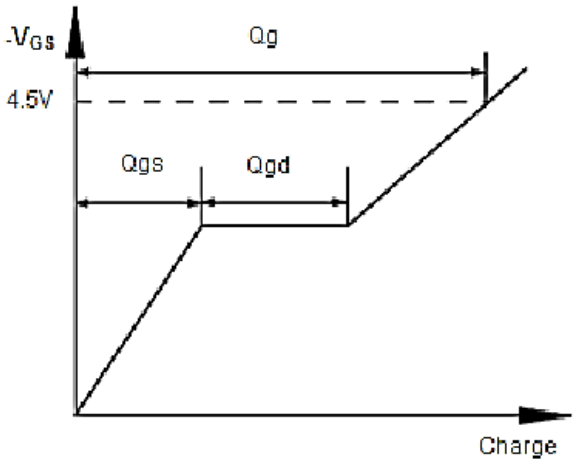


Fig.11 Gate Charge Waveform

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