



### **General Description**

The WSP4626 is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSP4626 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

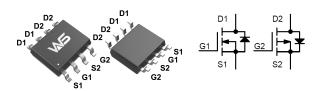
### **Product Summery**

BVDSS	RDSON	ID
20V	21mΩ	6.7A
-20V	48mΩ	-4.4A

### **Applications**

- ●MB/NB/UMPC/VGA
- DC-DC Power System
- Inverter

### **SOP-8 Pin Configuration**



### **Absolute Maximum Ratings**

		Rating		
Symbol	Parameter	N-Ch	P-Ch	Units
V <sub>DS</sub>	Drain-Source Voltage	20	-20	V
$V_{GS}$	Gate-Source Voltage	±10	±12	V
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current	6.7	-4.4	Α
I <sub>D</sub> @T <sub>A</sub> =70℃	Continuous Drain Current	5.3	-3.5	А
I <sub>DM</sub>	Pulsed Drain Current	20	-20	А
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation	2.0	2.0	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	-55 to 150	$^{\circ}$
$T_J$	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^{\circ}$

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{thJA}$	Thermal Resistance Junction-Ambient		62.5	°C/W





## Electrical Characteristics (T<sub>J</sub>=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	20			V
В	Static Drain-Source On-Resistance	$V_{GS}$ =4.5 $V$ , $I_D$ =6 $A$		21	28	· mΩ
R <sub>DS(ON)</sub>		$V_{GS}$ =2.5V , $I_D$ =5.2A		28	38	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ =250uA	0.4	0.72	1.2	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =20V , V <sub>GS</sub> =0V .			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}$ =0V			±100	nA
Qg	Total Gate Charge			10		
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =10V,V <sub>GS</sub> =4.5V,I <sub>D</sub> =6.5A.		1.4		nC
$Q_gd$	Gate-Drain Charge			3.2		
T <sub>d(on)</sub>	Turn-On Delay Time			14		
Tr	Rise Time	$V_{DD}$ =15V, $V_{GS}$ =4.5V, $R_{G}$ =6 $\Omega$ , $I_{D}$ =1A .		10		no
$T_{d(off)}$	Turn-Off Delay Time			34		ns
T <sub>f</sub>	Fall Time			11		
C <sub>iss</sub>	Input Capacitance			910		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V,f=1MHz .		230		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			163		
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			2.6	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A.			1.2	V



## P-Channel Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-20			V
В	Static Drain-Source On-Resistance	$V_{GS}$ =-4.5V , $I_D$ =-5A		48	65	mΩ
R <sub>DS(ON)</sub>		$V_{GS}$ =-2.5V , $I_D$ =-3A		62	85	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ =-250uA	-0.4	-0.68	-1.2	V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =-20V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm12V$ , $V_{DS}$ = $0V$			±100	nA
$Q_g$	Total Gate Charge			11.5		nC
$Q_gs$	Gate-Source Charge	$V_{DS}$ =-10V, $V_{GS}$ =-4.5V, $I_{D}$ =-4A.		3.5		
$Q_{gd}$	Gate-Drain Charge			3.3		
T <sub>d(on)</sub>	Turn-On Delay Time			22		
Tr	Rise Time	$V_{DD}$ =-10V, $V_{GS}$ =-4.5V , $R_G$ =3 $\Omega$ ,		15.7		ns
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =-4A .		59		
T <sub>f</sub>	Fall Time			5.5		
C <sub>iss</sub>	Input Capacitance			1415		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-10V,V <sub>GS</sub> =0V , f=1MHz.		134		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			102		
Is	Continuous Source Current	T <sub>A</sub> =25°C			-2.4	Α
V <sub>SD</sub>	Diode Forward Voltage	I <sub>SD</sub> = -1A , V <sub>GS</sub> =0V.			-1.2	V

A: The value of ReJA is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with TA=25°C. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.



### **N-Channel Typical Characteristics**

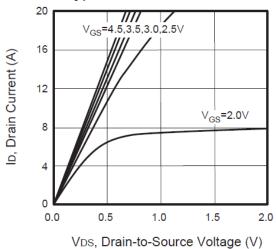


Figure 1. Output Characteristics

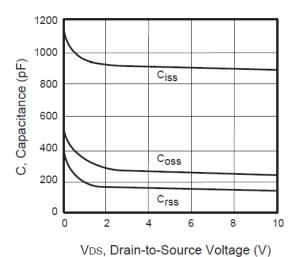


Figure 3. Capacitance

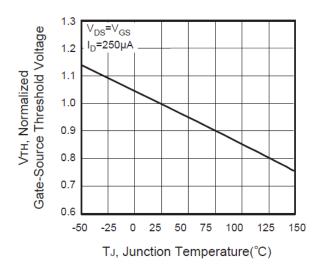


Figure 5. Gate Threshold Variation with Temperature

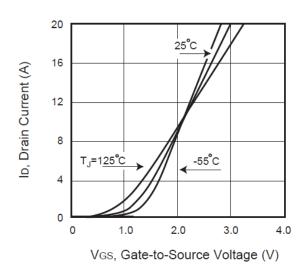


Figure 2. Transfer Characteristics

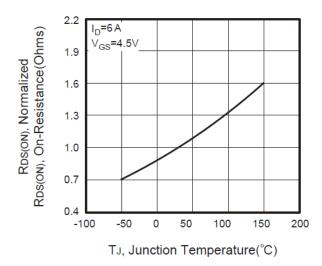


Figure 4. On-Resistance Variation with Temperature

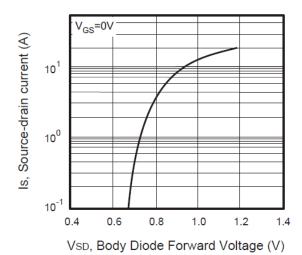


Figure 6. Body Diode Forward Voltage Variation with Source Current



**N-Ch and P-Channel MOSFET** 

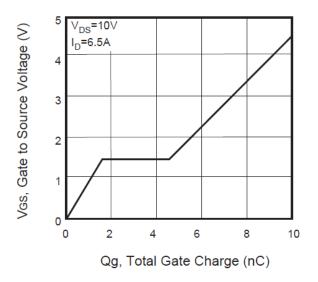


Figure 7. Gate Charge

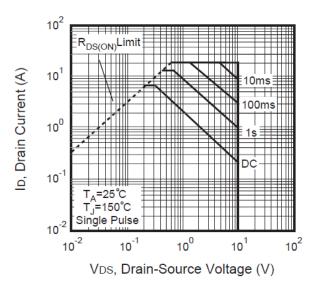


Figure 8. Maximum Safe Operating Area



### **P-Channel Typical Characteristics**

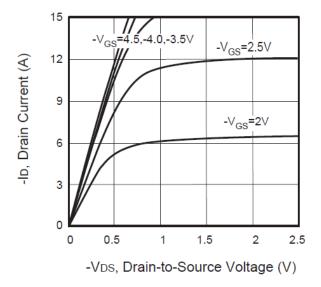


Figure 9. Output Characteristics

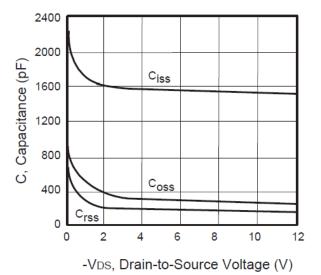


Figure 11. Capacitance

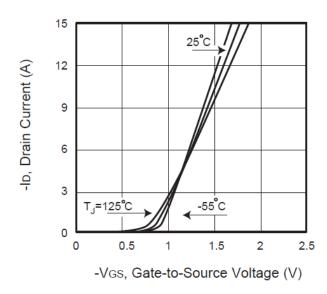


Figure 10. Transfer Characteristics

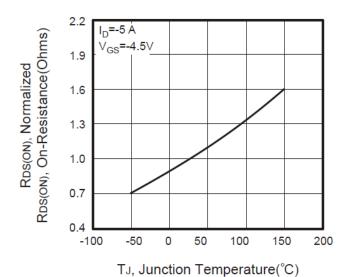


Figure 12. On-Resistance Variation with Temperature



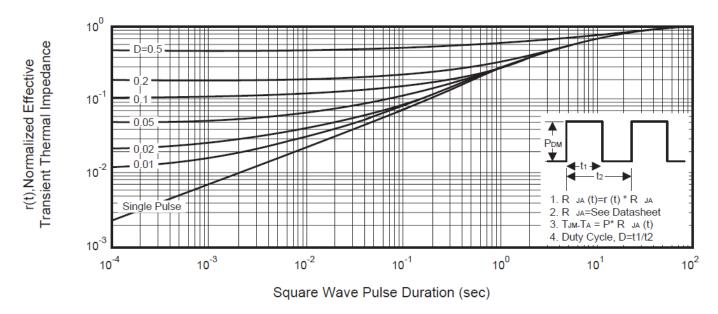


Figure 19. Normalized Thermal Transient Impedance Curve



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STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 IPS60R360PFD7SAKMA1
DMN2990UFB-7B SSM3K35CT,L3F IPLK60R1K0PFD7ATMA1 2N7002W-G MCAC30N06Y-TP IPWS65R035CFD7AXKSA1
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