General Description

The WSP6055 is the highest performance trench P-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSP6055 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

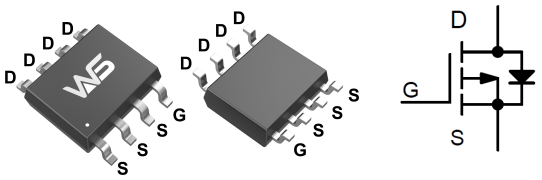
Product Summery

BVDSS	RDSON	ID
-55V	108mΩ	-6.8A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- CCFL Back-light Inverter

SOP-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-55	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current	-6.8	A
I_D@T_C=70℃	Continuous Drain Current	-4.4	A
I_{DM}	Pulsed Drain Current	-16	A
P_D@T_C=25℃	Total Power Dissipation	1.25	W
T_J/T_{STG}	Operating/Storage Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	---	125	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case	---	80	℃/W

P-Channel Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V , I_D=-3.5A	---	108	125	mΩ
		V_{GS}=-4.5V , I_D=-1A	---	125	155	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-1.6	-2.5	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-48V , V_{GS}=0V	---	---	-1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
Q_g	Total Gate Charge (-4.5V)	V_{DS} = -15V, I_D = -1.5A, V_{GS} = -10V	---	4.6	---	nC
Q_{gs}	Gate-Source Charge		---	1.4	---	
Q_{gd}	Gate-Drain Charge		---	1.6	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD} = -15V, I_D = -1A, V_{GS} = -10V, R_{GEN} = 3.3Ω	---	17	---	ns
T_r	Rise Time		---	5.5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	37	---	
T_f	Fall Time		---	2.5	---	
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}= -15V, f=1MHz	---	531	---	pF
C_{oss}	Output Capacitance		---	59	---	
C_{rss}	Reverse Transfer Capacitance		---	38	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	-1.7	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.

P-Channel Typical Characteristics

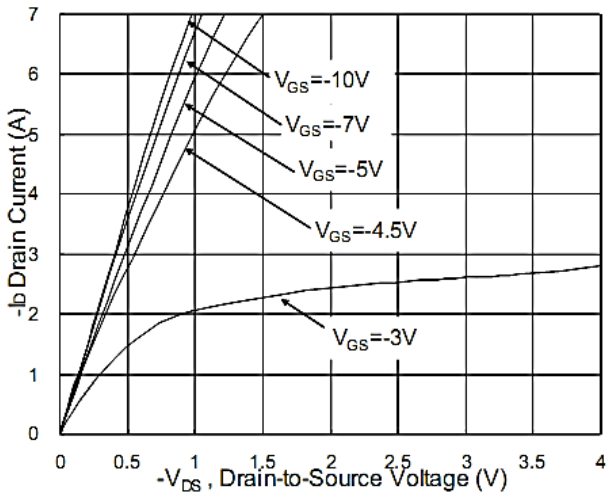


Fig.1 Typical Output Characteristics

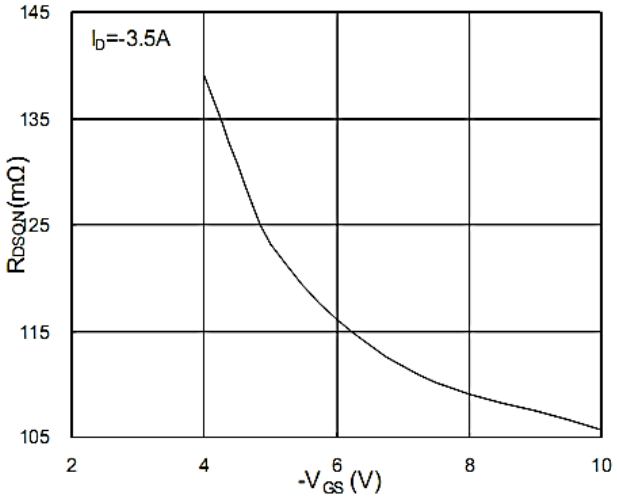


Fig.2 On-Resistance v.s Gate-Source

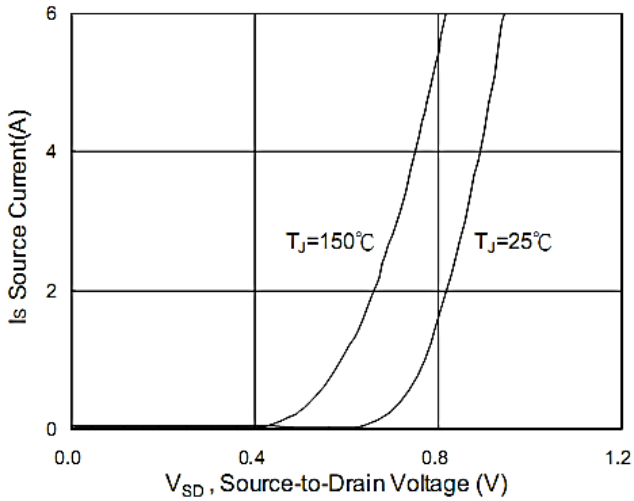


Fig.3 Forward Characteristics Of Reverse

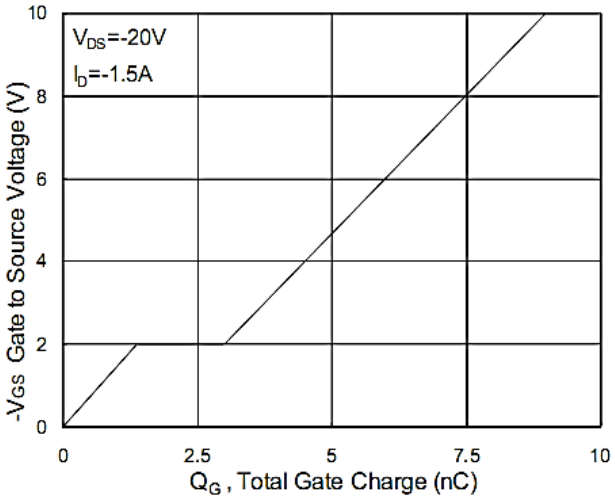


Fig.4 Gate-Charge Characteristics

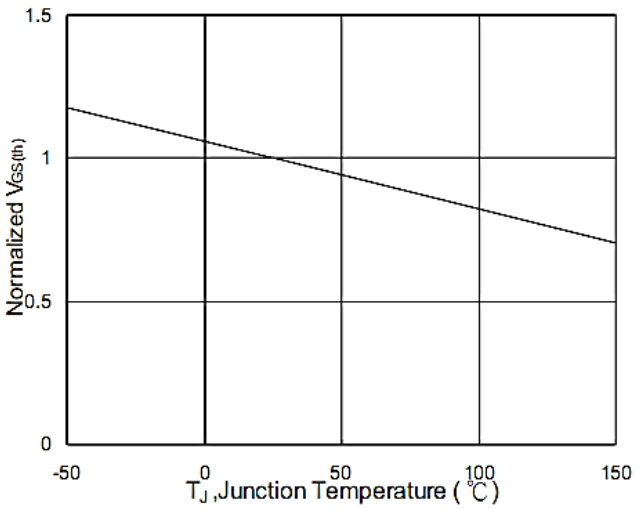


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

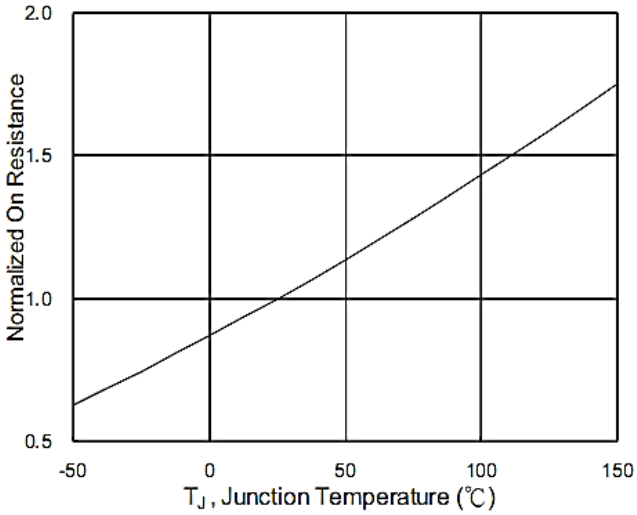


Fig.6 Normalized R_{DSON} v.s T_J

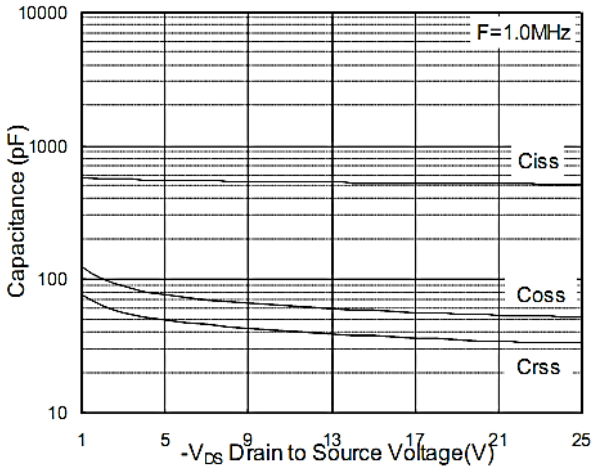


Fig.7 Capacitance

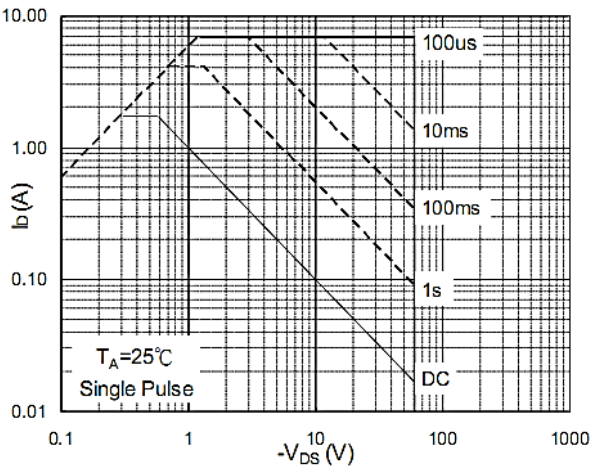


Fig.8 Safe Operating Area

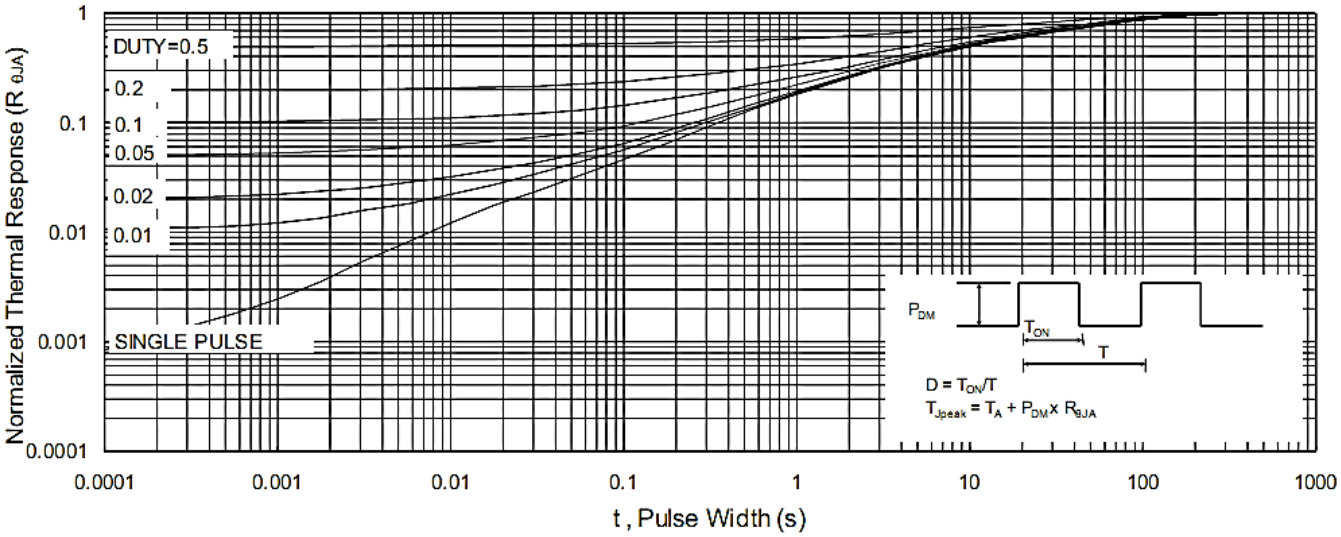


Fig.9 Normalized Maximum Transient Thermal Impedance

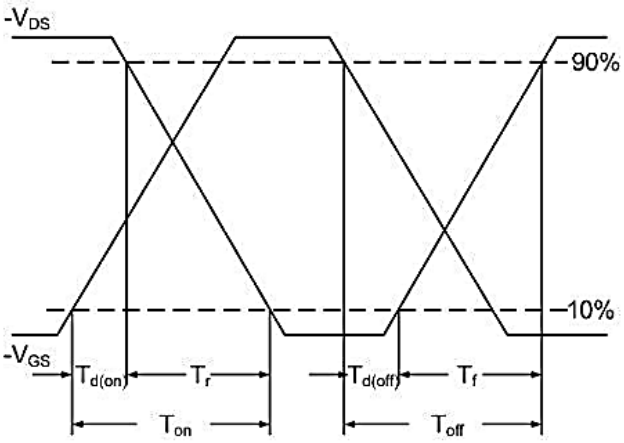


Fig.10 Switching time waveform

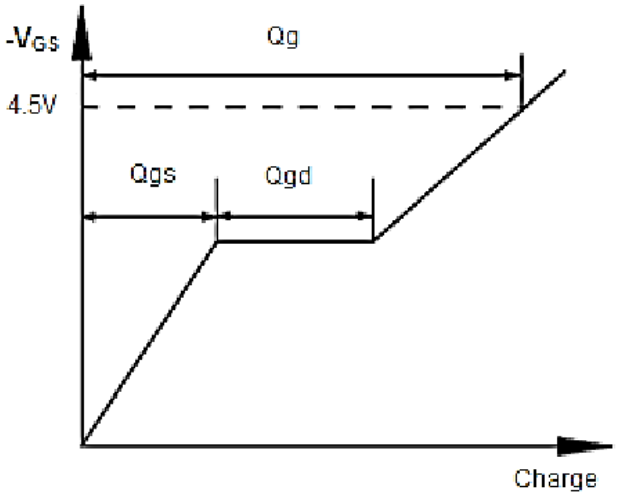


Fig.11 Gate Charge waveform

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