

General Description

The WSP85N10 is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density , which provide excellent $R_{DS(on)}$ and gate charge for most of the synchronous buck converter applications .

The WSP85N10 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

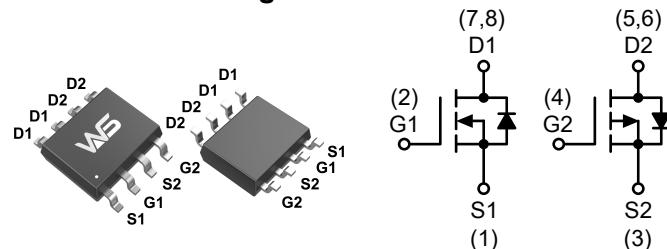
Product Summary

BV_{DSS}	$R_{DS(on)}$	I_D
100V	100m Ω	4.5A
-100V	150m Ω	-2.5A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- CCFL Back-light Inverter

SOP-8L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	100	-100	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Continuous Drain Current, $V_{GS(NP)}=10V, T_c=25^\circ C$	4.5*	-2.5*	A
	Continuous Drain Current, $V_{GS(NP)}=10V, T_c=100^\circ C$	2.5	-1.4	A
I_{DP}^a	Pulse Drain Current Tested, $V_{GS(NP)}=10V$	10	-7	A
E_{AS}^c	Avalanche Energy, Single pulse , L=0.5mH	6.25	12	mJ
I_{AS}^c	Avalanche Current, Single pulse , L=0.5mH	3	-5	A
P_D	Total Power Dissipation, $T_a=25^\circ C$	2.1	2.1	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T_J	Operating Junction Temperature Range	150	150	°C
R_{QJA}	Junction to Ambient,T<10s	60	60	°C/W
R_{QJA}	Junction to Ambient,Steady State ^b	95	95	°C/W

Note * : Max. current is limited by bonding wire.

Note a : Pulse width limited by max. junction temperature.

Note b : R_{QJA} steady state t=999s. R_{QJA} is measured with the device mounted on 1in², FR-4 board with 2oz. Copper.

Note c : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_j=25^\circ C$).

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	100	---	---	V
$R_{\text{DS(ON)}}^{\text{d}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=2.5\text{A}$	---	100	110	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=2\text{A}$	---	110	150	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	1.2	2.0	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	30	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2.5	3.6	Ω
Q_g^{e}	Total Gate Charge	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{DS}}=2.5\text{A}$	---	10	---	nC
Q_{gs}^{e}	Gate-Source Charge		---	2.4	---	
Q_{gd}^{e}	Gate-Drain Charge		---	3.0	---	
$T_{\text{d(on)}}^{\text{e}}$	Turn-On Delay Time	$V_{\text{DD}}=30\text{V}$, $R_L=30\text{R}$, $I_{\text{DS}}=1\text{A}$, $V_{\text{GEN}}=10\text{V}$, $R_G=6\text{R}$.	---	7	---	ns
T_r^{e}	Rise Time		---	9	---	
$T_{\text{d(off)}}^{\text{e}}$	Turn-Off Delay Time		---	5	---	
T_f^{e}	Fall Time		---	19	---	
$C_{\text{iss}}^{\text{e}}$	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	445	---	pF
$C_{\text{oss}}^{\text{e}}$	Output Capacitance		---	32	---	
$C_{\text{rss}}^{\text{e}}$	Reverse Transfer Capacitance		---	16	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	2.5	A
V_{SD}^{d}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=2.5\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.3	V

Note d : Pulse test ; pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Note e : Guaranteed by design, not subject to production testing.

P-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

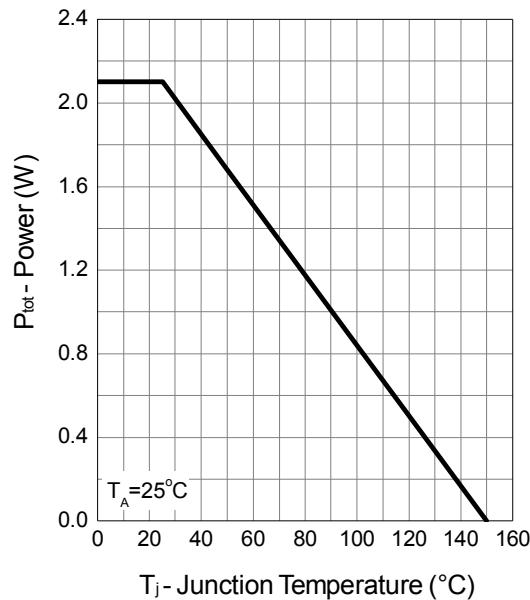
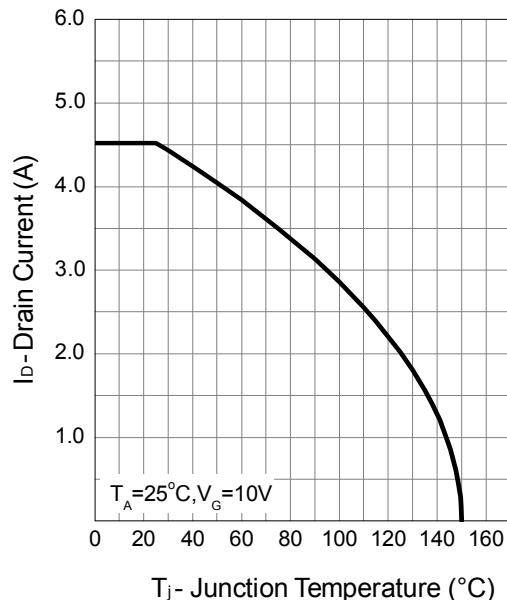
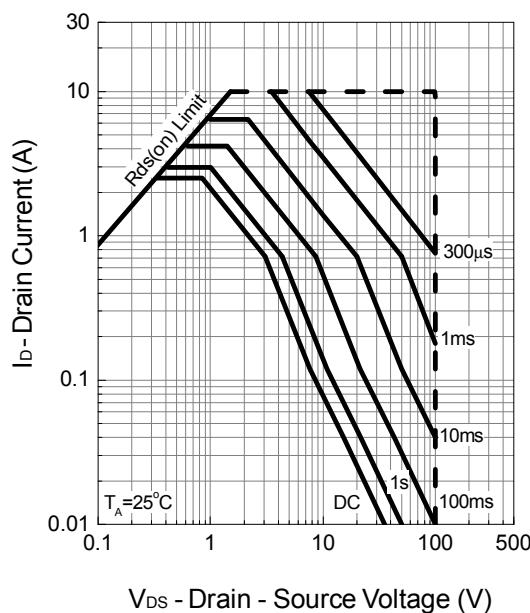
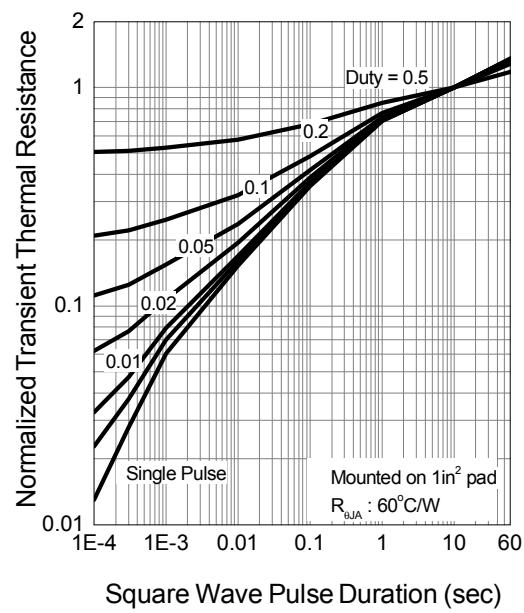
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-100	---	---	V
$R_{\text{DS(ON)}}^{\text{d}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-1.7\text{A}$	---	150	180	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-1\text{A}$	---	170	210	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1.3	-1.8	-2.3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	uA
		$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	-30	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Q_g^{e}	Total Gate Charge	$V_{\text{DS}}=-50\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-1.7\text{A}$	---	11	---	nC
Q_{gs}^{e}	Gate-Source Charge		---	2.0	---	
Q_{gd}^{e}	Gate-Drain Charge		---	3.0	---	
$T_{\text{d(on)}}^{\text{e}}$	Turn-On Delay Time	$V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_G=6\Omega$, $I_{\text{D}}=-1\text{A}$, $R_L=15\Omega$,	---	5	---	ns
T_r^{e}	Rise Time		---	7	---	
$T_{\text{d(off)}}^{\text{e}}$	Turn-Off Delay Time		---	22	---	
T_f^{e}	Fall Time		---	25	---	
$C_{\text{iss}}^{\text{e}}$	Input Capacitance	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	450	---	pF
$C_{\text{oss}}^{\text{e}}$	Output Capacitance		---	32	---	
$C_{\text{rss}}^{\text{e}}$	Reverse Transfer Capacitance		---	18	---	

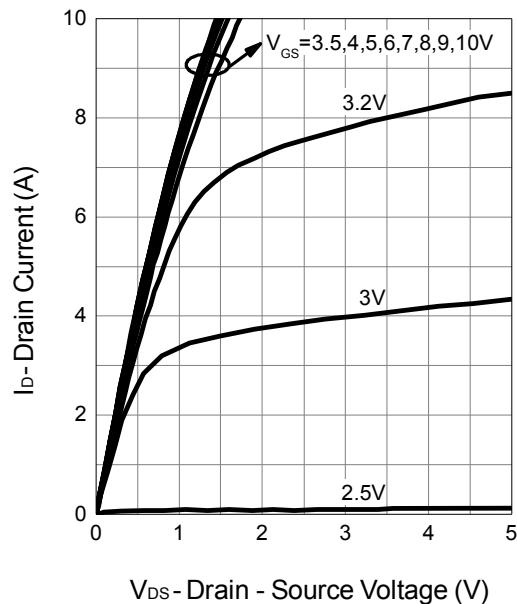
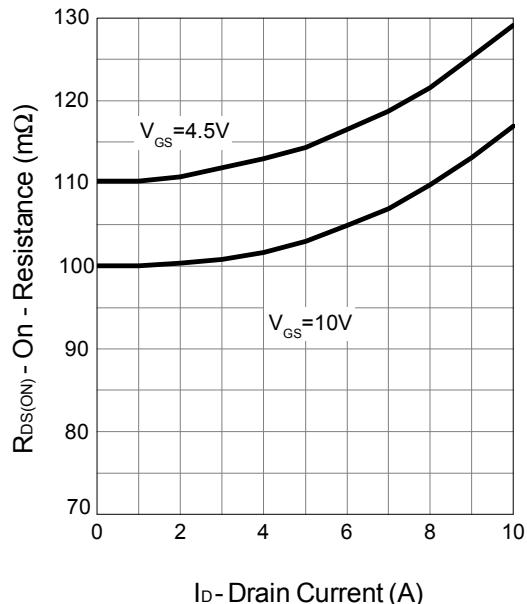
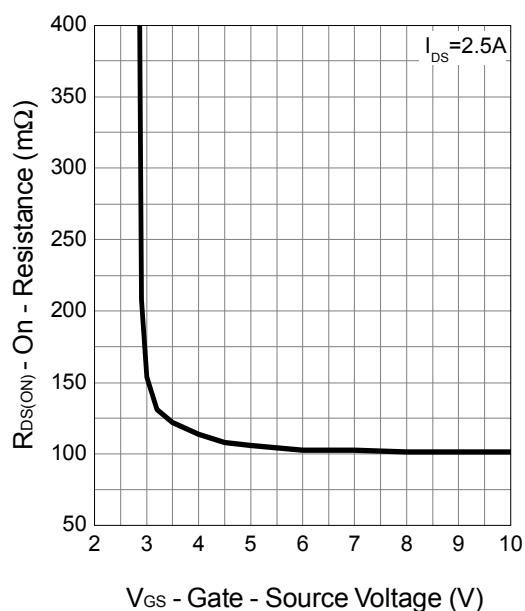
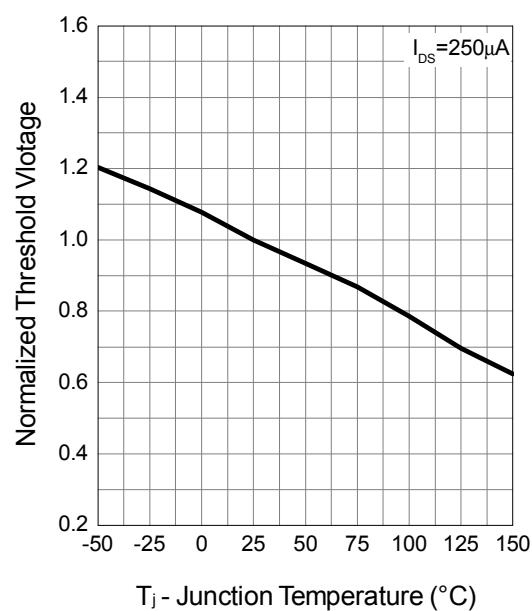
Diode Characteristics

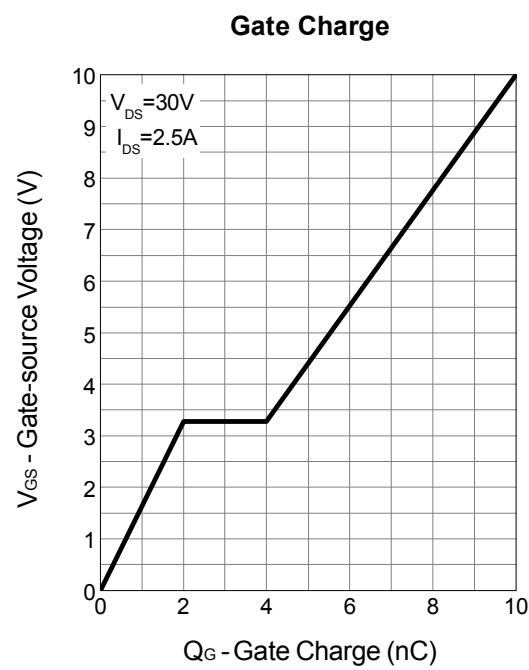
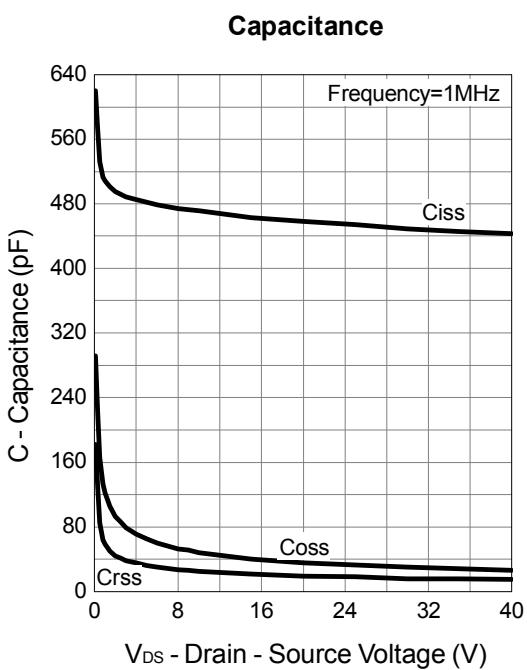
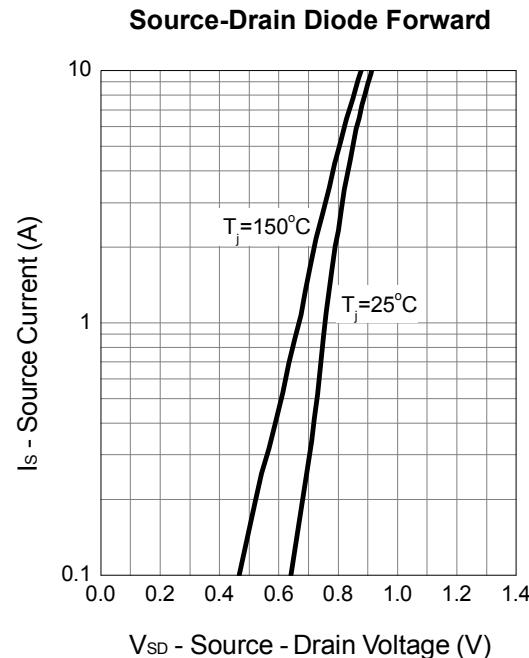
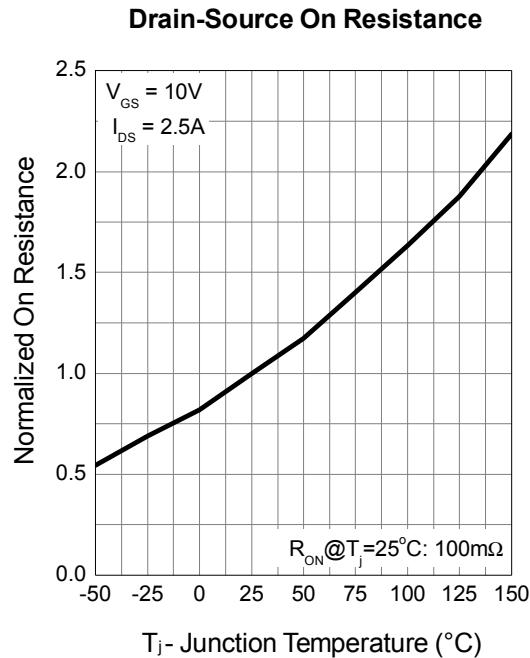
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-1.7	A
V_{SD}^{e}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=-1.7\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1.2	V

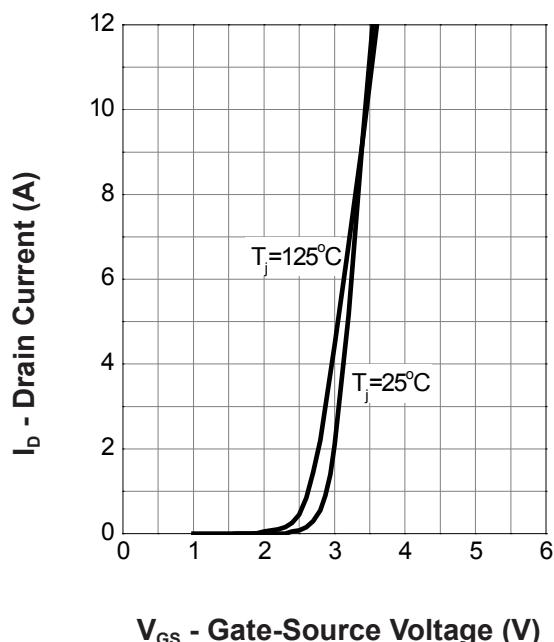
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Note e : Guaranteed by design, not subject to production testing.

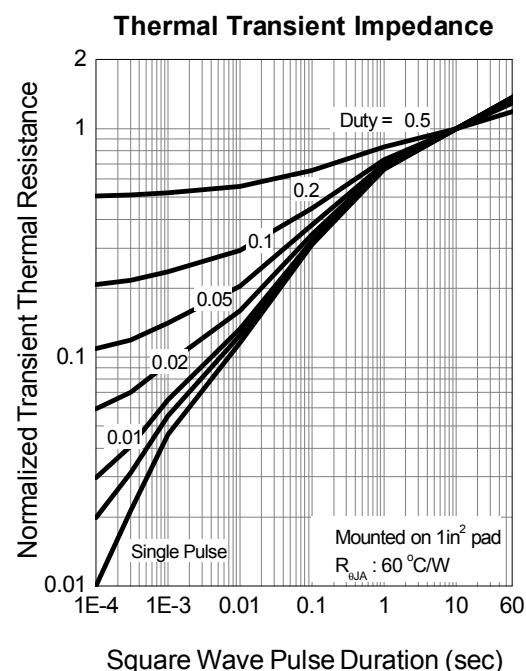
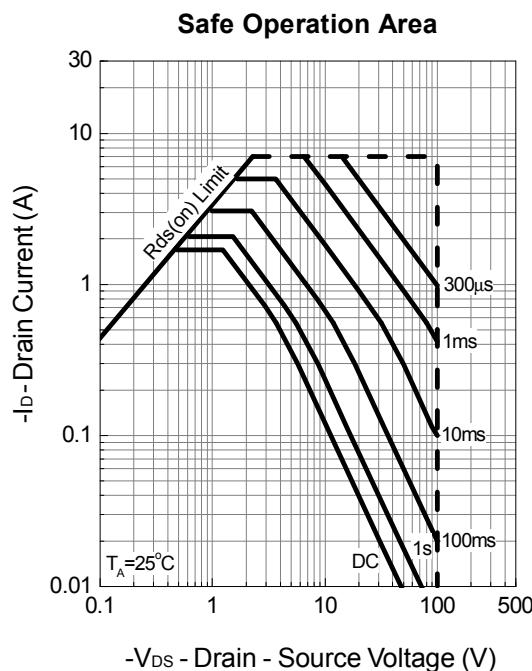
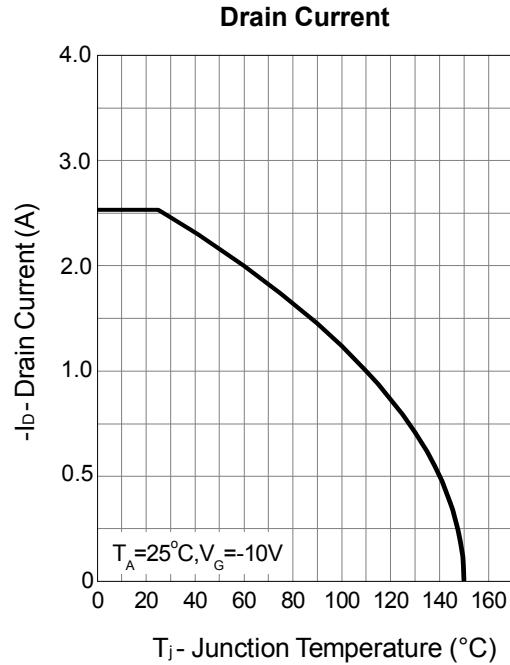
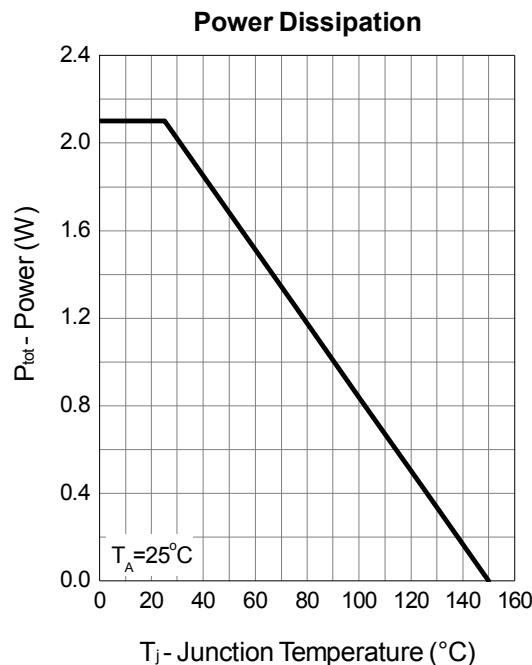
N-Channel Typical Characteristics
Power Dissipation

Drain Current

Safe Operation Area

Thermal Transient Impedance


N-Channel Typical Characteristics
Output Characteristics

Drain-Source On Resistance

Gate-Source On Resistance

Gate Threshold Voltage


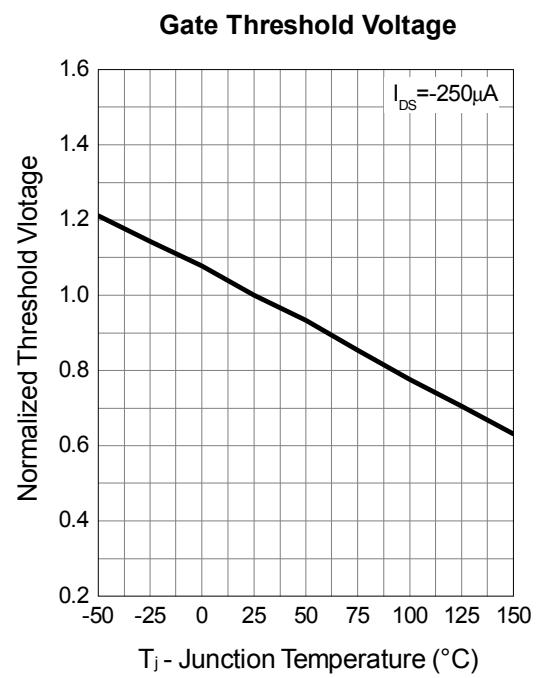
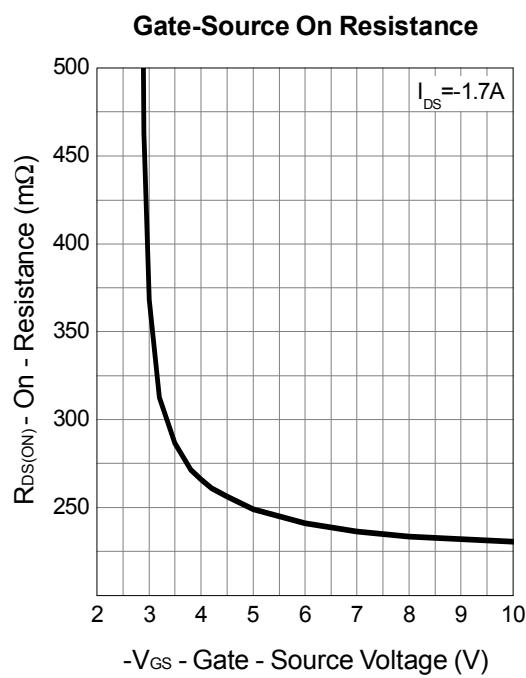
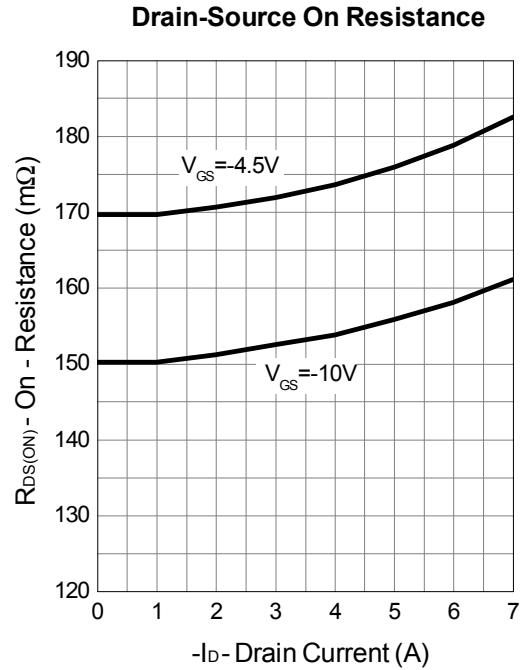
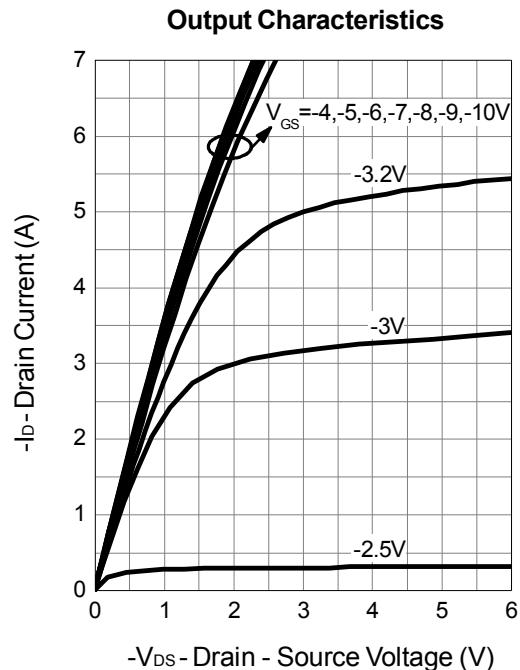
N-Channel Typical Characteristics


N-Channel Typical Characteristics**Transfer Characteristics**

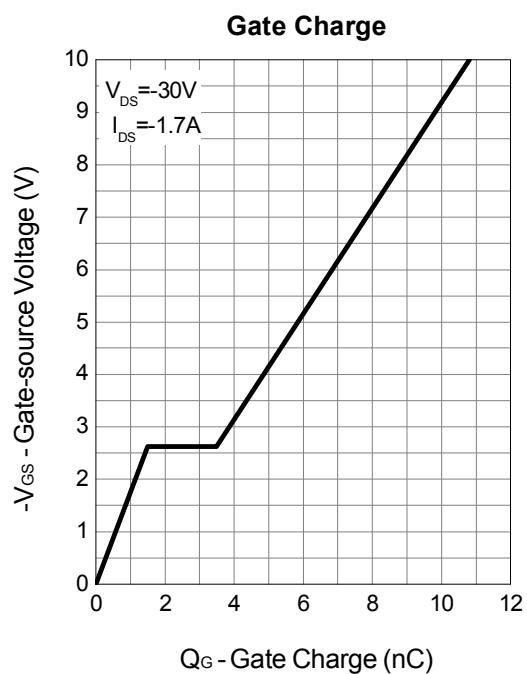
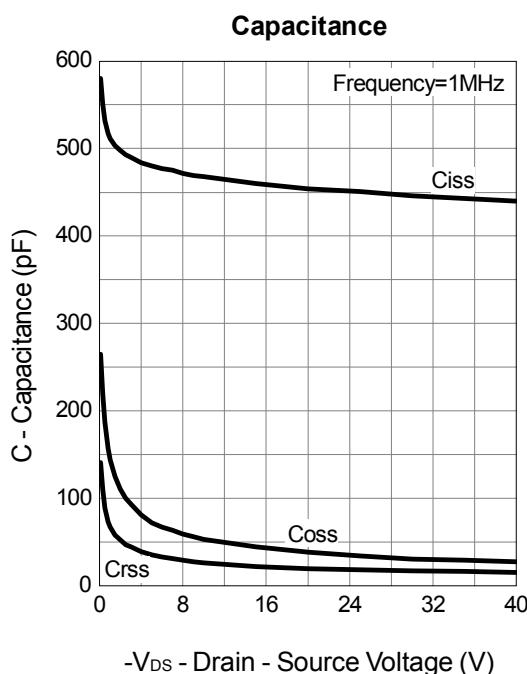
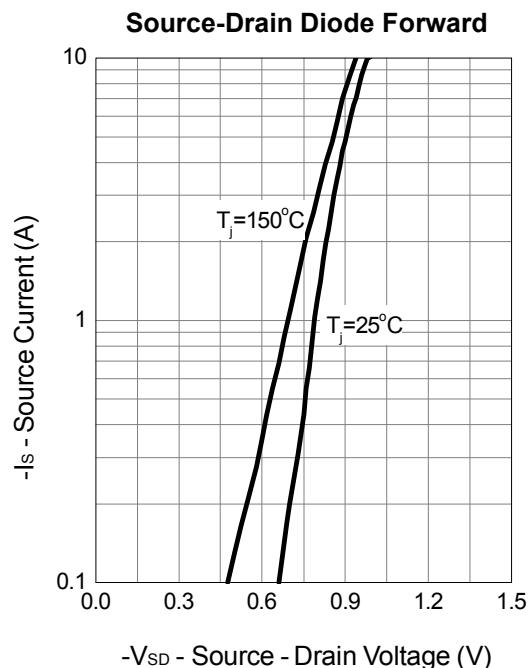
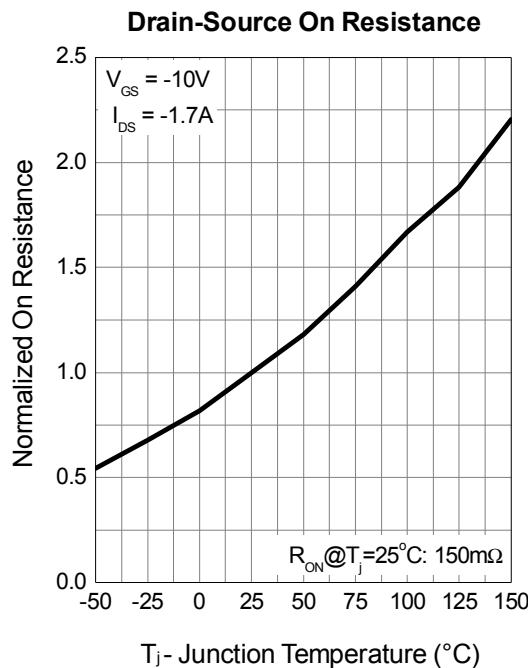
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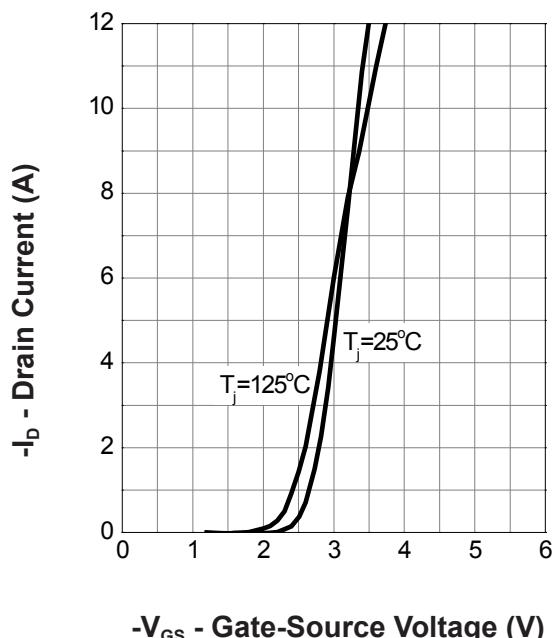


P-Channel Typical Characteristics

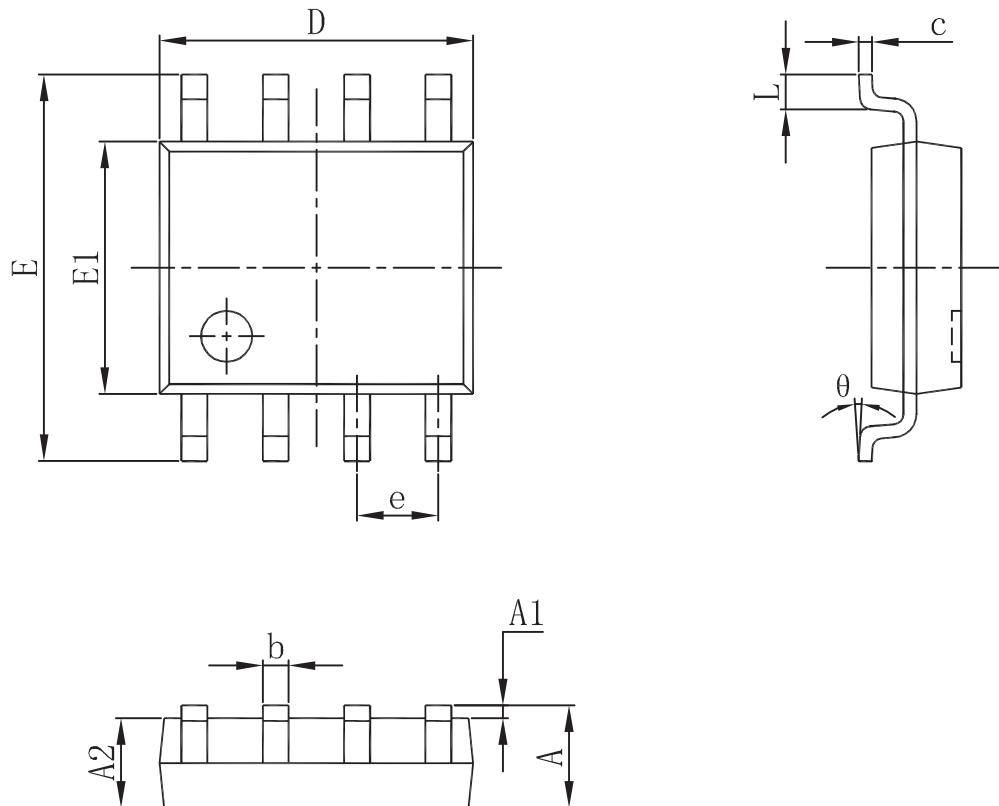


P-Channel Typical Characteristics



P-Channel Typical Characteristics**Transfer Characteristics**

Packaging information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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