

General Description

The WSP9435 is the highest performance trench P-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSP9435 meet the RoHS and Green Product requirement, with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

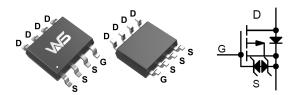
Product Summery

BVDSS	RDSON	ID
-30V	38mΩ	-5.4A

Applications

- Power Management in Notebook Computer,
 Portable Equipment and Battery Powered
 Systems
- ESD:2KV

SOP-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	-30	V	
V_{GS}	Gate-Source Voltage	±20	V	
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ -10V ¹	-5.4	Α	
I _D @T _A =70℃	Continuous Drain Current, V _{GS} @ -10V ¹	-4.5	Α	
I _{DM}	Pulsed Drain Current ^{1,2}	-21	Α	
P _D @T _A =25℃	Total Power Dissipation ³	2.5	W	
T _{STG}	Storage Temperature Range -55 to 150		$^{\circ}$ C	
T _J	Operating Junction Temperature Range	-55 to 150	$^{\circ}$	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{0JA}	Thermal Resistance Junction-Ambient ¹		80	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		30	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.023		V/°C
В	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-5.4A		38	48	mΩ
R _{DS(ON)}		V_{GS} =-4.5V , I_D =-2A		60	78	
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} . I _D =-250uA	-1.0	-1.5	-2.3	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =-250UA		4		mV/℃
	Drain Source Leakage Current	V_{DS} =-24V , V_{GS} =0V , T_J =25 $^{\circ}$ C			-1	uA
I _{DSS}	Drain-Source Leakage Current	V_{DS} =-24V , V_{GS} =0V , T_J =55 $^{\circ}$ C			-5	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20V$, V_{DS} = $0V$			±100	nA
gfs	Forward Transconductance	V_{DS} =-5 V , I_{D} =-4 A		11		S
Q_g	Total Gate Charge (-4.5V)			13	19	
Q_gs	Gate-Source Charge	V _{DS} =-15V , V _{GS} =-10V , I _D =-5.4A		1.3	2.2	nC
Q_gd	Gate-Drain Charge			3.1	2.7	
T _{d(on)}	Turn-On Delay Time			8	9.6	
Tr	Rise Time	V_{DD} =-15V , V_{GS} =-10V , R_{G} =6 Ω , I_{D} =-1A , R_{L} =10 Ω .		13	15.1	- ns
$T_{d(off)}$	Turn-Off Delay Time			26	36	
T _f	Fall Time			7	12.0	
C _{iss}	Input Capacitance			642		
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		76		pF
C _{rss}	Reverse Transfer Capacitance			66		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,4}	V _G =V _D =0V , Force Current			-2.0	Α
I _{SM}	Pulsed Source Current ^{2,4}	V _G =V _D =0V , Force Current			-21	Α
V_{SD}	Diode Forward Voltage ²	V_{GS} =0V , I_{S} =-1A , T_{J} =25 $^{\circ}$ C			-1.2	V
t _{rr}	Reverse Recovery Time			13		nS
Q _{rr}	Reverse Recovery Charge	IF=-5.4A,dI/dt=100A/ μ s , T_J =25 $^{\circ}$ C		7		nC

^{1.} The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper ,t≤10sec.

^{2.}The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2% 3.The power dissipation is limited by 150 $^{\circ}$ C junction temperature

^{4.} The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

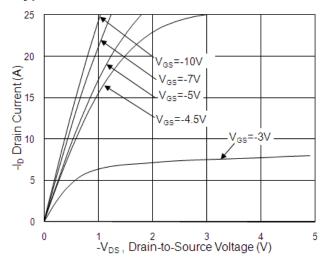


Fig.1 Typical Output Characteristics

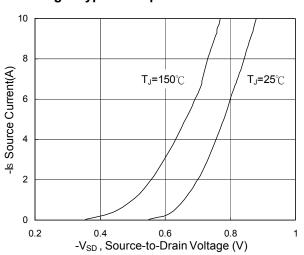


Fig.3 Forward Characteristics of Reverse

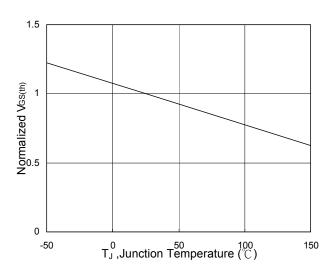


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

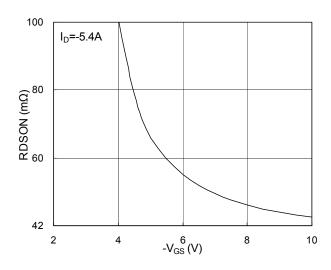


Fig.2 On-Resistance vs. Gate-Source

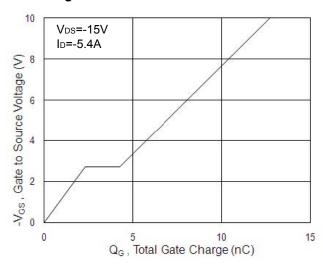


Fig.4 Gate-Charge Characteristics

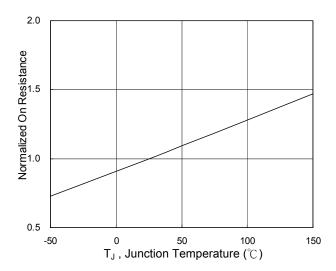
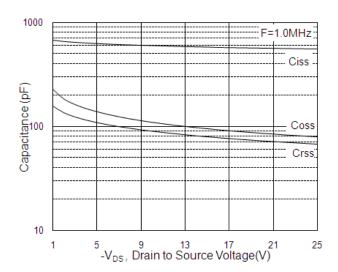


Fig.6 Normalized R_{DSON} vs. T_J





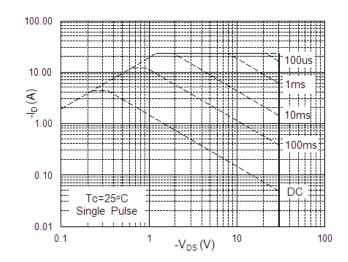


Fig.7 Capacitance

Fig.8 Safe Operating Area

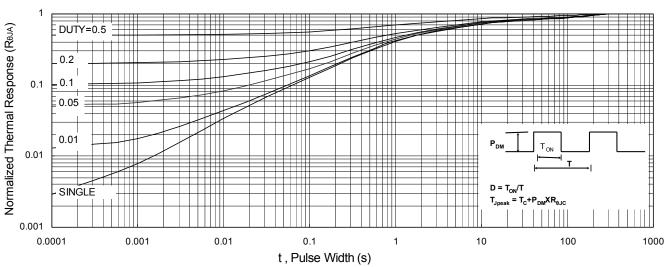
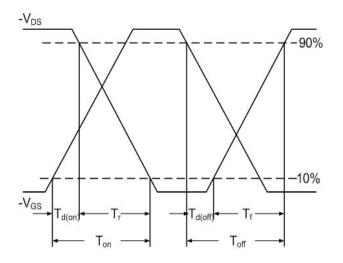


Fig.9 Normalized Maximum Transient Thermal Impedance



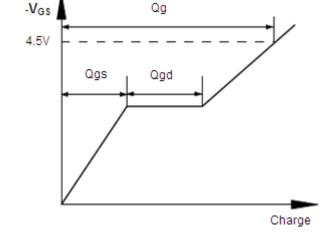


Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform



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