

General Description

The WSR180N08 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSR180N08 meet the RoHS and Green Product requirement,100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

Product Summery

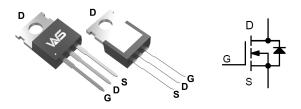
BV _{DSS}	R _{DSON}	I _D
85V	$3.2 m\Omega$	180A

Applications

Switching application

Power Management for Inverter Systems.

TO-220AB-3L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter Rating		Units
V_{DS}	Drain-Source Voltage 85		V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V	180	Α
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V	125	Α
I _{DM}	Pulsed Drain Current ² -T _C =25°C 00		Α
EAS	Avalanche Energy, Single pulse,L=0.5mH 140		mJ
I _{AS}	Avalanche Current, Single pulse,L=0.5mH	5	Α
P _D @T _C =25℃	Total Power Dissipation	250	W
P _D @T _C =100℃	Total Power Dissipation	100	W
T _{STG}	Storage Temperature Range -55 to 150		℃
TJ	Operating Junction Temperature Range 150		$^{\circ}$

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient		62.5	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case		0.5	°C/W



Electrical Characteristics (T_J=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	85			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.096		V/℃
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V,I _D =40A		3.2	4.0	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} . I _D =250uA	2.0	3.0	4.0	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =250uA		-5.5		mV/℃
	Drain Source Leakage Current	V_{DS} =80V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1	
I _{DSS}	Drain-Source Leakage Current	V_{DS} =80V , V_{GS} =0V , T_J =55 $^{\circ}$ C			10	- uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 25V$, V_{DS} = $0V$			±100	nA
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.2		Ω
Q_g	Total Gate Charge (10V)	V _{DS} =30V , V _{GS} =10V , I _D =40A		95		
Q _{gs}	Gate-Source Charge			28		nC
Q_gd	Gate-Drain Charge			23]
T _{d(on)}	Turn-On Delay Time			27		
Tr	Rise Time	V_{DD} =30V , V_{GS} =10V , R_G =6 Ω , I_D =1A		18		
T _{d(off)}	Turn-Off Delay Time			140		ns
T _f	Fall Time			94		
C _{iss}	Input Capacitance	V _{DS} =30V , V _{GS} =0V , f=1MHz		6750		
C _{oss}	Output Capacitance			945		pF
C _{rss}	Reverse Transfer Capacitance			258		

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy	V _{DD} =40V , L=0.5mH , I _{AS} = 5A	1000			mJ

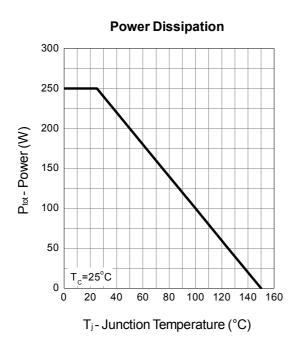
Diode Characteristics

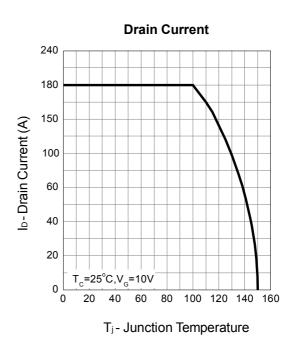
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			80	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =40A , T _J =25℃			1.2	V
t _{rr}	Reverse Recovery Time	IF=40A,dI/dt=100A/µs,T _J =25℃		58		nS
Q _{rr}	Reverse Recovery Charge			135		nC

Note * : Pulse test ; pulse width \leq 300 μ s, duty cycle \leq 2%.

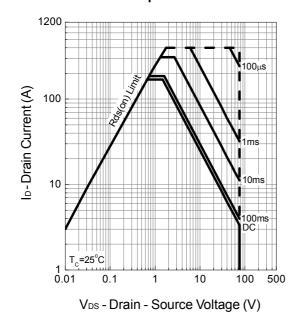


Typical Operating Characteristics

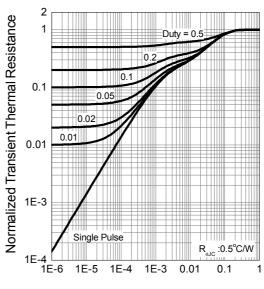




Safe Operation Area



Thermal Transient Impedance

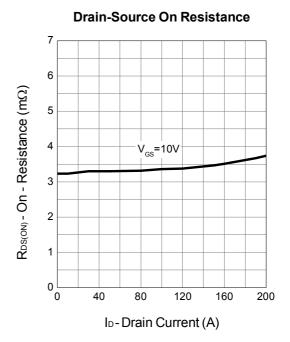


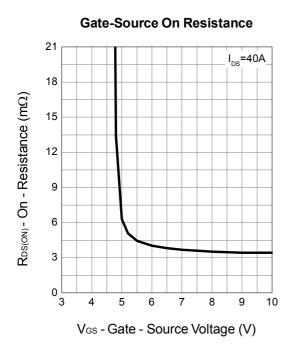
Square Wave Pulse Duration (sec)

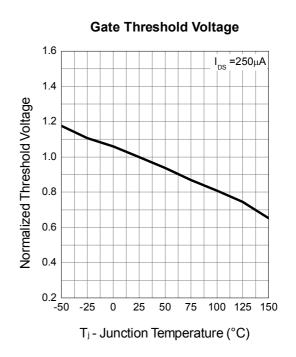


Typical Operating Characteristics

V_{DS} - Drain - Source Voltage (V)

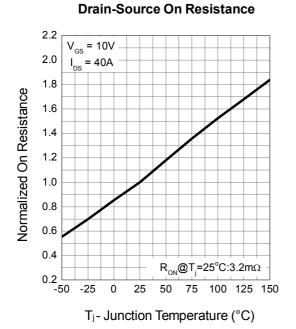




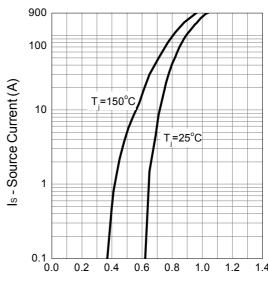




Typical Operating Characteristics

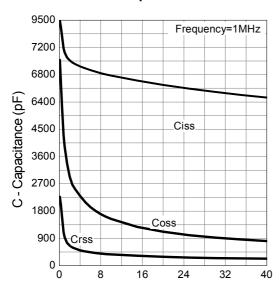


Source-Drain Diode Forward



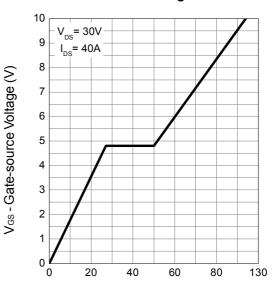
Vsp - Source - Drain Voltage (V)

Capacitance



V_{DS} - Drain-Source Voltage (V)

Gate Charge



Q_G-Gate Charge (nC)



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