

General Description

The WST2N7002 is the highest performance trench N-CH MOSFET with extreme high cell density, which provide excellent R_{DS(on)} and gate charge for most of the small power switching and load switch applications.

The WST2N7002 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- High-speed switching
- Green Device Available
- ESD Protected:2KV

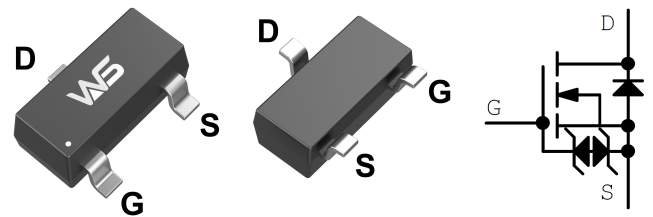
Product Summary

BVDSS	R _{DS(on)}	I _D
60V	2Ω	180mA

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC
- Networking DC-DC Power System
- Load Switch

SOT-23N Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	180	mA
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	150	mA
I _{DM}	Pulsed Drain Current ²	1.2	A
P _D @T _A =25°C	Total Power Dissipation ³	0.2	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	625	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	60	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.05	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =0.5A	---	2	6.6	Ω
		V _{GS} =4.5V, I _D =0.2A	---	3	8	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	1	---	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-3.7	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =60V, V _{GS} =0V, T _J =25°C	---	---	1	μA
		V _{DS} =60V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±10	μA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =0.3A	---	940	---	mS
T _{d(on)}	Turn-On Delay Time	V _{DD} =30V, V _{GS} =10V, R _G =3.3Ω, I _D =0.5A	---	3	6	ns
T _r	Rise Time		---	1.8	3.3	
T _{d(off)}	Turn-Off Delay Time		---	8	16	
T _f	Fall Time		---	6.8	13.6	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	18	56	pF
C _{oss}	Output Capacitance		---	12	17	
C _{rss}	Reverse Transfer Capacitance		---	7.6	10.6	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	180	mA
I _{SM}	Pulsed Source Current ^{2,4}		---	---	1.2	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature.
- 4.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

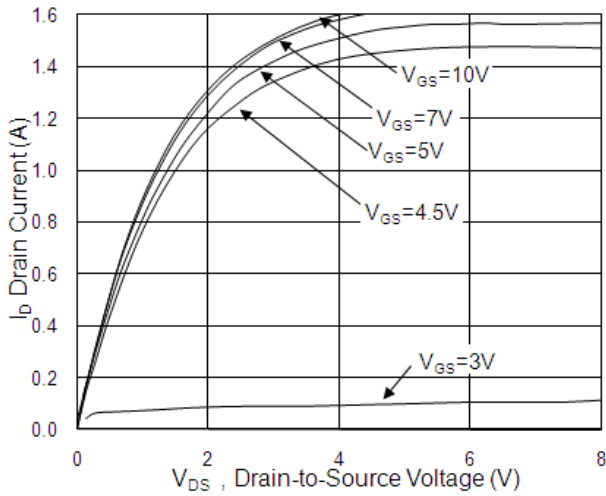


Fig.1 Typical Output Characteristics

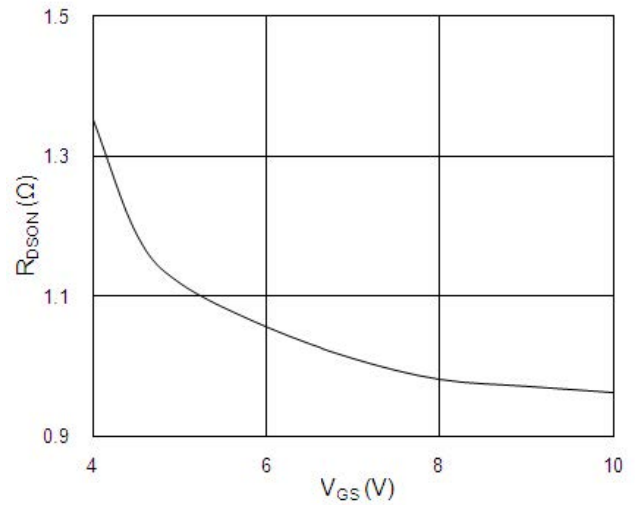


Fig.2 On-Resistance vs. Gate-Source Voltage

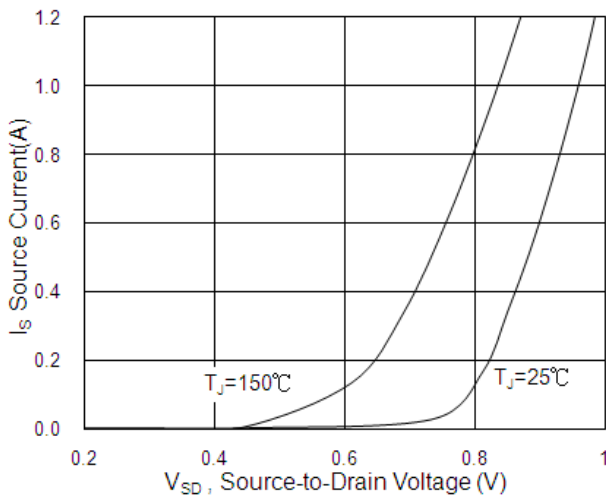


Fig.3 Forward Characteristics of Reverse

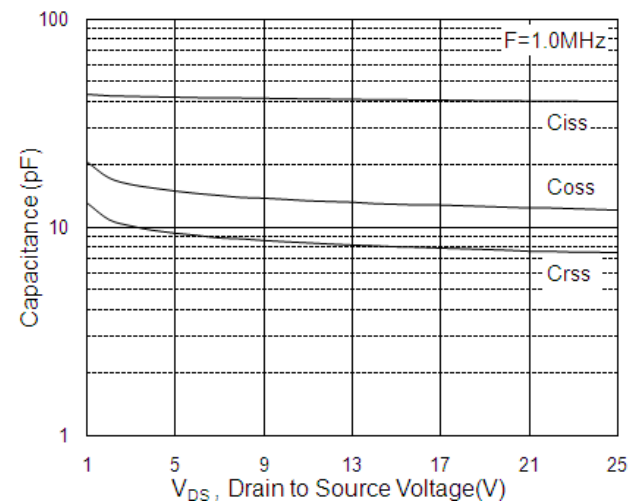


Fig.4 Capacitance

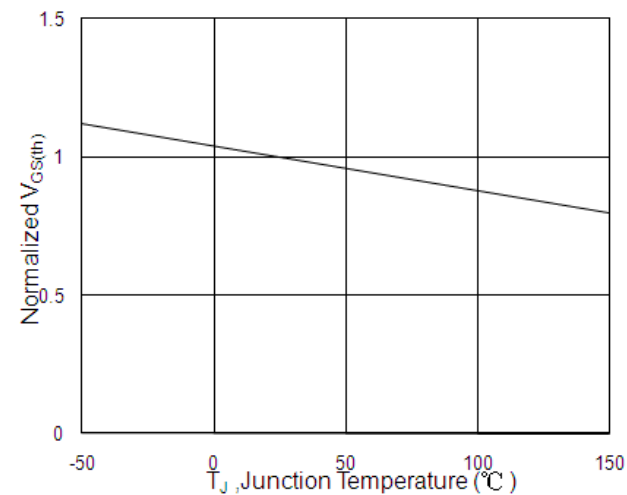


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

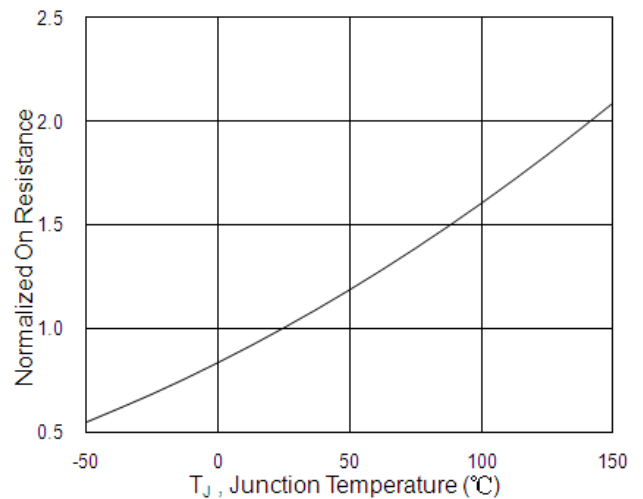


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

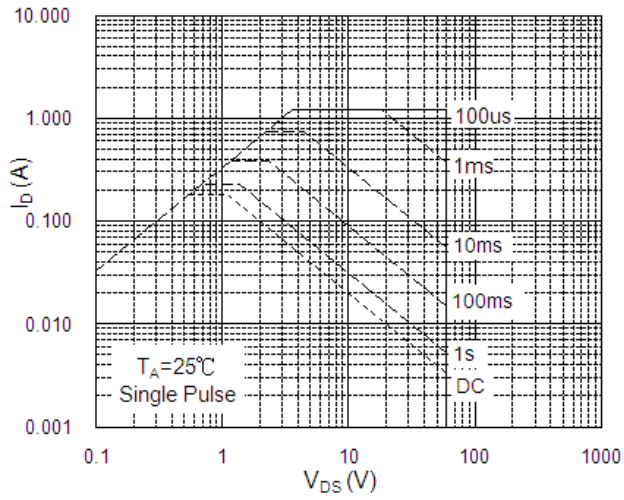


Fig.8 Safe Operating Area

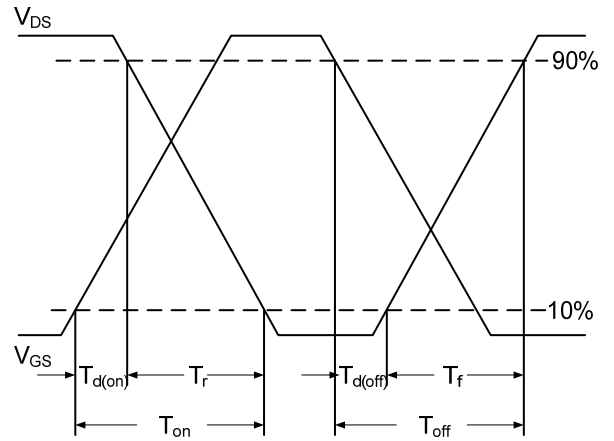


Fig.10 Switching Time Waveform

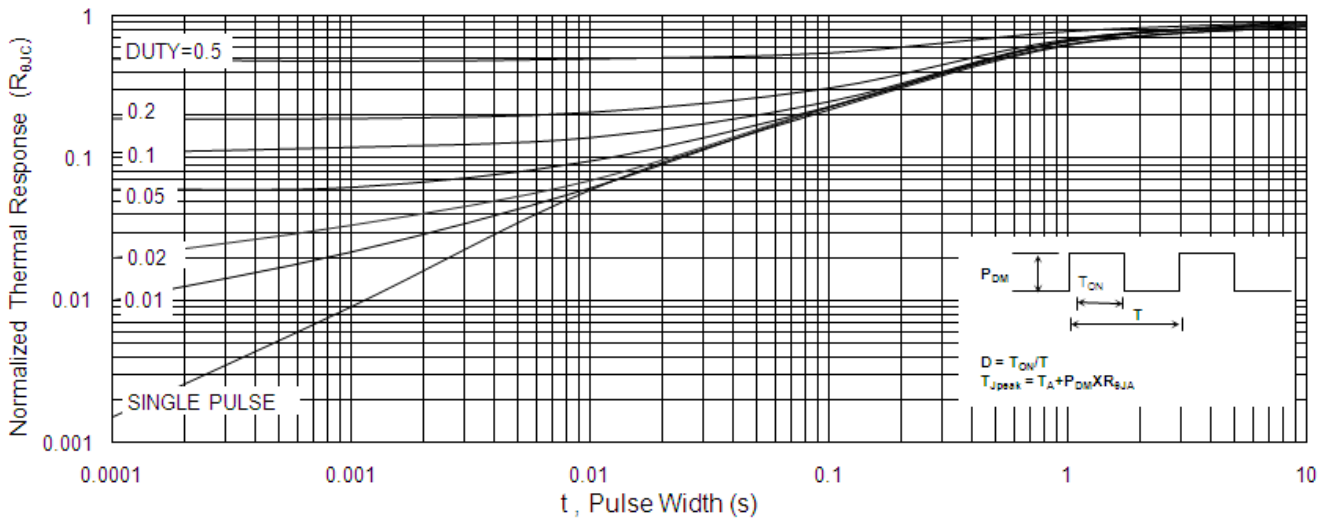


Fig.9 Normalized Maximum Transient Thermal Impedance



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