

**N-Ch MOSFET** 

#### **General Description**

The WST3408 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WST3408 meet the RoHS and Green Product requirement with full function reliability approved.

#### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline

**Absolute Maximum Ratings** 

• Green Device Available

### **Product Summery**

BVDSS	RDSON	ID
30V	26mΩ	5.5A

#### Applications

- High Frequency Point-of-Load Synchronous s Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

#### SOT-23-3L Pin Configuration



Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	5.5	А
I <sub>D</sub> @T <sub>C</sub> =70℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	4.5	А
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	20	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	24	mJ
I <sub>AS</sub>	Avalanche Current	8	А
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>4</sup>	1.5	W
T <sub>STG</sub>	Storage Temperature Range -55 to 150		°C
TJ	Operating Junction Temperature Range -55 to 150		°C

### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>ejA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>		90	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>		75	°C/W



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#### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\!{\rm C}$ , I_D=1mA		0.023		V/℃
Б	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =5.8A		26	32	
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		37	44	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage		1.0	1.4	2.0	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	──V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-4.2		mV/℃
	Dursin Source Lookene Surrent	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			5	uA uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}$ =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =6A		15		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.4	2.0	Ω
Qg	Total Gate Charge (4.5V)			7.6	9.9	
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> 5.8A		1.3	1.7	nC
Q <sub>gd</sub>	Gate-Drain Charge			1.7	2.2	
T <sub>d(on)</sub>	Turn-On Delay Time			10.1	20.3	
Tr	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ ,		3.2	6.3	
T <sub>d(off)</sub>	Turn-Off Delay Time	 I <sub>D</sub> =1A, RL=15Ω.		22.2	44.4	ns
T <sub>f</sub>	Fall Time			3	6	
Ciss	Input Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		391		
C <sub>oss</sub>	Output Capacitance			86.2		рF
C <sub>rss</sub>	Reverse Transfer Capacitance			59.4		

#### **Guaranteed Avalanche Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy <sup>5</sup>	V <sub>DD</sub> =25V , L=0.1mH , I <sub>AS</sub> =8A	20			mJ

### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	$V_G = V_D = 0V$ , Force Current			3	А
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>				15	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF=8A,dI/dt=100A/µs,Tյ=25℃		7.8		nS
Qrr	Reverse Recovery Charge			2.1		nC

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,t<10sec.

2.The data tested by pulsed , pulse width  $\,\leq\,$  300us , duty cycle  $\,\leq\,$  2%

3. The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}$ =25V, $V_{\text{GS}}$ =10V,L=0.1mH,I<sub>AS</sub>=8A

4.The power dissipation is limited by 150  $^\circ\!\!\mathbb{C}$  junction temperature

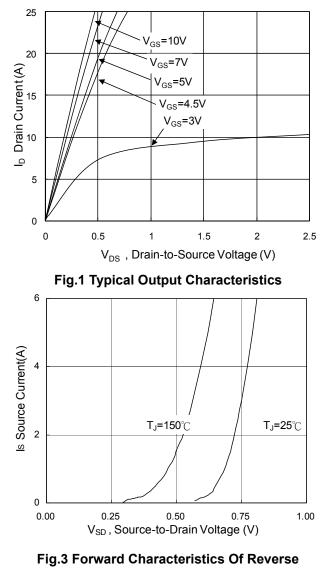
5.The Min. value is 100% EAS tested guarantee.

6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



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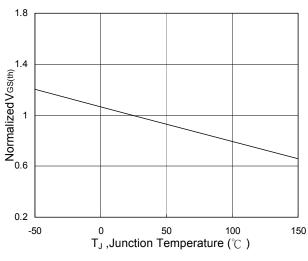


Fig.5 Normalized  $V_{GS(th)}\,vs.\,T_J$ 

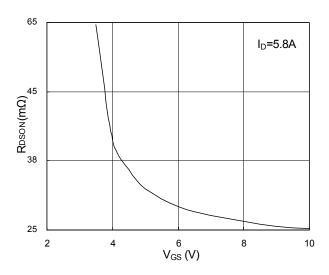


Fig.2 On-Resistance vs. Gate-Source

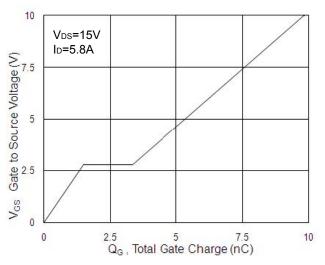


Fig.4 Gate-Charge Characteristics

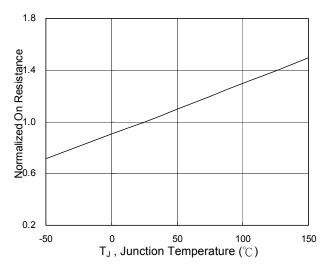


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>



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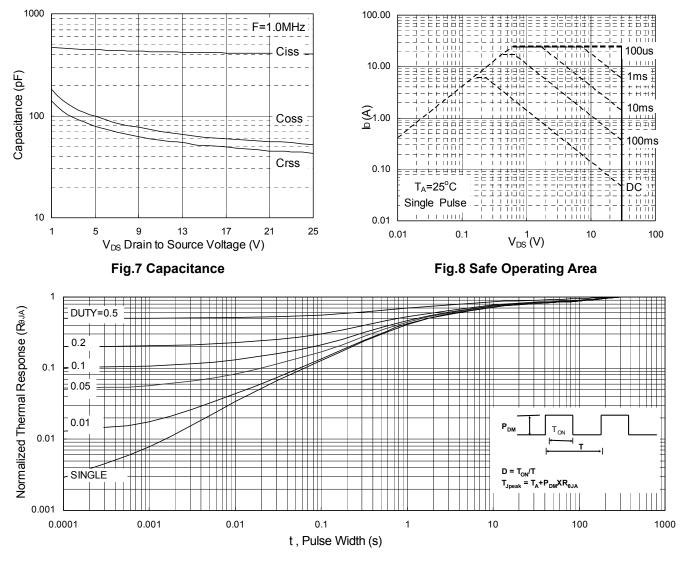
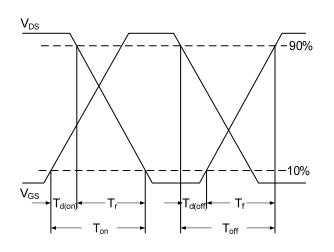
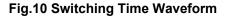


Fig.9 Normalized Maximum Transient Thermal Impedance





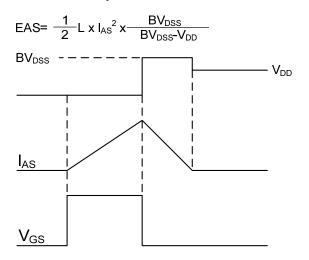


Fig.11 Unclamped Inductive Switching Waveform



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