

General Description

The WST6045 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the small power switching and load switch applications.

The WST6045 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

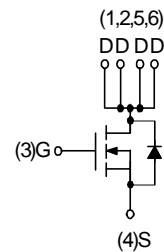
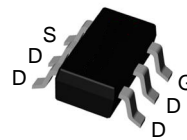
Product Summary

BVDSS	RDSON	ID
60V	40mΩ	5.0A

Applications

- Power management in portable and battery operated products
- One cell battery pack protection

SOT-23-6L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	5.0	A
$I_D@T_C=70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	4.0	A
I_{DM}	Pulsed Drain Current ²	20	A
$P_D@T_A=25^\circ C$	Total Power Dissipation ³	1.6	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	110	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	80	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	60	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.028	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =4.0A	---	40	50	mΩ
		V _{GS} =4.5V, I _D =3.0A	---	45	65	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	2.0	3.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-3.21	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =48V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =48V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =5A	---	12	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	4	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V, V _{GS} =10V, I _D =3.5A	---	11.5	---	nC
Q _{gs}	Gate-Source Charge		---	2.3	---	
Q _{gd}	Gate-Drain Charge		---	2.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GEN} =10V, R _G =3Ω I _D =1.0A, R _L =4.2Ω.	---	10	---	ns
T _r	Rise Time		---	6	---	
T _{d(off)}	Turn-Off Delay Time		---	21	---	
T _f	Fall Time		---	5	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	540	---	pF
C _{oss}	Output Capacitance		---	56	---	
C _{rss}	Reverse Transfer Capacitance		---	26	---	

Diode Characteristics

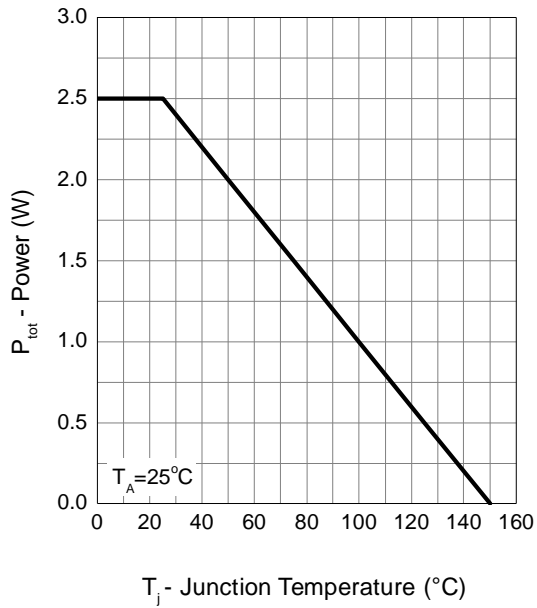
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	3.0	A
I _{SM}	Pulsed Source Current ^{2,4}		---	---	15	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _{SD} =3.5A, T _J =25°C	---	---	1.3	V
t _{rr}	Reverse Recovery Time	I _F =3.5A, dI/dt=100A/μs, T _J =25°C	---	20	---	nS
Q _{rr}	Reverse Recovery Charge		---	20	---	nC

Note :

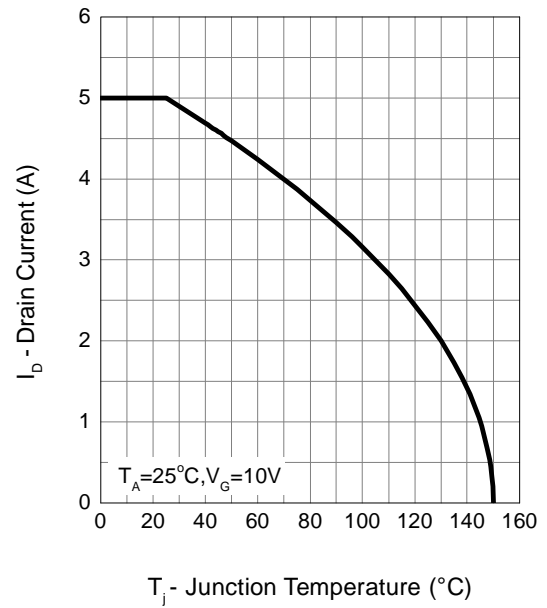
1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The power dissipation is limited by 150°C junction temperature
4. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

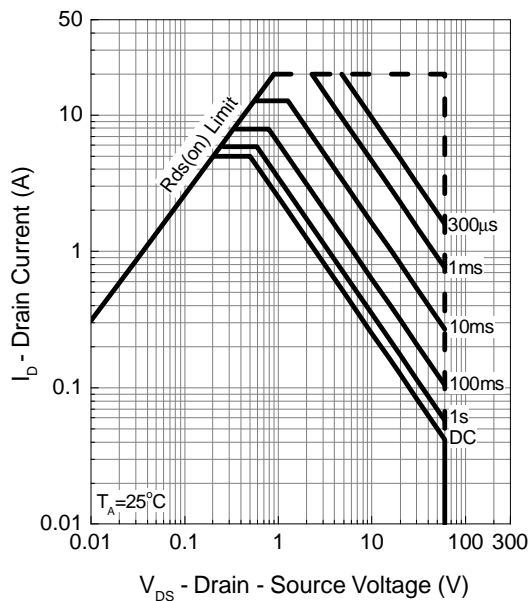
Power Dissipation



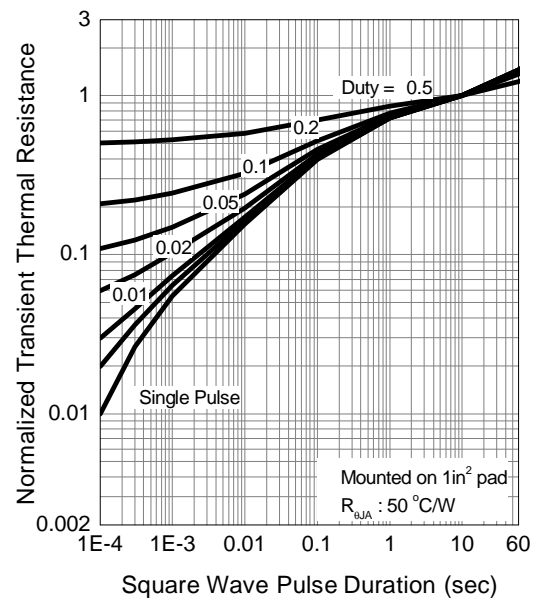
Drain Current



Safe Operation Area

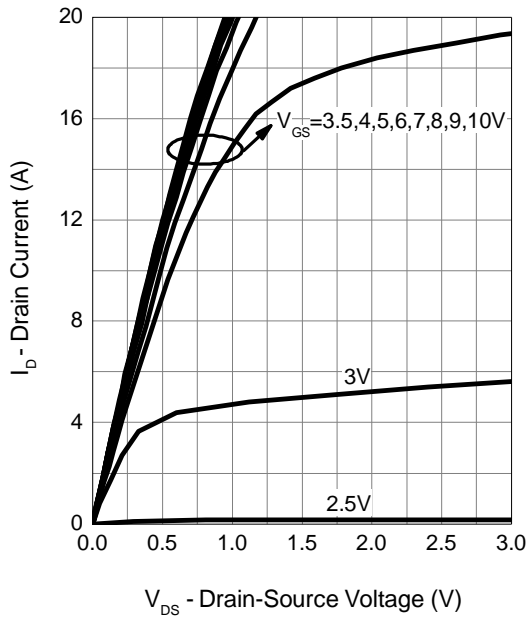


Thermal Transient Impedance

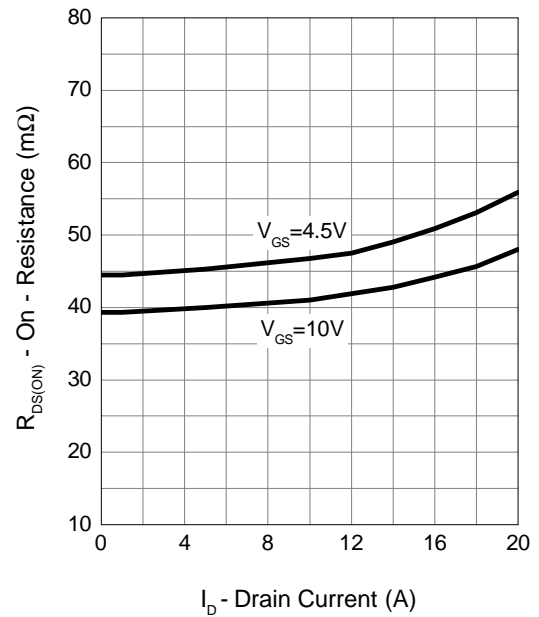


Typical Characteristics

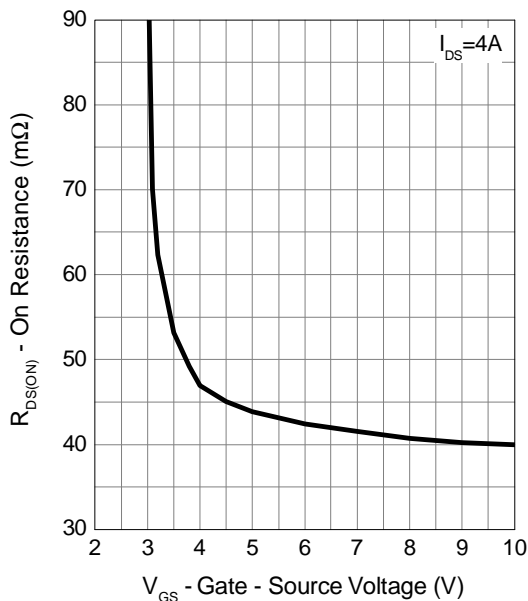
Output Characteristics



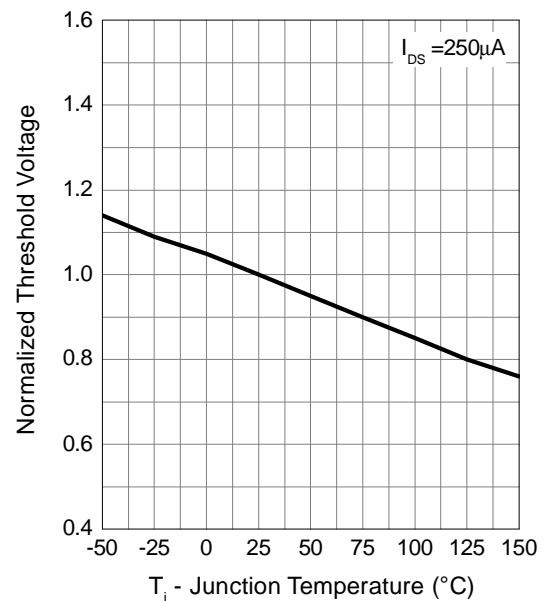
Drain-Source On Resistance



Gate-Source On Resistance

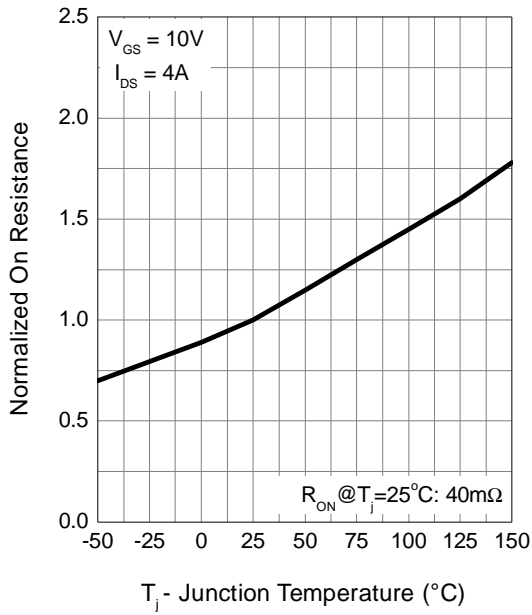


Gate Threshold Voltage

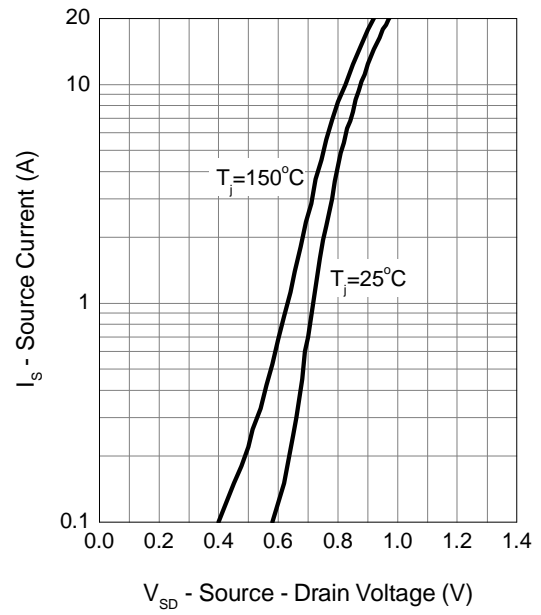


Typical Characteristics

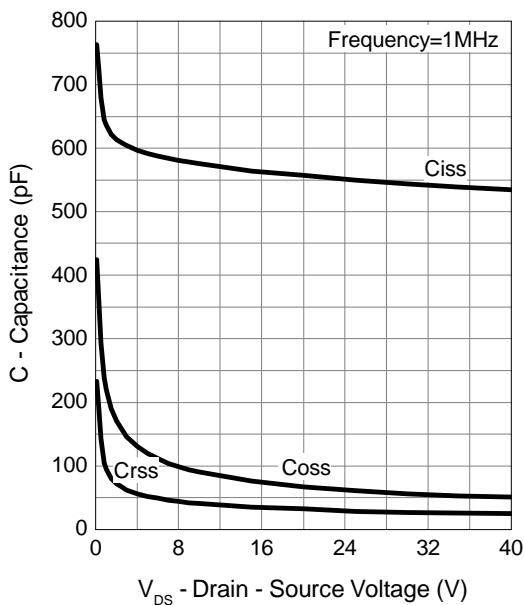
Drain-Source On Resistance



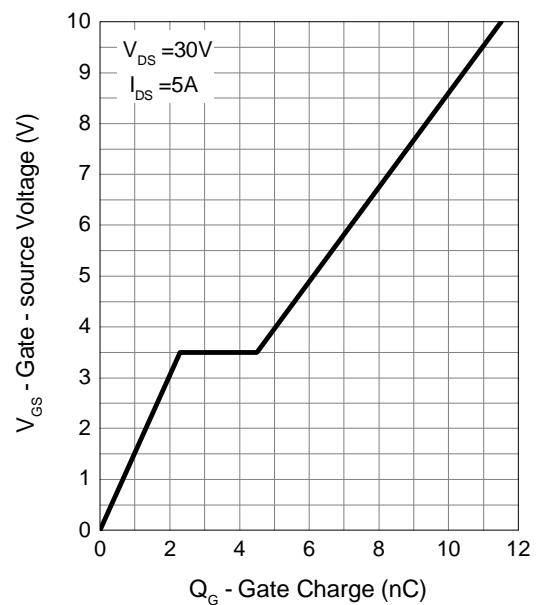
Source-Drain Diode Forward



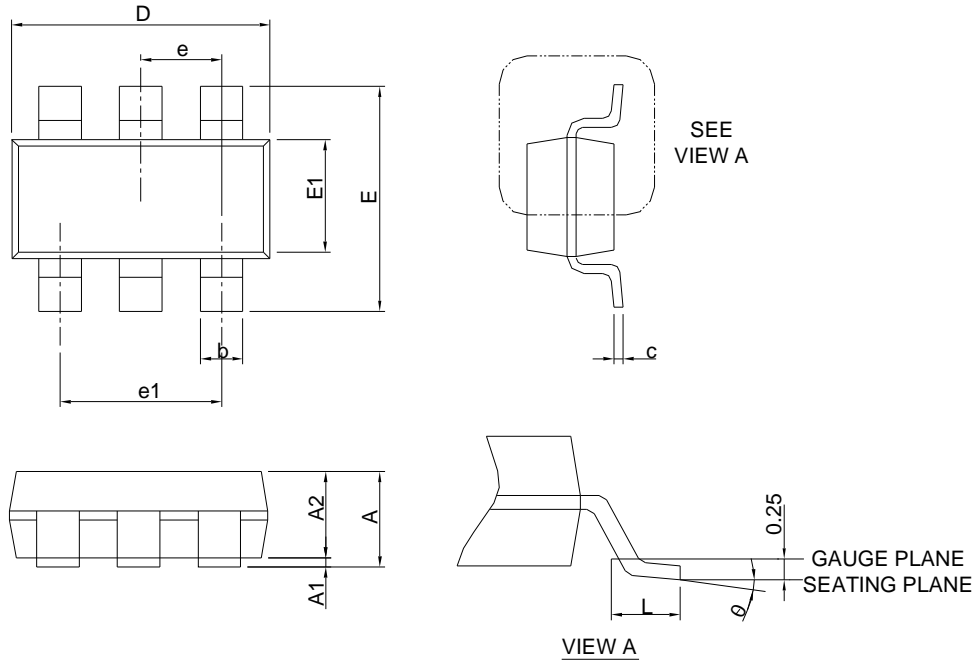
Capacitance



Gate Charge

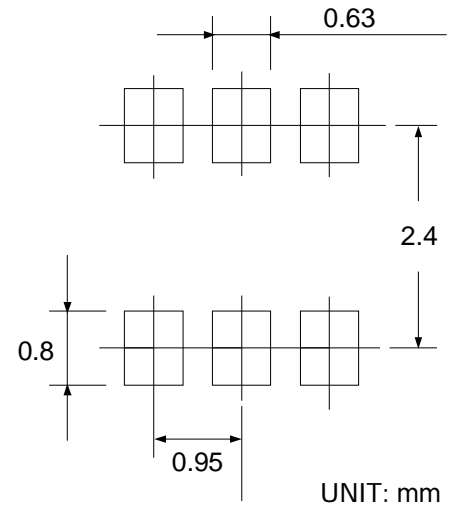


Package Information SOT-23-6L



SYMBOL	SOT-23-6L			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	-	1.25	-	0.049
A1	0.00	0.05	0.000	0.002
A2	0.90	1.20	0.035	0.047
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN



- Note : 1. Follow JEDEC TO-178 AB.
2. Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.



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