

General Description

The WST6401 is the highest performance trench P-ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WST6401 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

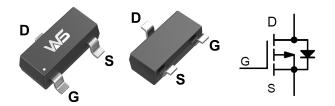
Product Summery

BVDSS	RDSON	ID
-20V	135mΩ	-2.5A

Applications

- High Frequency Point-of-Load Synchronous s Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOT-23N Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V _{DS}	Drain-Source Voltage	-20	V	
V _{GS}	Gate-Source Voltage	±12	V	
I _D @T₀=25℃	Continuous Drain Current, V _{GS} @ -4.5V ¹ -2.5			
I _D @T _c =70℃	Continuous Drain Current, V _{GS} @ -4.5V ¹	-1.9	А	
I _{DM}	Pulsed Drain Current ²	-10	A	
P _D @T _A =25℃	Total Power Dissipation ³	0.7	W	
T _{STG}	Storage Temperature Range -55 to 150		°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
R _{θJA}	Thermal Resistance Junction-ambient ¹		178	°C/W	
R _{eJC}	Thermal Resistance Junction-Case ¹		80	°C/W	



P-Ch MOSFET

Electrical Characteristics (T_J=25¹C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-20			V
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\!{\rm C}$, I_D=-1mA		-0.016		V/℃
	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V , I _D =-2A		135	165	mΩ
R _{DS(ON)}		V _{GS} =-2.5V , I _D =-1A		150	186	
		V _{GS} =-1.8V , I _D =-1.5A		250	355	
V _{GS(th)}	Gate Threshold Voltage		-0.5	-0.7	-1.2	2 V
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient			3.97		mV/℃
	Drain Source Leakage Current	V _{DS} =-16V , V _{GS} =0V , T _J =25℃			-1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-16V , V _{GS} =0V , T _J =55℃			-5	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 8V$, $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-2A				S
R _g	Gate Resistance	V_{DS} =0V , V_{GS} =0V , f=1MHz		13.1		Ω
Qg	Total Gate Charge (-4.5V)	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-2A		3.0		
Q _{gs}	Gate-Source Charge			0.5		nC
Q _{gd}	Gate-Drain Charge			0.8		
T _{d(on)}	Turn-On Delay Time	V _{DD} =-15V , V _{GS} =-4.5V , R _G =3.0Ω I _D =-2A		10		
Tr	Rise Time			5.0		ns
T _{d(off)}	Turn-Off Delay Time			21		
T _f	Fall Time			7		
C _{iss}	Input Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		290		
C _{oss}	Output Capacitance			60		pF
C _{rss}	Reverse Transfer Capacitance			34		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current ^{1,4}	$V_G = V_D = 0V$, Force Current			-2.5	А
I _{SM}	Pulsed Source Current ^{2,4}				-10	А
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25℃			-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t \leq 10 sec.

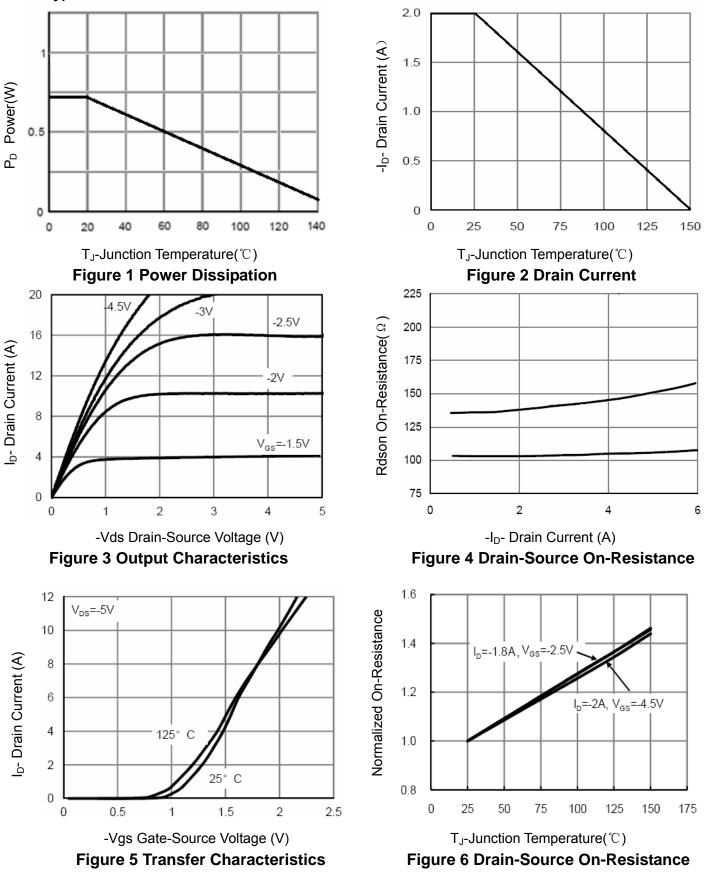
3, Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

 $4\,{\scriptstyle\smallsetminus}\,$ Guaranteed by design, not subject to production



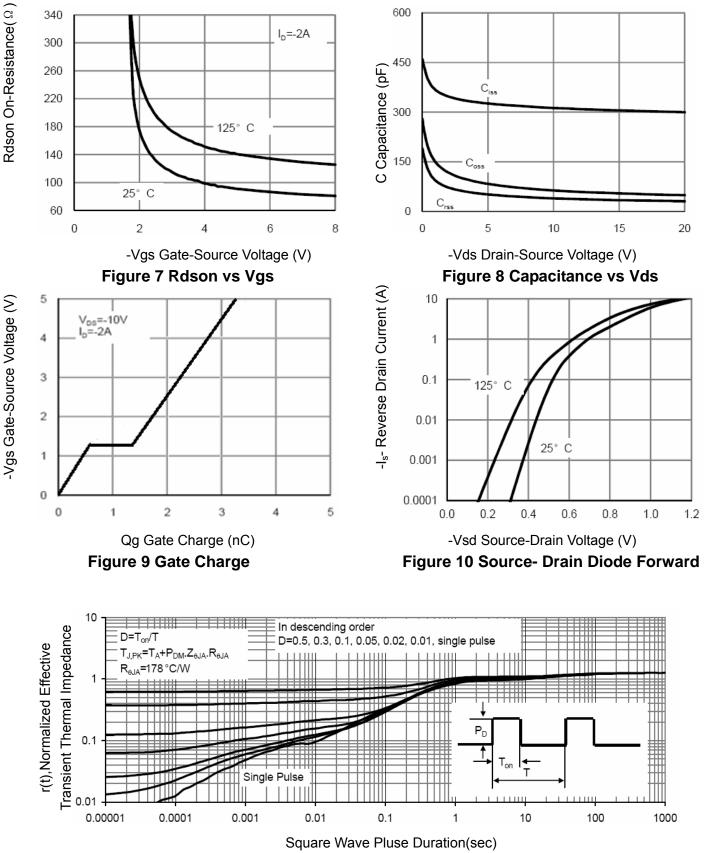
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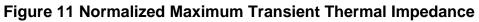






P-Ch MOSFET







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