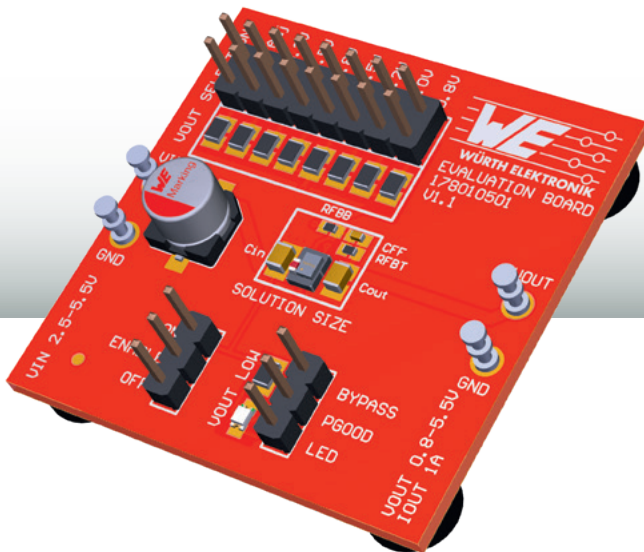




# Quick Start Guide

Mag<sup>3</sup>C Power Module Evaluation Board  
for 171010501 LGA6-EP

Evaluation Board  
178010501

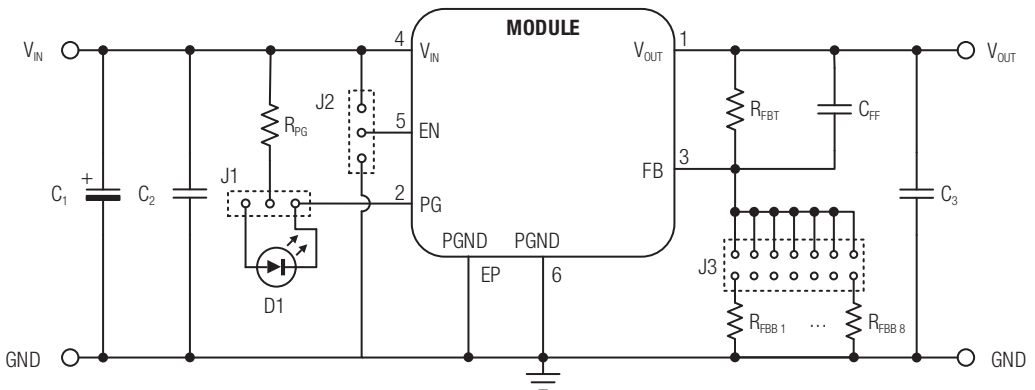
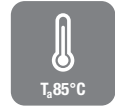


Version 1.1

 WARNING! – Before operating read the attached IMPORTANT NOTICE document!

# Schematic

## Features



The additional aluminum polymer capacitor  $C_1$  is only for evaluation board protection purposes. It is mounted at the termination of the supply line and provides slight damping of possible oscillations of the series resonance circuit represented by the inductance of the supply line and the input capacitance. It is not essential for operation.

For accurate  $V_{IN}$  and  $V_{OUT}$  voltage measurements it is recommended to measure directly at the input and output capacitors  $C_2$  and  $C_3$ .

It is **not** recommended to use this evaluation board with input and output wire lengths longer than 1 m.

For the datasheet of the power module visit us at:

<https://katalog.we-online.de/de/pm/MAGIC-VDDM>



This product is highly sensitive to electrostatic discharge (ESD). As such, always use proper ESD precautions when handling. Failing to follow the aforementioned recommendations can result in severe damage to the part.



**WARNING!** – Before operating read the attached IMPORTANT NOTICE document!



Ref.Des.	Description (Order Code)	
IC1	Magi <sup>2</sup> C MicroModule (171010501)	
C1	Aluminum polymer capacitor 220 $\mu$ F/10 V (875105244013)	
C2	Ceramic chip capacitor 4.7 $\mu$ F/25 V X5R, 0805 (885012107018)	
C3	Ceramic chip capacitor 10 $\mu$ F/16 V X5R, 0805 (885012107014)	
	Ceramic chip capacitor 10 $\mu$ F/10 V X5R, 0805* (885012107010)	
C <sub>FF</sub>	Ceramic chip capacitor 22 pF/25 V NP0/COG 0402 (885012005009)	
R <sub>FBT</sub>	24.9 k $\Omega$	
R <sub>FBB</sub>	Set by jumper	Open for V <sub>OUT</sub> = 0.8 V
		97.6 k $\Omega$ for V <sub>OUT</sub> = 1.0 V
		48.7 k $\Omega$ for V <sub>OUT</sub> = 1.2 V
		28 k $\Omega$ for V <sub>OUT</sub> = 1.5 V
		19.6 k $\Omega$ for V <sub>OUT</sub> = 1.8 V (default setting)
		11.5 k $\Omega$ for V <sub>OUT</sub> = 2.5 V
		7.87 k $\Omega$ for V <sub>OUT</sub> = 3.3 V
		To be soldered for adjustable output voltage
R <sub>PG</sub>	Pull-up resistor for PG pin 1.1 k $\Omega$	
D1	SMD Chip LED 0805, Red, 2 V <sub>F</sub> (150080RS75000)	
J1	Jumper to enable PG diode (150080RS75000) or connect pull-up direct to PG pin (61300311121)	
J2	Jumper for EN connection to either V <sub>IN</sub> (device enabled) or GND (device disabled) (61300311121)	
J3	Jumper for output voltage selection. Only one resistor should be selected at a time (61301621121)	

\* alternative recommended part



For layout, Gerber and STP files visit us on:  
[www.we-online.com/magic-vdmm](http://www.we-online.com/magic-vdmm)



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