

General Description

The MC14069 is a general purpose hex unbuffered inverter. Each inverter has a single stage.

It operates over a recommended VDD power supply range of 3V to 15V referenced to VSS (usually ground).

Unused inputs must be connected to VDD, VSS, or another input.

Features

- Wide supply voltage range from 3V to 15V
- Fully static operation
- 5V, 10V, and 15V parametric ratings
- Standardized symmetrical output characteristics
- Specified from -40°C to +85°C
- Packaging information: SOP14

Order Information

Product Model	Package Type	Marking	Packing	Packing Qty
MC14069UBDR2G	SOP-14	14069	Tape	2500Pcs/Reel

Block Diagram And Pin Description

Block Diagram

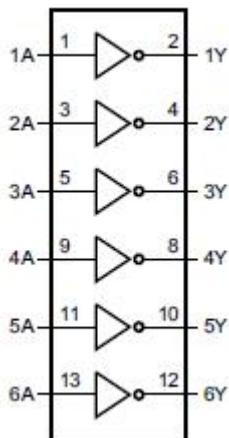


Figure 1. Logic diagram

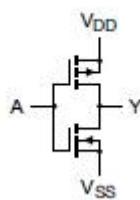
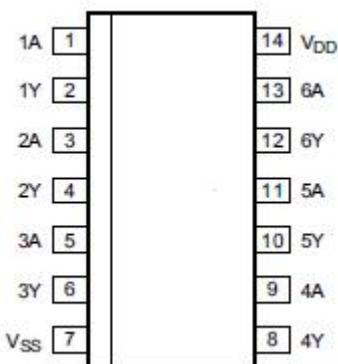


Figure 2. Schematic diagram (one inverter)

Pin Configurations**Pin Description**

Pin No.	Pin Name	Description
1	1A	data input
2	1Y	data output
3	2A	data input
4	2Y	data output
5	3A	data input
6	3Y	data output
7	V _{SS}	ground (0 V)
8	4Y	data output
9	4A	data input
10	5Y	data output
11	5A	data input
12	6Y	data output
13	6A	data input
14	V _{DD}	supply voltage

Function Table

Input	Output
nA	nY
L	H
H	L

Note: H=HIGH voltage level; L=LOW voltage level.



Electrical Parameter

3.1、Absolute Maximum Ratings

(Voltages are referenced to V_{SS} (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Max.	Unit
supply voltage	V _{DD}	-		-0.5	+18	V
DC input current	I _{IK}	any one input		-	±10	mA
input voltage	V _I	all inputs		-0.5	V _{DD} +0.5	V
storage temperature	T _{stg}	-		-65	+150	°C
total power dissipation	P _{tot}	-		-	500	mW
device dissipation	P	per output transistor		-	100	mW
Soldering temperature	T _L	10s	DIP	245	250	°C
			SOP			

Note:

- [1] For DIP14 packages: above 70°C the value of P_{tot} derates linearly with 12mW/K.
- [2] For SOP14 packages: above 70°C the value of P_{tot} derates linearly with 8mW/K.
- [3] For (T)SSOP14 packages: above 60°C the value of P_{tot} derates linearly with 5.5mW/K.

3.2、Recommended Operating Conditions

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
supply voltage	V _{DD}	-		3	-	15	V
ambient temperature	T _{amb}	in free air		-40	-	+85	°C



3.3、Electrical Characteristics

3.3.1、DC Characteristics 1

($T_{amb}=25^{\circ}C$, voltages are referenced to V_{SS} (ground=0 V), unless otherwise specified.)

Parameter	Symbol	Conditions (V)			$T_{amb}=25^{\circ}C$			Unit
		V_o	V_{IN}	V_{DD}	Min.	Typ.	Max.	
supply current	I_{DD}	-	0, 5	5	-	0.01	0.25	uA
		-	0, 10	10	-	0.01	0.5	uA
		-	0, 15	15	-	0.01	1	uA
LOW-level output current	I_{OL}	0.4	0, 5	5	0.51	1	-	mA
		0.5	0, 10	10	1.3	2.6	-	mA
		1.5	0, 15	15	3.4	6.8	-	mA
HIGH-level output current	I_{OH}	4.6	0, 5	5	-0.51	-1	-	mA
		2.5	0, 5	5	-1.6	-3.2	-	mA
		9.5	0, 10	10	-1.3	-2.6	-	mA
		13.5	0, 15	15	-3.4	-6.8	-	mA
LOW-level output voltage	V_{OL}	-	0, 5	5	-	0	0.05	V
		-	0, 10	10	-	0	0.05	V
		-	0, 15	15	-	0	0.05	V
HIGH-level output voltage	V_{OH}	-	0, 5	5	4.95	5	-	V
		-	0, 10	10	9.95	10	-	V
		-	0, 15	15	14.95	15	-	V
LOW-level input voltage	V_{IL}	0.5, 4.5	-	5	-	-	1	V
		1, 9	-	10	-	-	2	V
		1.5, 13.5	-	15	-	-	2.5	V
HIGH-level input voltage	V_{IH}	0.5	-	5	4	-	-	V
		1	-	10	8	-	-	V
		1.5	-	15	12.5	-	-	V
input leakage current	I_I	-	0, 15	15	-	$\pm 10^{-5}$	± 0.1	uA



3.3.2、DC Characteristics 2

(T_{amb}=-40°C to +85°C, voltages are referenced to V_{SS} (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions (V)			T _{amb} =-40°C		T _{amb} =+85°C		Unit
		V _O	V _{IN}	V _{DD}	Min.	Max.	Min.	Max.	
supply current	I _{DD}	-	0, 5	5	-	0.25	-	7.5	uA
		-	0, 10	10	-	0.5	-	15	uA
		-	0, 15	15	-	1	-	30	uA
LOW-level output current	I _{OL}	0.4	0, 5	5	0.61	-	0.42	-	mA
		0.5	0, 10	10	1.5	-	1.1	-	mA
		1.5	0, 15	15	4	-	2.8	-	mA
HIGH-level output current	I _{OH}	4.6	0, 5	5	-0.61	-	-0.42	-	mA
		2.5	0, 5	5	-1.8	-	-1.3	-	mA
		9.5	0, 10	10	-1.5	-	-1.1	-	mA
		13.5	0, 15	15	-4	-	-2.8	-	mA
LOW-level output voltage	V _{OL}	-	0, 5	5	-	0.05	-	0.05	V
		-	0, 10	10	-	0.05	-	0.05	V
		-	0, 15	15	-	0.05	-	0.05	V
HIGH-level output voltage	V _{OH}	-	0, 5	5	4.95	-	4.95	-	V
		-	0, 10	10	9.95	-	9.95	-	V
		-	0, 15	15	14.95	-	14.95	-	V
LOW-level input voltage	V _{IL}	0.5, 4.5	-	5	-	1	-	1	V
		1, 9	-	10	-	2	-	2	V
		1.5, 13.5	-	15	-	2.5	-	2.5	V
HIGH-level input voltage	V _{IH}	0.5	-	5	4	-	4	-	V
		1	-	10	8	-	8	-	V
		1.5	-	15	12.5	-	12.5	-	V
input leakage current	I _I	-	0, 15	15	-	±0.1	-	±1	uA

3.3.3、AC Characteristics 1

(T_{amb}=25°C, V_{SS}=0V, t_r, t_f=20ns, C_L=50pF, R_L=200kΩ, unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
propagation delay time	t _{PHL} , t _{PLH}	see Figure 4	V _{DD} =5V	-	55	110	ns
			V _{DD} = 10V	-	30	60	ns
			V _{DD} = 15V	-	25	50	ns
transition time	t _{THL} , t _{T LH}	see Figure 4	V _{DD} =5V	-	100	200	ns
			V _{DD} = 10V	-	50	100	ns
			V _{DD} = 15V	-	40	80	ns
input capacitance	C _I	any input		-	10	15	pF

Testing Circuit

AC Testing Circuit

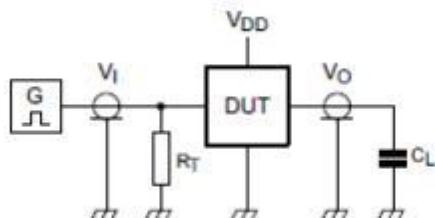


Figure 3. Test circuit for switching times

Definitions for test circuit:

DUT=Device Under Test.

C_L=Load capacitance including jig and probe capacitance.

R_T=Termination resistance should be equal to the output impedance Z_o of the pulse generator.

4.2、AC Testing Waveforms

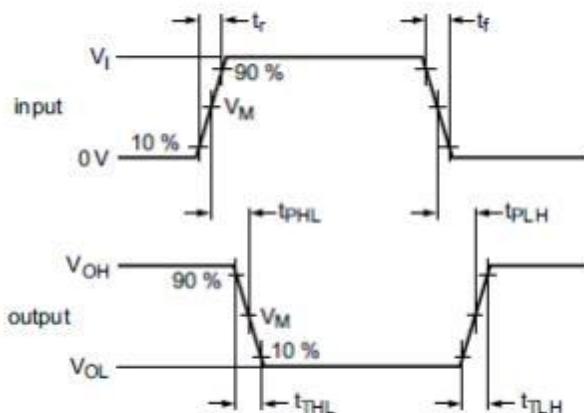


Figure 4. Propagation delay, output transition time

4.3、Measurement Points

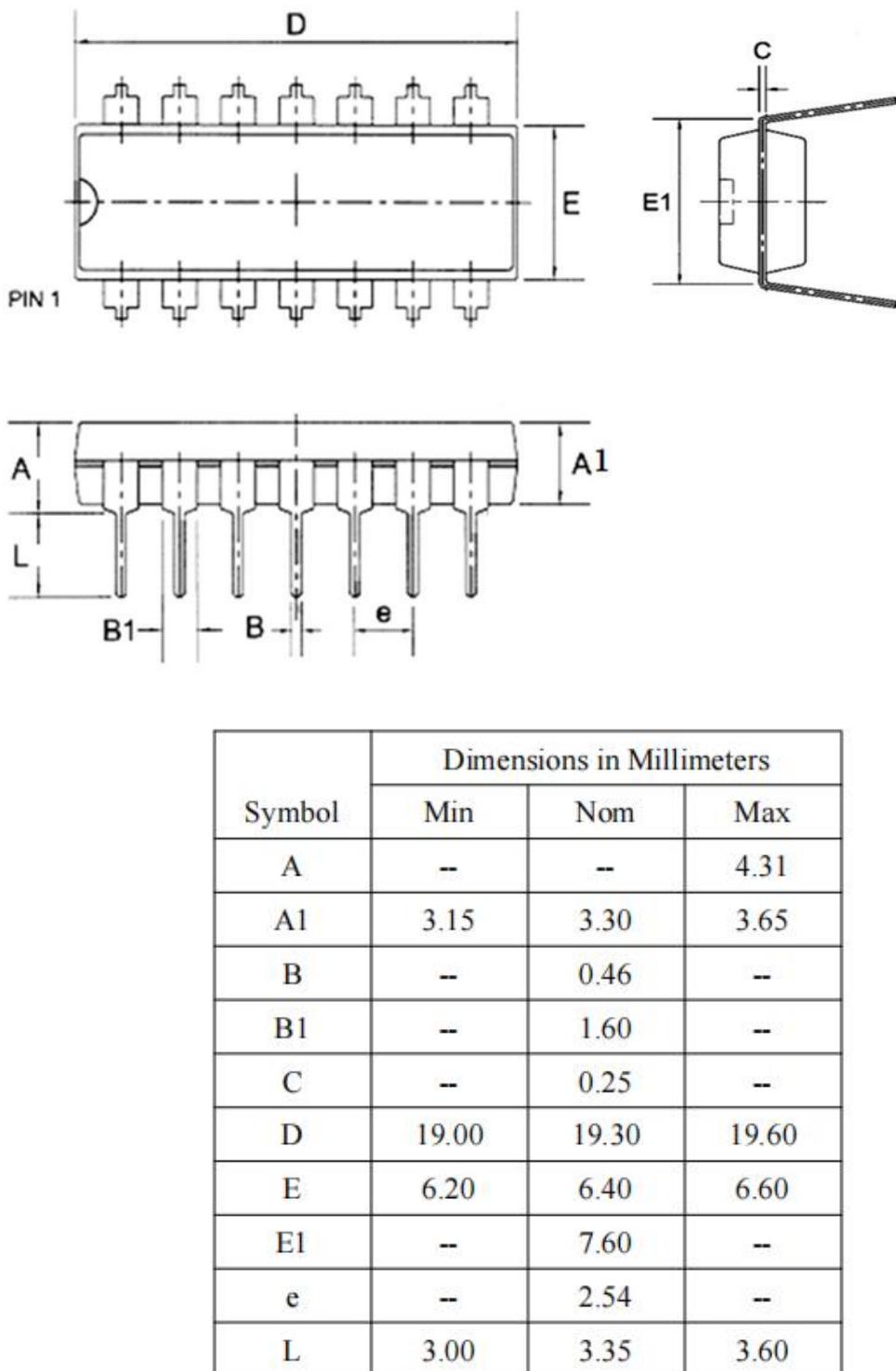
Supply voltage	Input	Output
V _{DD}	V _M	V _M
5V to 15V	0.5×V _{DD}	0.5×V _{DD}

4.4、Test Data

Supply voltage	Input	Load
V _{DD}	V _I	C _L
5V to 15V	V _{SS} or V _{DD}	50pF

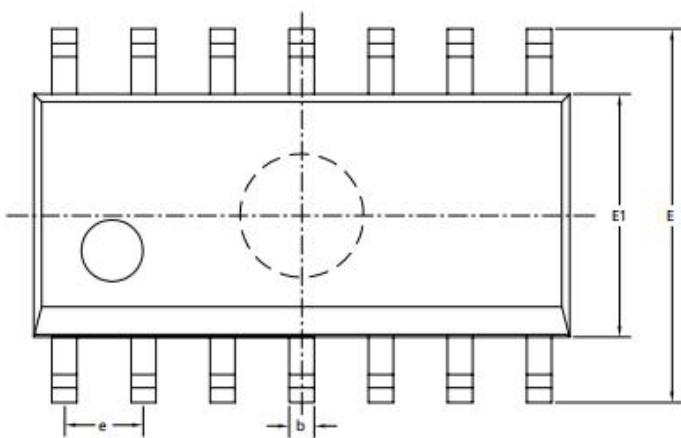
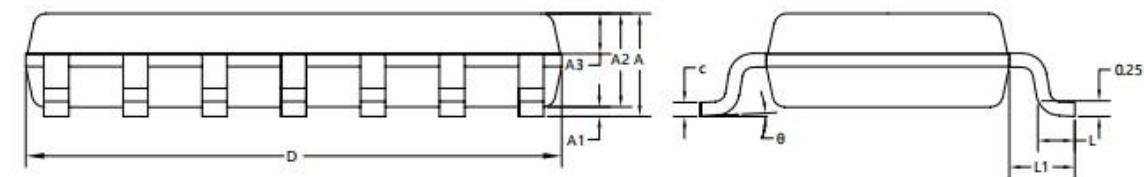
Package Information

DIP14





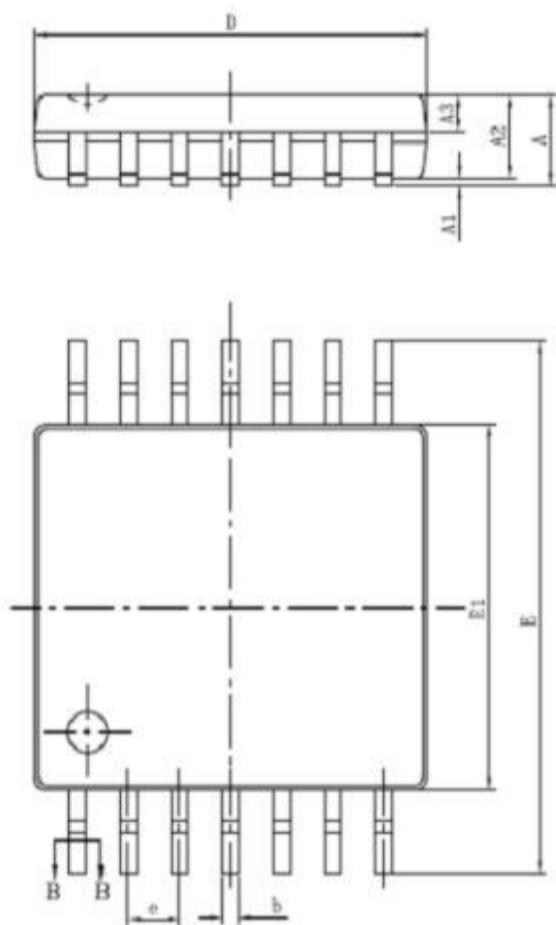
SOP14



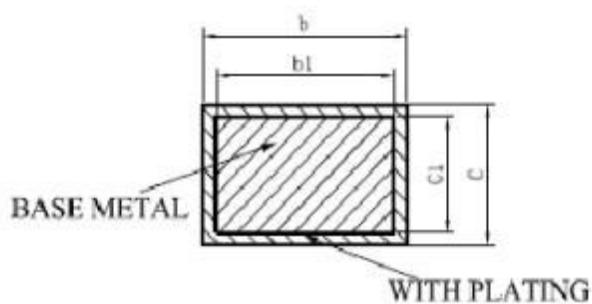
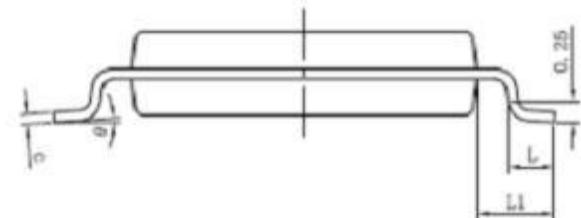
SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	1.50	1.60	1.70
A1	0.10	0.15	0.25
A2	1.40	1.45	1.50
A3	0.60	0.65	0.70
b	0.35	0.40	0.45
c	0.15	0.20	0.25
D	8.50	8.60	8.70
E	5.80	6.00	6.20
E1	3.85	3.90	3.95
e	1.27BSC		
L	0.50	0.60	0.70
L1	1.05REF		
θ	0*	4*	8*



TSSOP14



SYMBOL	MILLIMETER	
	MIN	MAX
A	—	1.20
A1	0.05	0.15
A2	0.90	1.05
A3	0.39	0.49
b	0.20	0.30
b1	0.19	0.25
c	0.13	0.19
c1	0.12	0.14
D	4.86	5.06
E1	4.30	4.50
E	6.20	6.60
e	0.65BSC	
L	0.45	0.75
L1	1.00BSC	
θ	0	8°



SECTION B-B



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