

FH30120GS

N-Channel Trench Power MOSFET

Description

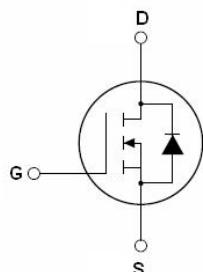
The FH30120GS uses advanced Shielded Gate trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Application

- Motor drivers
- Power switching application
- DC/DC Converters In Computing
- Isolated DC/DC Converters In Telecom and Industrial

Features

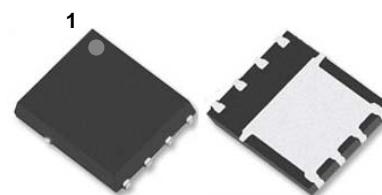
- $V_{DS} = 30V$; $I_D = 120A$
- $R_{DS(ON)}(\text{Typ.}) = 1.0\text{ m}\Omega$ @ $V_{GS} = 10\text{ V}$
- $R_{DS(ON)}(\text{Typ.}) = 1.6\text{ m}\Omega$ @ $V_{GS} = 4.5\text{ V}$
- High density cell design for ultra low R_{dson}
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation



Schematic diagram



Marking and pin Assignment



PDFN5x6-8L top and bottom view

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Parameter | | Max. | Units |
|-------------------|---|---------------------------|-------------|---------------------------|
| V_{DSS} | Drain-Source Voltage | | 30 | V |
| V_{GSS} | Gate-Source Voltage | | ± 20 | V |
| I_D^* | Continuous Drain Current | $T_c = 25^\circ\text{C}$ | 120 | A |
| | | $T_c = 100^\circ\text{C}$ | 68 | A |
| I_{DM}^{***} | Pulsed Drain Current | | 314 | A |
| E_{AS}^{****} | Single Pulsed Avalanche Energy | | 128 | mJ |
| P_D^* | Power Dissipation | $T_c = 25^\circ\text{C}$ | 34 | W |
| $R_{\theta JC}^*$ | Thermal Resistance, Junction to Case | | 3.2 | $^\circ\text{C}/\text{W}$ |
| T_J, T_{STG} | Operating and Storage Temperature Range | | -55 to +150 | $^\circ\text{C}$ |

Notes :

* Surface Mounted on 1 in² pad area, $t \leq 10\text{ sec}$

** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

*** limited by bonding wire

**** $VD=20V, VG=10V, RG=25\Omega$, $L=0.5mH$

Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise specified)

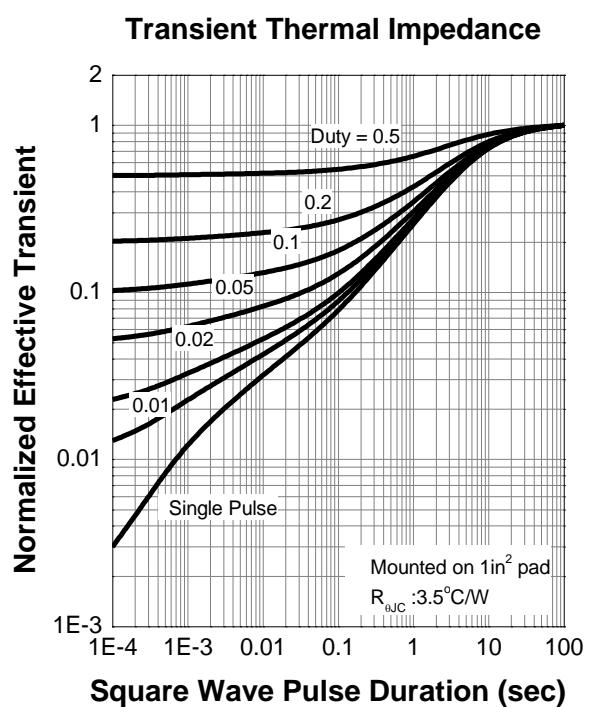
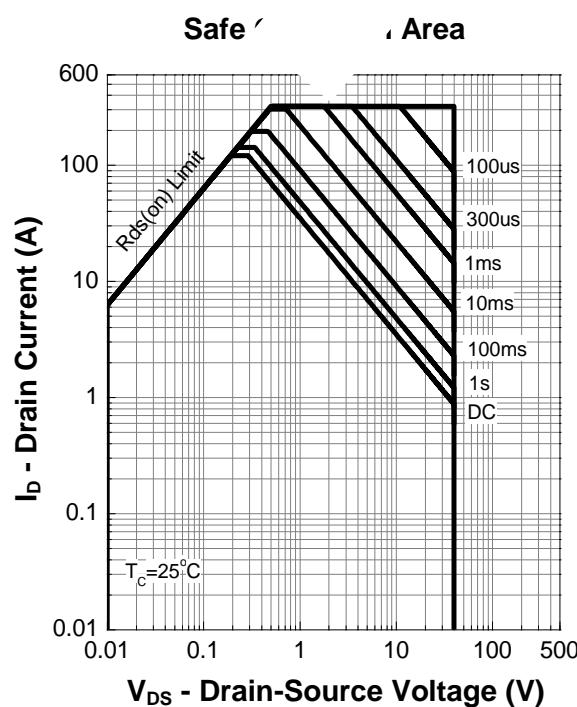
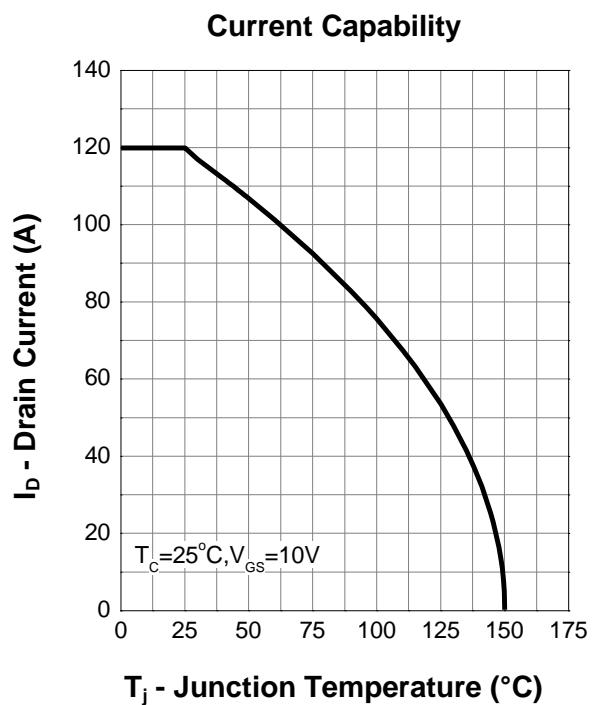
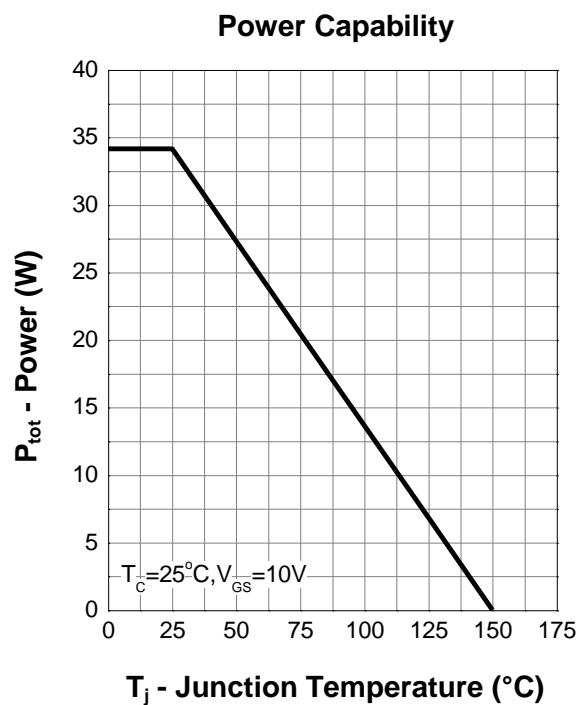
| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|---|--|--|------|------|-----------|------------------|
| Off Characteristic | | | | | | |
| $V_{(\text{BR})\text{DSS}}$ | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$ | 30 | - | - | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$ | - | - | 1.0 | μA |
| I_{GSS} | Gate to Body Leakage Current | $V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$ | - | - | ± 100 | nA |
| On Characteristics | | | | | | |
| $V_{\text{GS}(\text{th})}$ | Gate Threshold Voltage | $V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$ | 1.0 | 1.8 | 2.5 | V |
| $R_{\text{DS}(\text{on})}$ ^a | Static Drain-Source on-Resistance | $V_{\text{GS}}=10\text{V}, I_D=20\text{A}$ | - | 1.0 | 1.4 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=4.5\text{V}, I_D=10\text{A}$ | - | 1.6 | 2.0 | |
| g_{FS} | Forward Transconductance | $V_{\text{DS}}=10\text{V}, I_D=10\text{A}$ | - | 15.5 | - | S |
| Dynamic Characteristics ^b | | | | | | |
| C_{iss} | Input Capacitance | $V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$ | - | 3930 | - | pF |
| C_{oss} | Output Capacitance | | - | 1020 | - | pF |
| C_{rss} | Reverse Transfer Capacitance | | - | 167 | - | pF |
| Q_g | Total Gate Charge | $V_{\text{DS}}=15\text{V}, I_D=24\text{A}, V_{\text{GS}}=10\text{V}$ | - | 82 | - | nC |
| Q_{gs} | Gate-Source Charge | | - | 14 | - | nC |
| Q_{gd} | Gate-Drain("Miller") Charge | | - | 15 | - | nC |
| Switching Characteristics ^b | | | | | | |
| $t_{\text{d}(\text{on})}$ | Turn-on Delay Time | $V_{\text{DD}}=15\text{V}, I_D=15\text{A}, R_{\text{GEN}}=3.3\Omega, V_{\text{GS}}=10\text{V}$ | - | 15.6 | - | ns |
| t_r | Turn-on Rise Time | | - | 23.5 | - | ns |
| $t_{\text{d}(\text{off})}$ | Turn-off Delay Time | | - | 62.8 | - | ns |
| t_f | Turn-off Fall Time | | - | 15.2 | - | ns |
| Drain-Source Diode Characteristics and Maximum Ratings | | | | | | |
| I_s | Maximum Continuous Drain to Source Diode Forward Current | - | - | 120 | A | |
| I_{SM} | Maximum Pulsed Drain to Source Diode Forward Current | - | - | 314 | A | |
| V_{SD} ^a | Drain to Source Diode Forward Voltage | $V_{\text{GS}}=0\text{V}, I_s=20\text{A}$ | - | - | 1.2 | V |
| | | | - | 57 | - | ns |
| t_{rr} | Body Diode Reverse Recovery Time | $I_F=30\text{A}, dI/dt=100\text{A}/\mu\text{s}$ | - | 71 | - | nC |
| Q_{rr} | Body Diode Reverse Recovery Charge | | - | - | - | |

Notes :

a : Pulse test ; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$

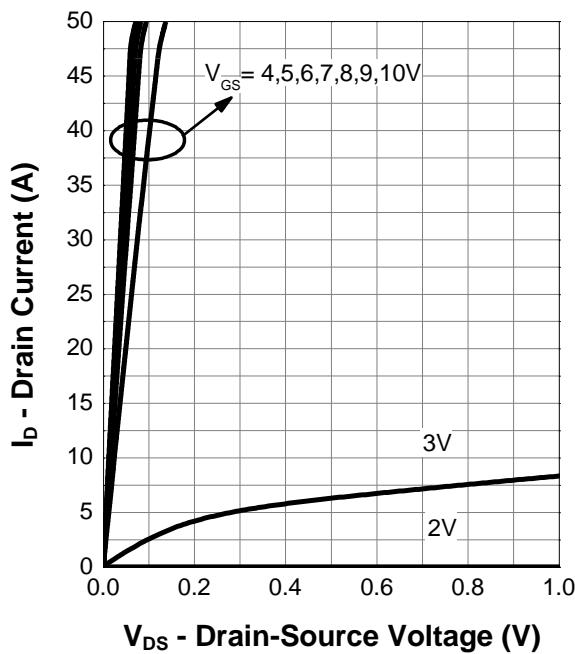
b : Guaranteed by design, not subject to production testing

Typical Characteristics (Cont.)

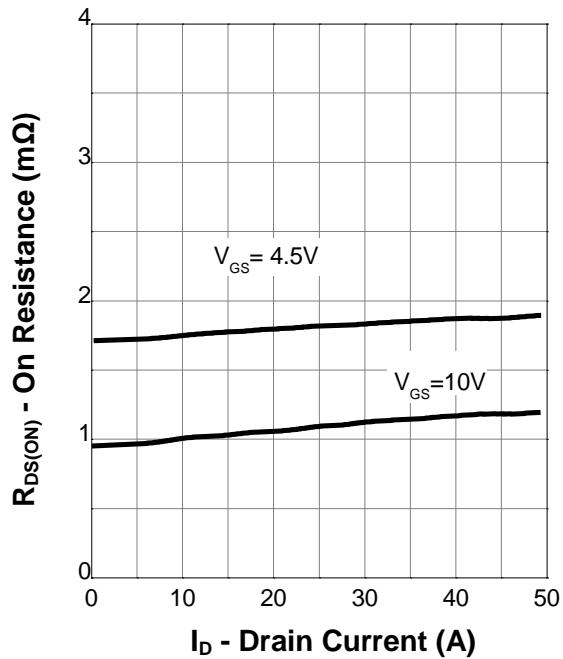


Typical Characteristics (Cont.)

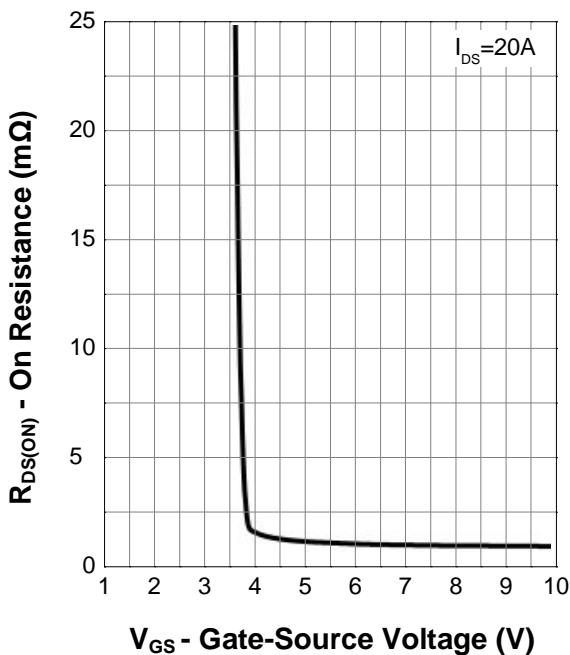
Output Characteristics



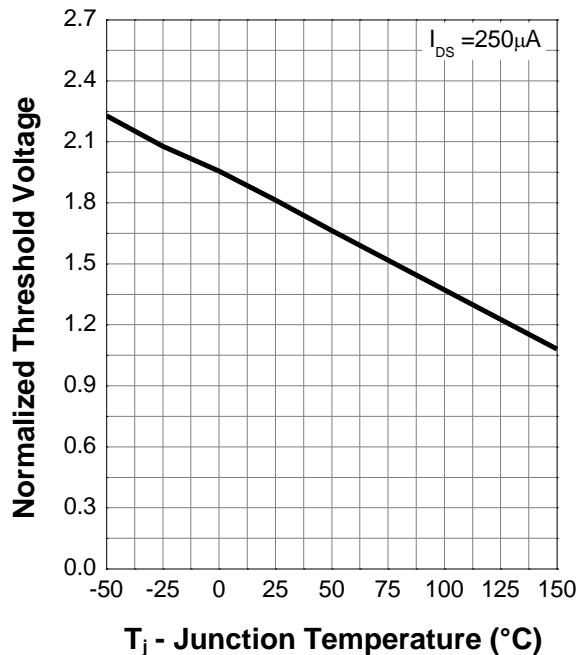
On Resistance



Transfer Characteristics

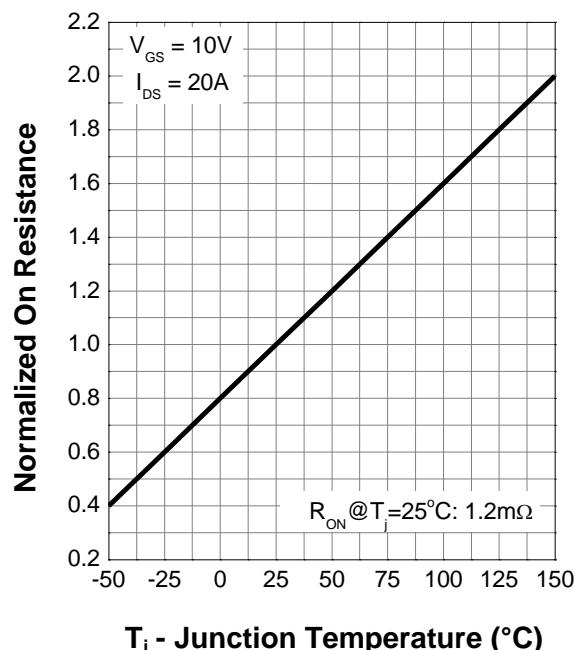


Normalized Threshold Voltage

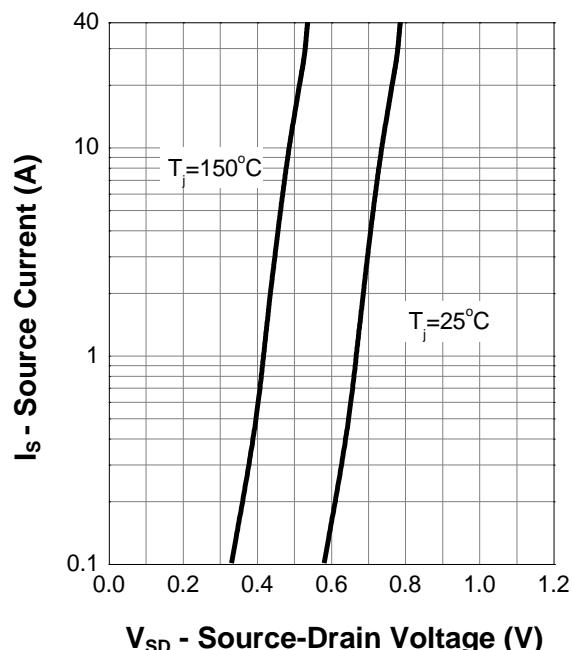


Typical Characteristics (Cont.)

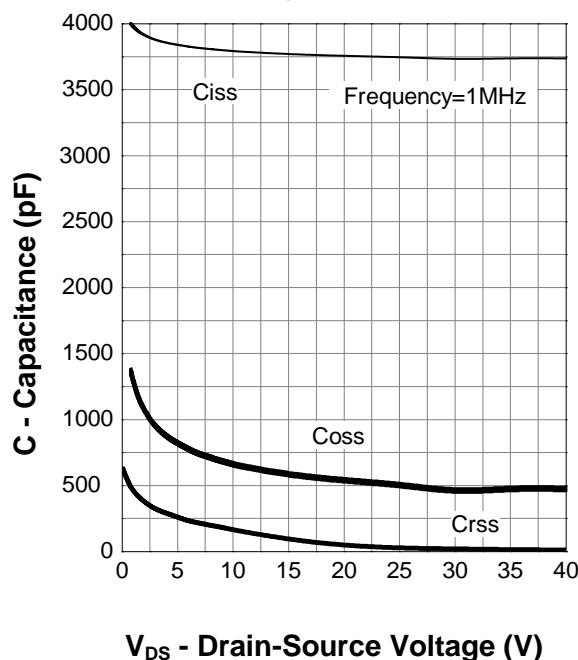
Normalized On Resistance



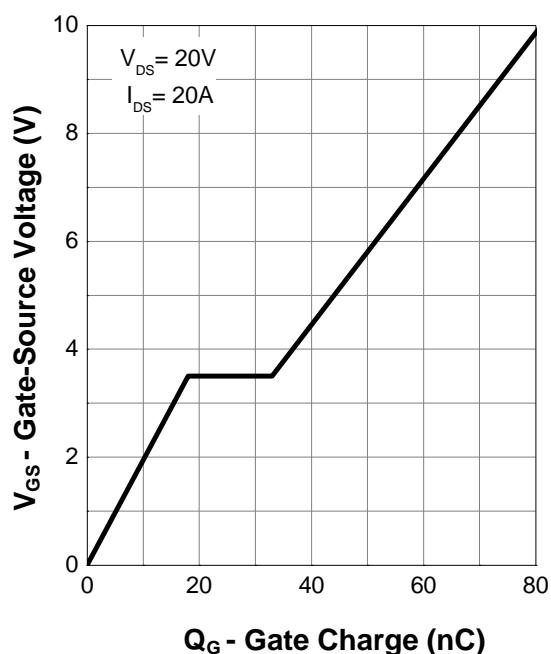
Diode Forward Current



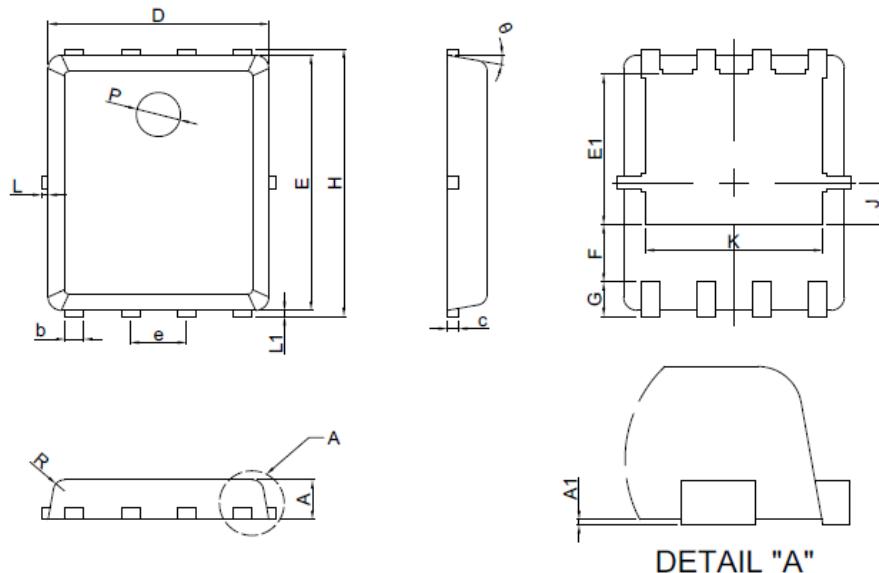
Capacitance



Gate Charge



Package Dimensions : PDFN5x6-8L



| Symbol | Dimensions In Millimeters | |
|--------|---------------------------|------|
| | MIN. | MAX. |
| A | 0.80 | 1.00 |
| A1 | 0.00 | 0.05 |
| b | 0.35 | 0.49 |
| c | 0.254REF | |
| D | 4.90 | 5.10 |
| F | 1.40REF | |
| E | 5.70 | 5.90 |
| e | 1.27BSC | |
| H | 5.95 | 6.20 |
| L1 | 0.10 | 0.18 |
| G | 0.60REF | |
| K | 4.00REF | |
| L | - | 0.15 |
| J | 0.95BSC | |
| P | 1.00REF | |
| E1 | 3.40REF | |
| θ | 6° | 14° |
| R | 0.25REF | |

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