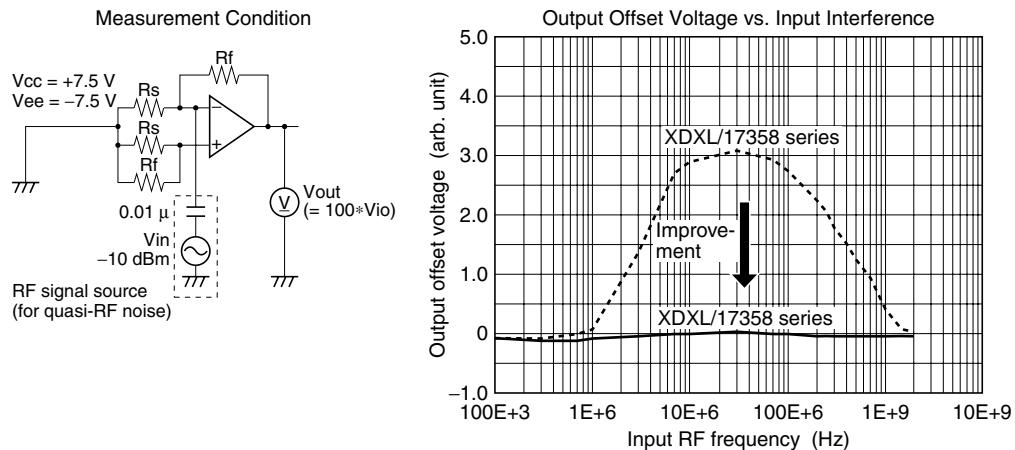


Description

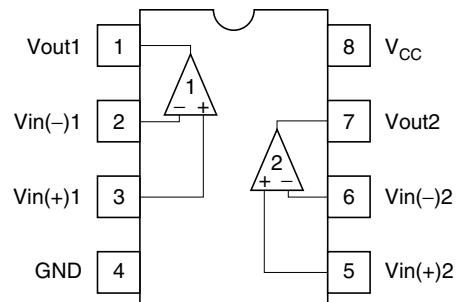
XDXL/17358 series are dual operational amplifier that provide high gain and internal phase compensation, with single power supply. They can be widely applied to control equipments and to general use.

Features

- Wide range of supply voltage, and single power supply used
- Wide range of common mode voltage, and possible to operate with an input about 0 V, and output around 0 V is available
- Frequency characteristics and input bias current are temperature compensated
- Low electro-magnetic susceptibility level

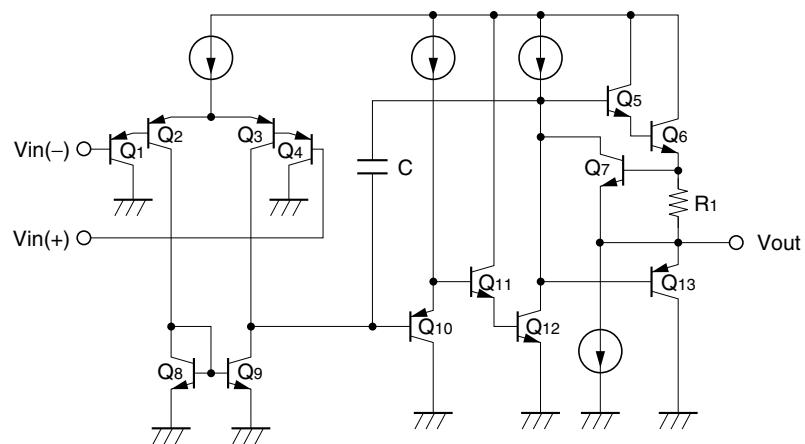


Pin Arrangement



(Top View)

Circuit Schematic (1/2)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	
		XDXL/17358	Unit
Supply voltage	V _{cc}	32	V
Sink current	I _{sink}	50	mA
Power dissipation	P _T	570 * ¹	mW
Common mode input voltage	V _{CM}	-0.3 to V _{cc}	V
Differential input voltage	V _{in} (diff)	±V _{cc}	V
Operating temperature	T _{opr}	-40 to +85	°C
Storage temperature	T _{stg}	-55 to +125	°C

- Notes:
1. This is the allowable values up to Ta = 50°C. Derate by 8.3 mW/°C.
 2. These are the allowable values up to Ta = 25°C mounting in air.

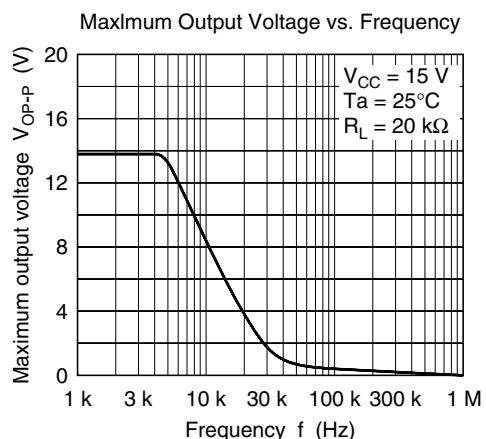
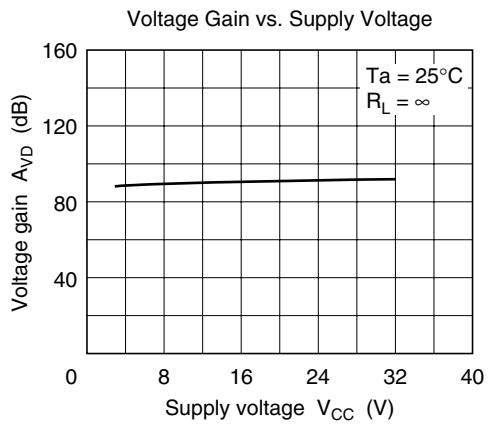
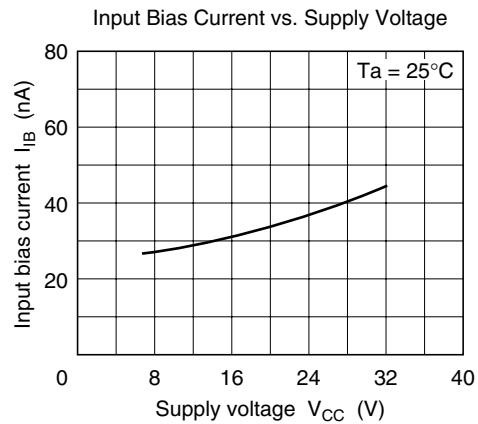
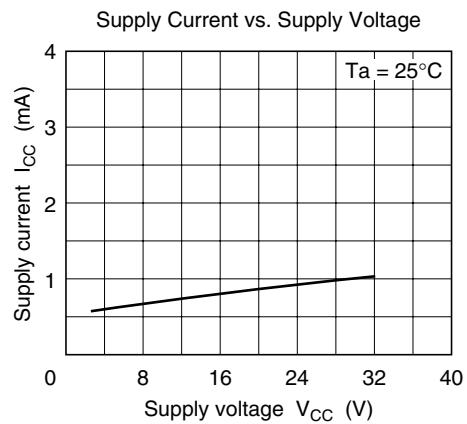
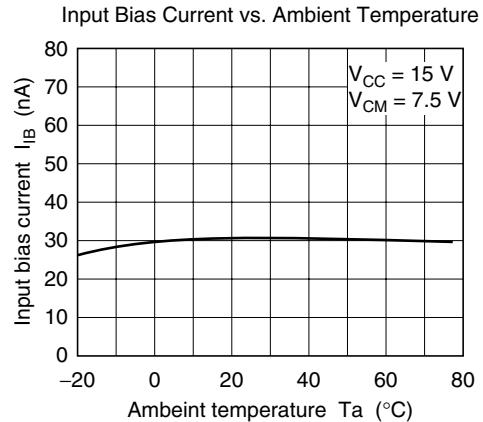
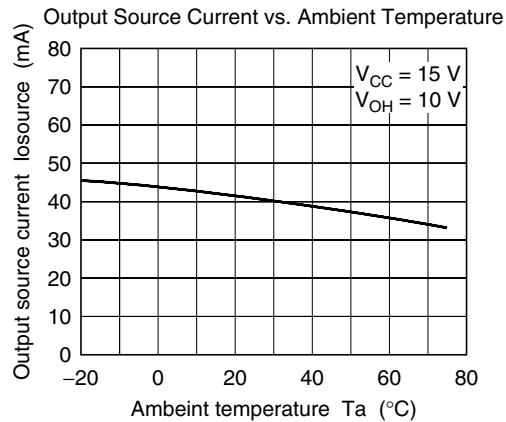
When it is mounted on glass epoxy board of 40 mm × 40 mm × 1.5 mm with 30% wiring density, the allowable value is 570 mW up to Ta = 45°C. If Ta > 45°C, derate by 7.14 mW/°C.

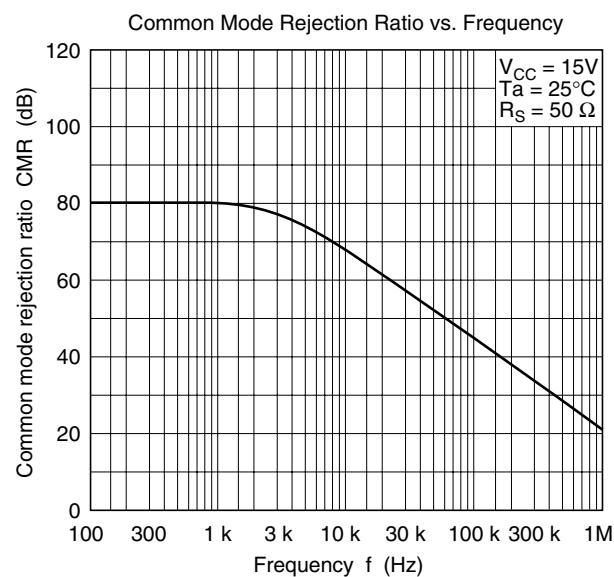
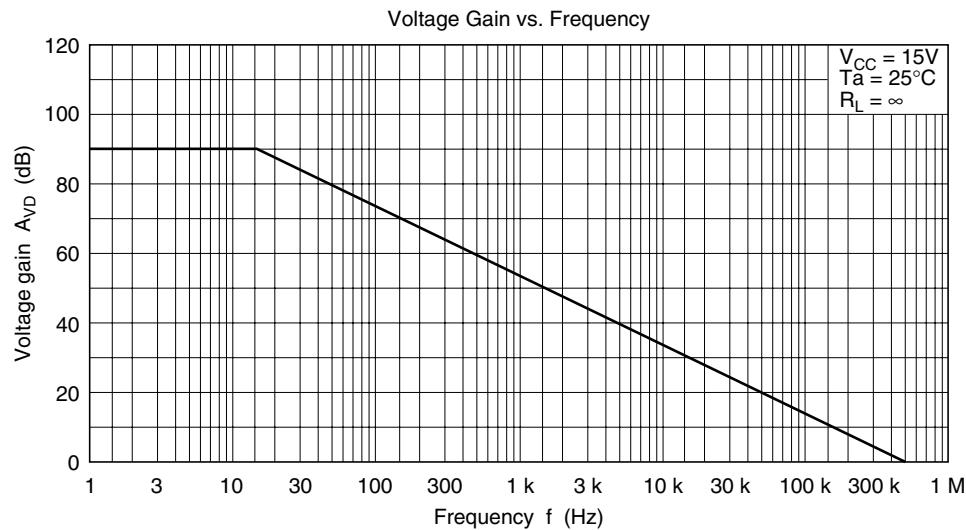
Electrical Characteristics

($V_{CC} = +15$ V, $T_a = 25^\circ\text{C}$)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Input offset voltage	V_{IO}	—	3	7	mV	$V_{CM} = 7.5$ V, $R_s = 50\Omega$, $R_f = 50k\Omega$
Input offset current	I_{IO}	—	5	50	nA	$V_{CM} = 7.5$ V, $ I_{IO} = I_{I(+)} - I_{I(-)} $
Input bias current	I_{IB}	—	30	250	nA	$V_{CM} = 7.5$ V
Power source rejection ratio	PSRR	—	93	—	dB	$R_s = 1k\Omega$, $R_f = 100k\Omega$
Voltage gain	A_{VD}	75	90	—	dB	$R_L = \infty$, $R_s = 1k\Omega$, $R_f = 100k\Omega$
Common mode rejection ratio	CMR	—	80	—	dB	$R_s = 50\Omega$, $R_f = 5k\Omega$
Common mode input voltage range	$V_{CM(+)}$ $V_{CM(-)}$	13.5 —	— —	— -0.3	V	$R_s = 1k\Omega$, $R_f = 100k\Omega$ $R_s = 1k\bullet$, $R_f = 100k\Omega$
Peak-to-peak output voltage	V_{OP-P}	—	13.6	—	V	$f = 100$ Hz, $R_L = 20k\Omega$, $R_s = 1k\Omega$, $R_f = 100k\Omega$
Output source current	$I_{OSOURCE}$	20	40	—	mA	$V_{IN^+} = 1$ V, $V_{IN^-} = 0$ V, $V_{OH} = 10$ V
Output sink current	I_{OSINK}	10	20	—	mA	$V_{IN^-} = 1$ V, $V_{IN^+} = 0$ V, $V_{OL} = 2.5$ V
Output sink current	I_{OSINK}	15	50	—	μ A	$V_{IN^-} = 1$ V, $V_{IN^+} = 0$ V, $V_{OUT} = 200$ mV
Supply current	I_{CC}	—	0.8	2	mA	$V_{IN} = GND$, $R_L = \infty$
Slew rate	SR	—	0.2	—	V/ μ s	$R_L = \infty$, $V_{CM} = 7.5$ V, $f = 1.5$ kHz
Channel separation	CS	—	120	—	dB	$f = 1$ kHz

Characteristic Curves





Solder Mounting Method

1. Small and light surface-mount packages require special attentions on solder mounting.
On solder mounting, pre-heating before soldering is needed.
The following figure shows an example of infrared rays reflow.
2. The difference of thermal expansion coefficient between mounted substrates and IC leads may cause a failure like solder peeling or solder wet, and electrical characteristics may change by thermal stress.
Therefore, mounting should be done after sufficient confirmation for especially in case of ceramic substrates.

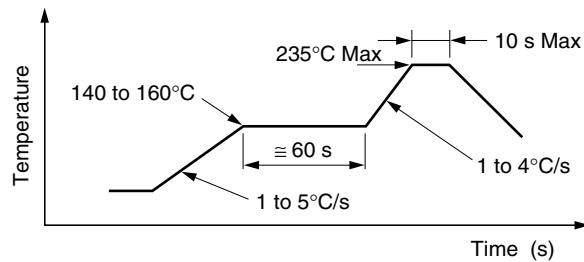
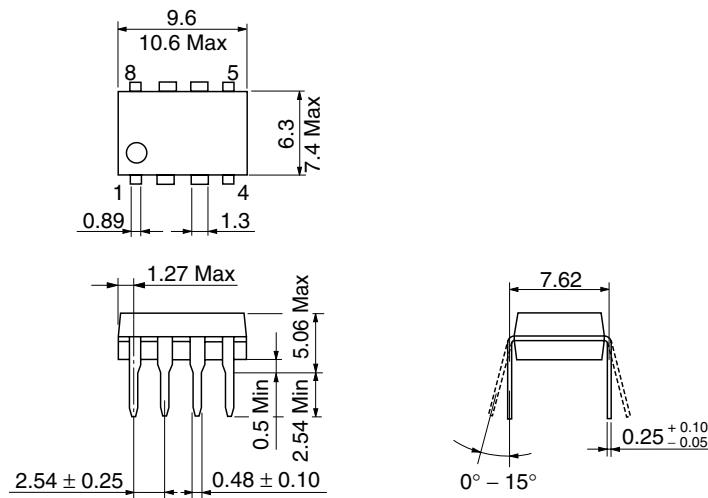


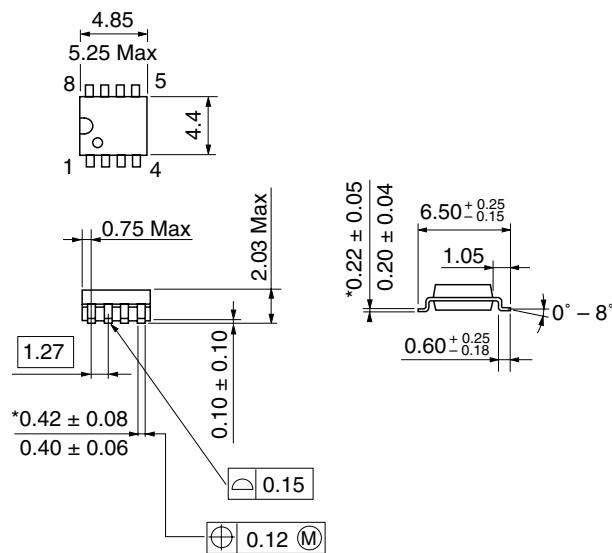
Figure 1 An Example of Infrared Rays Reflow Conditions

Package Dimensions

Unit: mm



Unit: mm



以上信息仅供参考. 如需帮助联系客服人员。谢谢 XINLUDA

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