Description

The XL1682 is a high performance AC/DC universal input Primary Side Regulation Power Factor Controller for LED driver applications. The device uses Pulse Frequency Modulation (PFM) technology to regulate output current while achieving high power factor and low THD.

The XL1682 provides accurate constant current (CC) regulation while removing the opto-coupler and secondary control circuitry. It also eliminates the need of loop compensation circuitry while maintaining stability. The XL1682 achieves excellent regulation and high efficiency, yet meets the requirement of IEC61000-3-2 harmonic standard.

The XL1682 features low start -up current, low operation current and high efficiency. It also has rich protection features including over voltage, short circuit, over current protection etc.

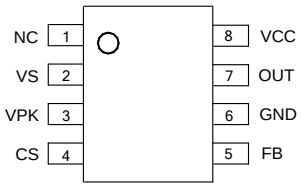
The XL1682 is available in SO-8 package.

Features

- Primary Side Control for Output Current Regulation Without Opto-coupler and Secondary CV/CC Control Circuitry
- Low Start-up Current
- High Power Factor and Low THD for Universal Input Range
- Tight CC Regulation Performance for Universal Input Mains Voltage Range
- Eliminates Control Loop Compensation Circuitry
- Built-in Acceleration Start
- Open-load and Reload Detection
- Over Voltage and Short Circuit Protection
- Over Current Protection
- Cost Effective Total PFC LED Driver Solution
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3**)

Pin Assignments

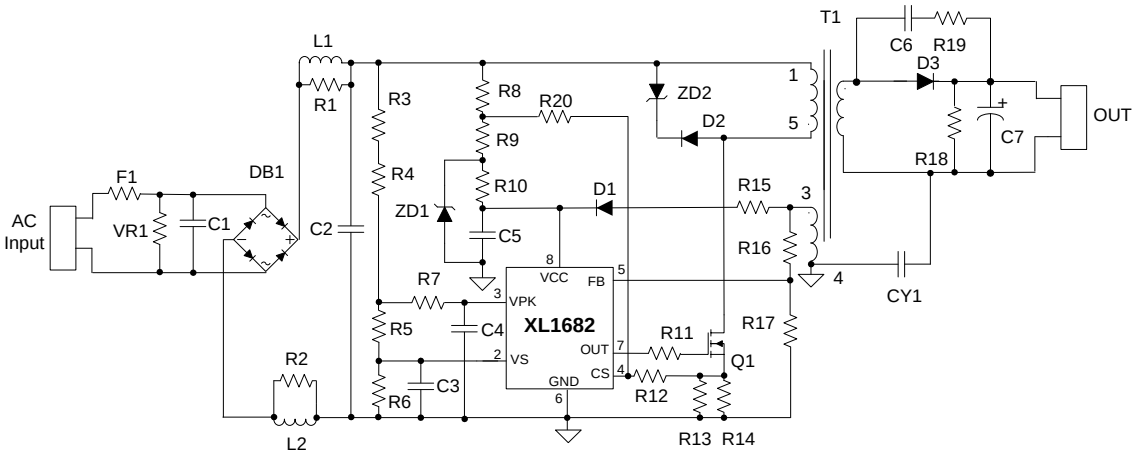
(Top View)



SO-8

Applications

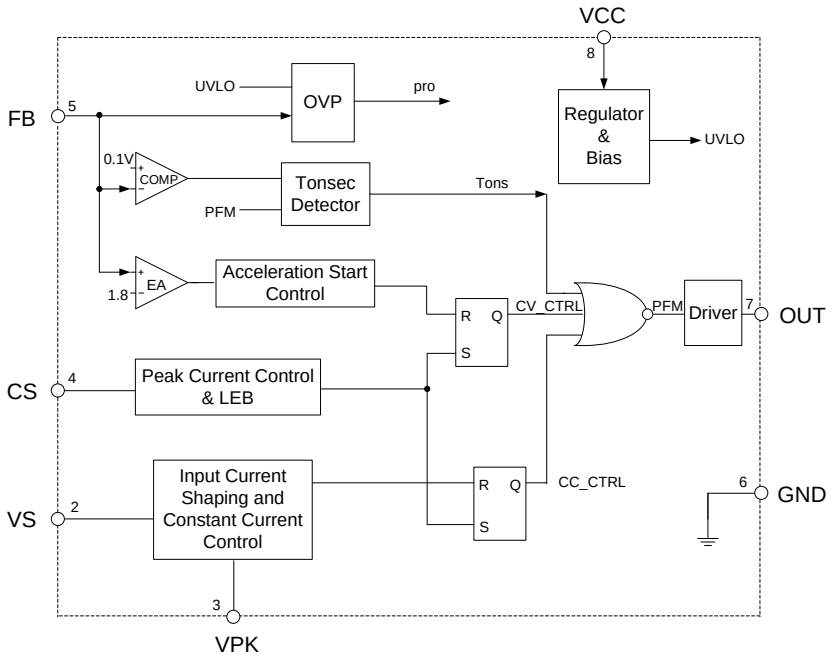
- Single Stage Power Factor Correction Power Supply for LED Lighting



Pin Descriptions

Pin Number	Pin Name	Function
1	NC	No connection
2	VS	The rectified input voltage sensing pin. The pin is detecting the instantaneous rectified sine waveform of input voltage
3	VPK	The rectified input voltage peak value sensing pin. The pin is detecting the rectified sine waveform peak value of input voltage
4	CS	Primary current sensing
5	FB	This pin captures the feedback voltage from the auxiliary winding. FB voltage is used to control no load output voltage and determine acceleration stop point at start-up phase
6	GND	Ground. Current return for gate driver and control circuits of the IC
7	OUT	Gate driver output
8	VCC	Supply voltage of gate driver and control circuits of the IC

Functional Block Diagram



Symbol	Parameter	Rating	Unit
V_{CC}	Power Supply Voltage	-0.3 to +35	V
I_{OUT}	Driver Output Current	300	mA
V_{VS}, V_{PK}, V_{CS}	Voltage at VS, VPK, CS	-0.3 to 7	V
V_{FB}	FB Input Voltage	-40 to 10	V
T_J	Operating Junction Temperature	+150	°C
T_{STG}	Storage Temperature	-65 to +150	°C
T_{LEAD}	Lead Temperature (Soldering, 10 sec)	+300	°C
P_D	Power Dissipation (T_A = +50°C)	0.65	W
θ_{JA}	Thermal Resistance (Junction to Ambient)	190	°C/W
–	ESD (Machine Model)	200	V
–	ESD (Human Body Model)	3000	V

Note 4: Stresses greater than those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “Recommended Operating Conditions” is not implied. Exposure to “Absolute Maximum Ratings” for extended periods may affect device reliability.

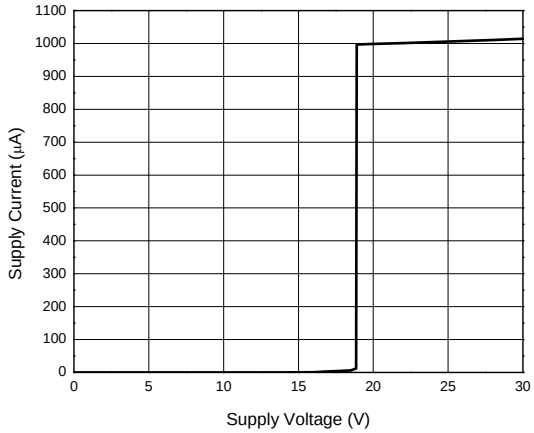
Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{CC}	Power Supply Voltage	9	21	V
T_A	Ambient Temperature	-40	+105	°C

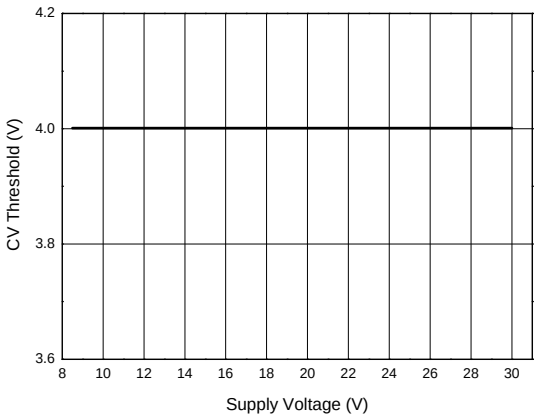
Electrical Characteristics (@ V_{CC} = 15V, T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
UVLO Section						
V_{TH} (ST)	Start-up Threshold	–	18	19	20	V
V_{OPR} (Min)	Minimal Operating Voltage	After turn on	7.5	8	8.5	V
V_{CC_OVP}	VCC OVP Voltage	–	28	32	34	V
Standby Current Section						
I_{ST}	Start-up Current	V_{CC} = V_{TH} (ST)-0.5V, Before start up	–	–	20	μA
I_{CC} (Max)	Maximum Operating Current	V_{VS} = V_{PK} = 3V	–	1000	1300	μA
Drive Output Section						
V_{OH}	Output High Level Voltage	$I_{GD-SOURCE}$ = 20mA V_{CC} = 12V	10	–	–	V
V_{OL}	Output Low Level Voltage	$I_{GD-SINK}$ = 20mA V_{CC} = 12V	–	–	1	V
t_R	Output Voltage Rise Time	C_L = 1nF	100	140	190	ns
t_F	Output Voltage Fall Time	C_L = 1nF	30	60	90	ns
$V_{O-CLAMP}$	Output Clamp Voltage	$I_{GD-SOURCE}$ = 5mA V_{CC} = 20V	12	13.5	15	V
V_{UVLO}	UVLO Saturation Voltage	V_{CC} = 0 to $V_{CC\ –ON}$ I_{SINK} = 10mA	–	–	1.1	V
VS Input Section						
V_{VS}/V_{PK} (Max)	Maximum Ratio	V_{VS} = V_{PK} = 3V	0.8	1	1.2	V
V_{VS}/V_{PK} (Min)	Minimum Ratio	V_{VS} = 0V, V_{PK} = 3V	–	–	0.2	V
Current Sense Section						
t_{ON} (Min)	Minimum On Time	–	500	750	1000	ns
V_{SOCP}	Short Circuit Protection Voltage	–	3	4	–	V
Feedback Input Section						
I_{FB}	FB Pin Input Leakage Current	V_{FB} = 4V	–	2	8	μA
V_{FB} (ACC)	Acceleration Start Threshold	–	1.4	1.8	2.2	V
V_{FB} (CV)	CV Threshold	–	3.8	4.1	4.4	V
V_{FB} (OVP)	Over Voltage Protection	–	5.6	6.25	6.9	V
Over Temperature Protection Section						
–	Shutdown Temperature	–	–	+140	–	°C
–	Temperature Hysteresis	–	–	+20	–	°C

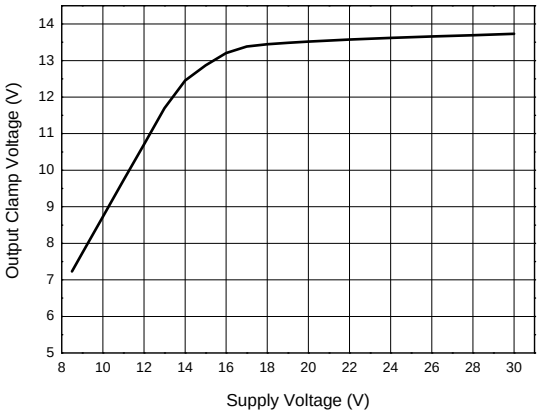
Supply Current vs. Supply Voltage



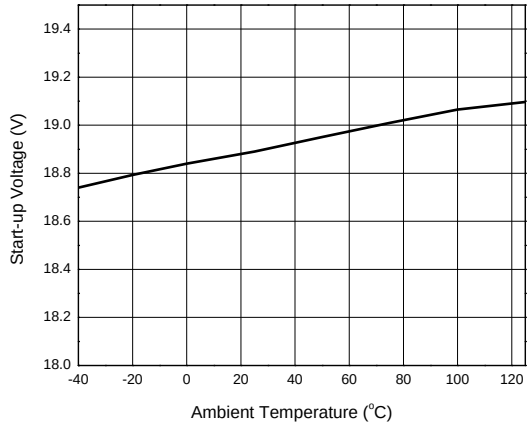
CV Threshold vs. Supply Voltage



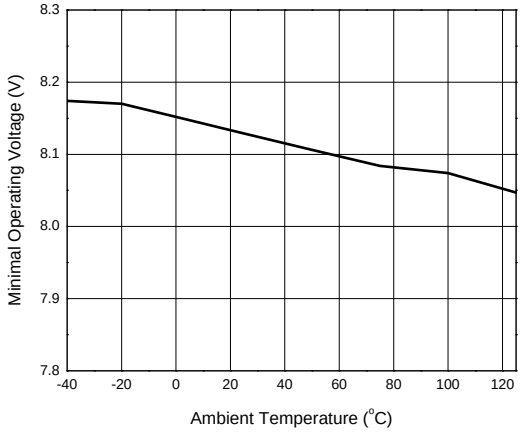
Output Clamp Voltage vs. Supply Voltage



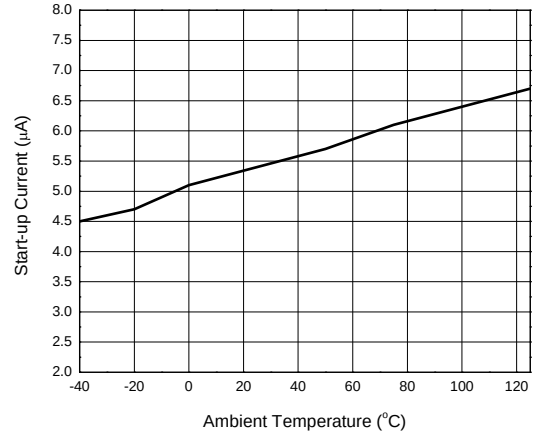
Start-up Voltage vs. Ambient Temperature



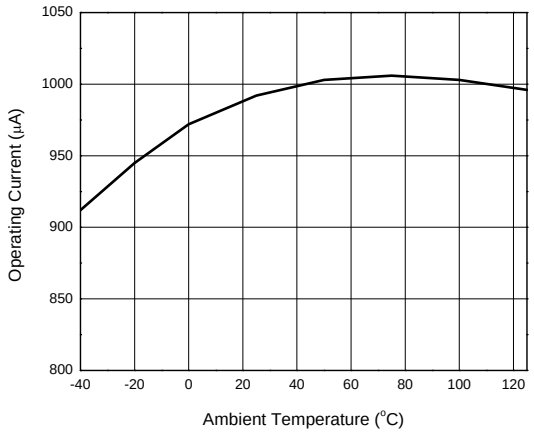
Minimal Operating Voltage vs. Ambient Temperature



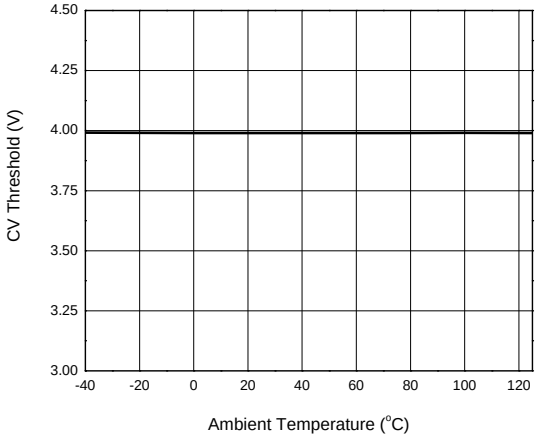
Start-up Current vs. Ambient Temperature



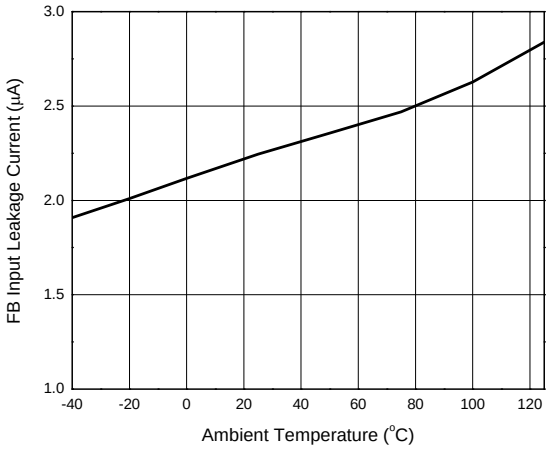
Operating Current vs. Ambient Temperature

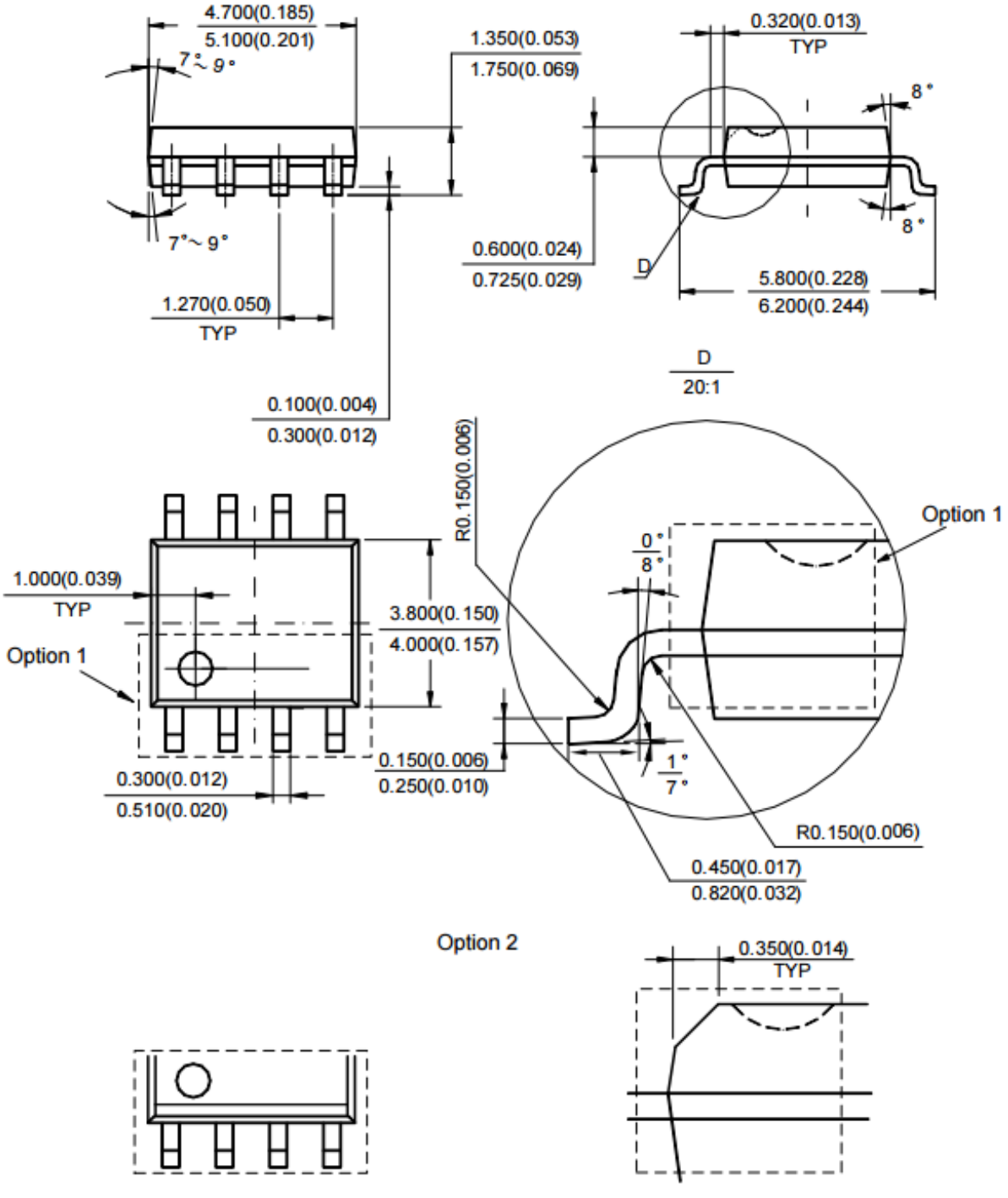


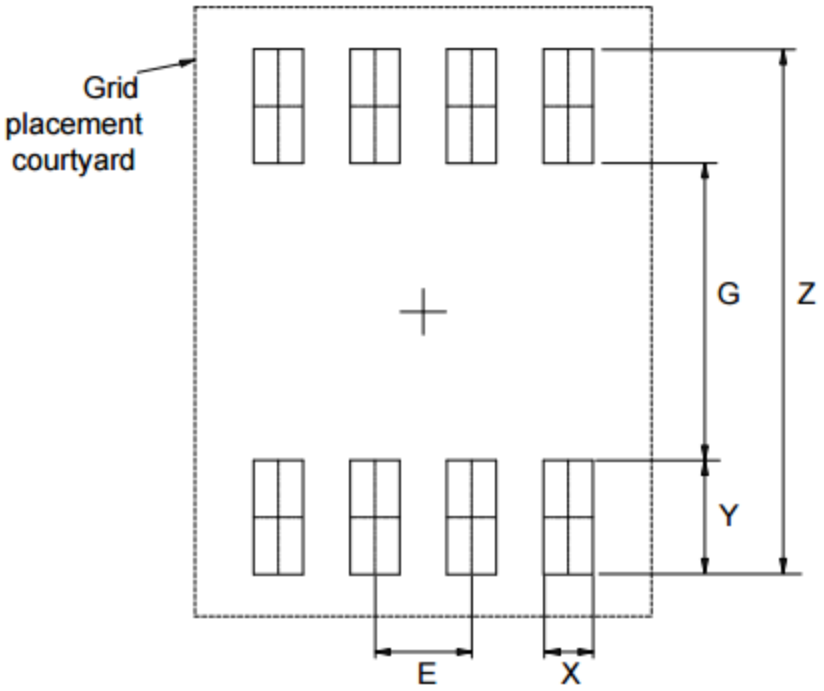
CV Threshold vs. Ambient Temperature



FB Input Leakage Current vs. Ambient Temperature




4.700(0.185)
5.100(0.201)
7°~9°
1.350(0.053)
1.750(0.069)
0.320(0.013)
TYP
8°
7°~9°
0.600(0.024)
0.725(0.029)
D
8°
5.800(0.228)
6.200(0.244)
1.270(0.050)
TYP
0.100(0.004)
0.300(0.012)
D
20:1
R0.150(0.006)
Option 1
0°
8°
1.000(0.039)
TYP
3.800(0.150)
4.000(0.157)
Option 1
0.150(0.006)
0.250(0.010)
1°
7°
0.300(0.012)
0.510(0.020)
R0.150(0.006)
0.450(0.017)
0.820(0.032)
Option 2
0.350(0.014)
TYP



Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E (mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050

以上信息仅供参考.如需帮助联系客服人员。谢谢 XINLUDA

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