

XD339 DIP14 XD239 DIP14 XD2901 DIP14 XL339 SOP14 XL239 SOP14 XL2901 SOP14

FEATURES

- Wide Supply Ranges
 - Single Supply: 2 V to 36 V (Tested to 30 V for Non-V Devices and 32 V for V-Suffix Devices)
 - Dual Supplies: ±1 V to ±18 V (Tested to ±15 V for Non-V Devices and ±16 V for V-Suffix Devices)
- Low Supply-Current Drain Independent of Supply Voltage: 0.8 mA (Typ)

- Low Input Bias Current: 25 nA (Typ)
- Low Input Offset Current: 3 nA (Typ) (139)
- Low Input Offset Voltage: 2 mV (Typ)
- Common-Mode Input Voltage Range Includes Ground
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage: ±36 V
- Low Output Saturation Voltage
- Output Compatible With TTL, MOS, and CMOS

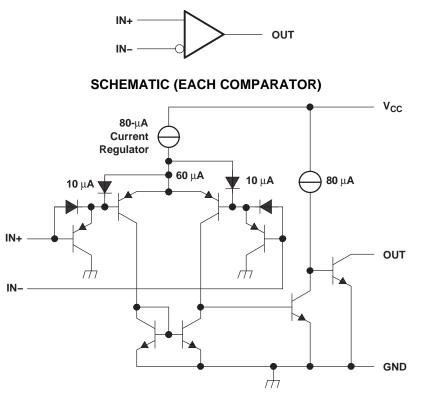
DESCRIPTION/ORDERING INFORMATION

These devices consist of four independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible, as long as the difference between the two supplies is 2 V to 36 V, and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wired-AND relationships.

The XD239 and XL239 are characterized for operation from –25°C to 125°C. The XD339 and XL339 are characterized for operation from 0°C to 70°C. The 2901 are characterized for operation from –40°C to 125°C.

10UT [20UT [2IN- [2IN- [2IN+ [1IN- [1 2 3 4 5 6 7	υ	14 13 12 11 10 9] OUT3] OUT4] GND] 4IN+] 4IN-] 3IN+
1IN+[0 7		9 8] 3IN+] 3IN-

SYMBOL (EACH COMPARATOR)



All current values shown are nominal.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT	
V_{CC}	Supply voltage ⁽²⁾			36	V	
V _{ID}	Differential input voltage ⁽³⁾			±36	V	
VI	Input voltage range (either input)		-0.3	36	V	
Vo	Output voltage			36	V	
I _O	Output current			20	mA	
	Duration of output short circuit to ground ⁽⁴⁾		Ur	limited		
		D package		86		
	Package thermal impedance, junction to free $air^{(5)}$ $^{(6)}$	DB package		96		
θ_{JA}		N package		80	°C/W	
		NS package		76		
		PW package		113		
		FK package		5.61		
θ_{JC}	Package thermal impedance, junction to case ^{(7) (8)}	J package		15.05	°C/W	
		W package		14.65		
TJ	Operating virtual-junction temperature			150	°C	
	Case temperature for 60 s	FK package		260	°C	
	Lead temperature 1,6 mm (1/16 in) from case for 60 s	J package		300	°C	
T _{stg}	Storage temperature range		-65	150	°C	

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

All voltage values, except differential voltages, are with respect to network ground. (2)

Differential voltages are at IN+ with respect to IN-. (3)

Short circuits from outputs to $V_{\mbox{\scriptsize CC}}$ can cause excessive heating and eventual destruction. (4)

Short circuits from outputs to v_{CC} can cause excessive freaming and eventual destruction. Maximum power dissipation is a function of T_J (max), θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J (max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability. The package thermal impedance is calculated in accordance with JESD 51-7. Maximum power dissipation is a function of $T_J (max)$, θ_{JC} , and T_C . The maximum allowable power dissipation at any allowable case (5)

(6)

(7)temperature is $P_D = (T_J (max) - T_C)/\theta_{JC}$. Operating at the absolute maximum T_J of 150°C can affect reliability. The package thermal impedance is calculated in accordance with MIL-STD-883.

(8)

ELECTRICAL CHARACTERISTICS

at specified free-air temperature, $V_{CC} = 5 V$ (unless otherwise noted)

	PARAMETER	TEST CON	IDITIONS ⁽¹⁾			XD239 XD339		XL239 XL339		UNIT	
					MIN	TYP	MAX	MIN	TYP	MAX	Í
		$V_{CC} = 5 V \text{ to } 3$	0 V,	25°C		2	5		1	3	
V _{IO}	Input offset voltage	$V_{IC} = V_{ICR} min,$ $V_{O} = 1.4 V$		Full range			9			4	mV
				25°C		5	50		5	50	~^
I _{IO}	Input offset current	V _O = 1.4 V		Full range			150			150	nA
	Innut biog ourrent			25°C		-25	-250		-25	-250	~^
I _{IB}	Input bias current	$v_0 = 1.4 v$	V _O = 1.4 V				-400			-400	nA
V	V _{ICR} Common-mode input- voltage range ⁽³⁾			25°C	0 to V _{CC} - 1.5			0 to V _{CC} - 1.5			v
VICR				Full range	0 to V _{CC} - 2			0 to V _{CC} - 2			V
A _{VD}	Large-signal differential- voltage amplification	$V_{CC} = 15 \text{ V},$ $V_{O} = 1.4 \text{ V to}$ $R_{L} \ge 15 \text{ k}\Omega \text{ to}$	11.4 V, V _{CC}	25°C	50	200		50	200		V/mV
	Lich lovel output ourrept	V 4.V	V _{OH} = 5 V	25°C		0.1	50		0.1	50	nA
I _{OH}	High-level output current	V _{ID} = 1 V	V _{OH} = 30 V	Full range			1			1	μA
V _{OL}	Low-level output voltage	$V_{ID} = -1 V$, $I_{OL} = 4 mA$	L _ 4 m^	25°C		150	400		150	400	mV
			1 _{OL} = 4 IIIA	Full range			700			700	mv
I _{OL}	Low-level output current	$V_{ID} = -1 V$,	V _{OL} = 1.5 V	25°C	6	16		6	16		mA
I _{CC}	Supply current (four comparators)	V _O = 2.5 V,	No load	25°C		0.8	2		0.8	2	mA

(1) All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

(2) Full range (MIN to MAX) for XD239/XL239 is -25°C to 85°C, and for XD339/XL339 is 0°C to 70°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

(3) The voltage at either input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V_{CC+} – 1.5 V; however, one input can exceed V_{CC}, and the comparator will provide a proper output state as long as the other input remains in the common-mode range. Either or both inputs can go to 30 V without damage.

SWITCHING CHARACTERISTICS

 $V_{CC} = 5 \text{ V}, \text{ T}_{A} = 25^{\circ}\text{C}$

PARAMETER	TEST C	TEST CONDITIONS		
Booponoo timo	R_L connected to 5 V through 5.1 kΩ, C_L = 15 pF ⁽¹⁾ (2)	100-mV input step with 5-mV overdrive	1.3	
Response time	$C_{L} = 15 \text{ pF}^{(1)}$ (2)	TTL-level input step	0.3	μs

(1) C_L includes probe and jig capacitance.

(2) The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.

ELECTRICAL CHARACTERISTICS

at specified free-air temperature, $V_{CC} = 5 V$ (unless otherwise noted)

				- (2)	2901				
PARAMETER		TEST CONDITIONS ⁽¹⁾		T _A ⁽²⁾	MIN	TYP	MAX	UNIT	
		V _{IC} = V _{ICR} min,	Non-A devices	25°C		2	7	mV	
				Full range			15		
V _{IO}	Input offset voltage	$V_{O} = 1.4 V,$ $V_{CC} = 5 V \text{ to MAX}^{(3)}$		25°C		1	2		
			A-suffix devices	Full range			4		
	Input offect ourrent			25°C		5	5 50		
I _{IO}	Input offset current	$v_0 = 1.4 v$	$V_0 = 1.4 V$				200	nA	
				25°C		-25	-250	- 4	
I _{IB}	Input bias current	$v_0 = 1.4 v$	V _O = 1.4 V				-500	nA	
Common-mode input- V _{ICR} voltage range ⁽⁴⁾				25°C	0 to V _{CC} - 1.5			V	
				Full range	0 to V _{CC} - 2				
A _{VD}	Large-signal differential- voltage amplification	$V_{CC} = 15 \text{ V}, \text{ V}_{O} = 1.4 \text{ V}$ $R_{L} \ge 15 \text{ k}\Omega \text{ to } \text{ V}_{CC}$	V_{CC} = 15 V, V_O = 1.4 V to 11.4 V, R _L ≥ 15 kΩ to V _{CC}		25	100		V/mV	
	LPak land and an entry		V _{OH} = 5 V	25°C		0.1	50	nA	
I _{OH}	High-level output current	$V_{ID} = 1 V$	$V_{OH} = V_{CC} MAX^{(3)}$	Full range			1	μA	
		$V_{ID} = -1 V,$ $I_{OL} = 4 mA$	Non-V devices	0500		150	500	mV	
V _{OL}	Low-level output voltage		V-suffix devices All devices	25°C		150	400		
				Full range			700		
I _{OL}	Low-level output current	$V_{ID} = -1 V,$	V _{OL} = 1.5 V	25°C	6	16		mA	
	Supply current	V _O = 2.5 V,	$V_{CC} = 5 V$	0590		0.8	2	mA	
I _{CC}	(four comparators)	No load	$V_{CC} = MAX^{(3)}$	25°C		1	2.5		

(1) All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

(2) Full range (MIN to MAX) for 2901 is -40°C to 125°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

(3) V_{CC} MAX = 30 V for non-V devices, and 32 V for V-suffix devices

(4) The voltage at either input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V_{CC+} – 1.5 V; however, one input can exceed V_{CC}, and the comparator will provide a proper output state as long as the other input remains in the common-mode range. Either or both inputs can go to V_{CC} MAX without damage.

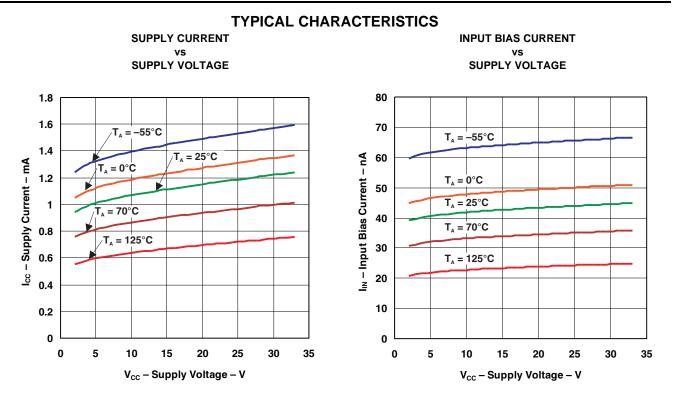
SWITCHING CHARACTERISTICS

 $V_{CC}=5~V,~T_{A}=25^{\circ}C$

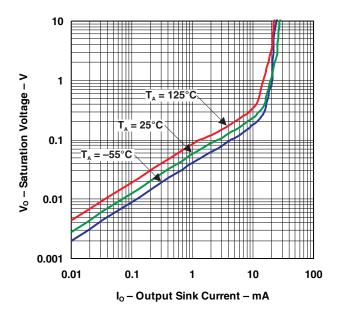
PARAMETER	TEST CON	2901		
PARAMETER	TEST CONL	TEST CONDITIONS		UNIT
Response time	$R_{\rm L}$ connected to 5 V through 5.1 k Ω ,	100-mV input step with 5-mV overdrive	1.3	
	$C_{L} = 15 \text{ pF}^{(1)}$ (2)	TTL-level input step	0.3	μs

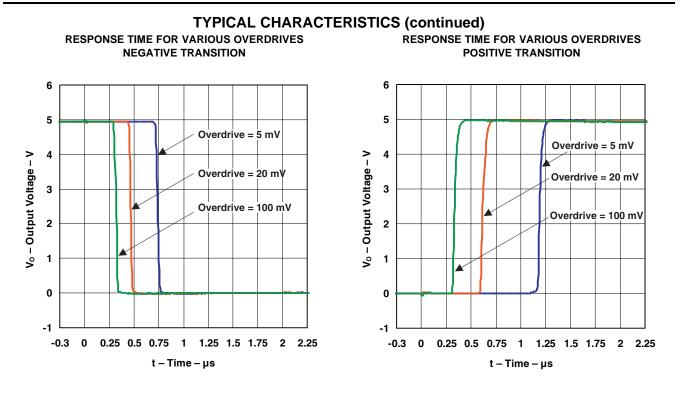
(1) C_L includes probe and jig capacitance.

(2) The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



OUTPUT SATURATION VOLTAGE





以上信息仅供参考. 如需帮助联系客服人员。谢谢 XINLUDA

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