

Description

The XL4310 is a monolithic IC specifically designed to regulate the output current and voltage levels of switching battery chargers and power supplies

The device contains two Op Amps and a 2.5V precision shunt voltage reference. Op Amp 1 is designed for voltage control with its non-inverting input internally connected to the output of the shunt regulator. Op Amp 2 is for current control with both inputs uncommitted. The IC offers the power converter designer a control solution that features increased precision with a corresponding reduction in system complexity and cost.

Features

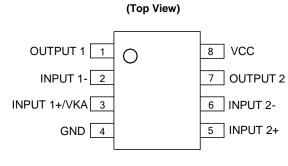
OP Amp

- Input Offset Voltage: 0.5mV
- Supply Current: 75µA per OP Amp at 5.0V Supply Voltage
- Unity Gain Bandwidth:1MHz
- Output Voltage Swing: 0 to V_{cc}-1.5V
- Power Supply Range: 3 to 36V

Voltage Reference

- Fixed Output Voltage Reference: 2.5V
- Reference Voltage Tolerance :±0.4%
- Sink Current Capability: 0.05 to 80mA
- Typical Output Impedance: 0.2Ω

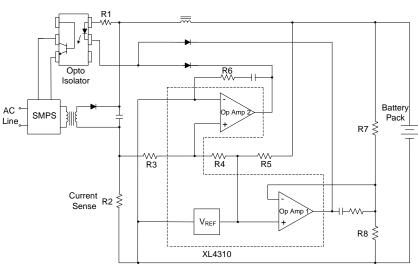
Pin Assignments



SO-8

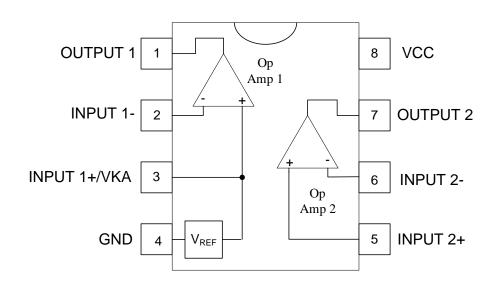
Applications

- Battery Charger
- Switching Power Supply



Application of XL4310 in a Constant Current and Constant Voltage Charger

Typical Applications Circuit



Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Rating	Unit
V _{CC}	Power Supply Voltage (VCC to GND)	40	V
V _{IN}	Op Amp1 and 2 Input Voltage Range (Pins 2, 5, 6)	-0.3 to V _{CC} +0.3	V
V _{ID}	Op Amp 2 Input Differential Voltage (Pins 5, 6)	40	V
Ιĸ	Voltage Reference Cathode Current (Pin 3)	100	mA
PD	Power Dissipation ($T_A = +25^{\circ}C$)	500	mW
TJ	Operating Junction Temperature	+150	°C
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering 10sec)	+260	°C
ESD	ESD (Human Body Model)	≥2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Parameter	Min	Мах	Unit
Supply Voltage	3	36	V
Ambient Temperature	-40	+105	°C

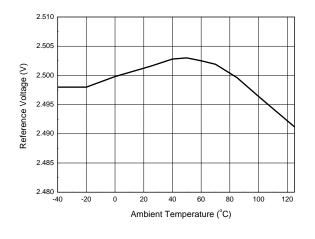
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Electrical Characteristics (@V_{CC}=5V, T_A=+25°C, unless otherwise specified.)

Para	Parameters Conditions		Min	Тур	Max	Unit		
Total Supply Current, excluding Current in Voltage Reference		V_{CC} =5V, no load, -40°C ≤ T_A ≤ +105°C		-	0.15	0.25	~^^	
		V_{CC} =30V, no load, -40°C ≤ T_A ≤ +105°C		-	0.20	0.30	mA	
Voltage Reference	Section							
Reference Voltage			T _A = +25°C	2.49	2.50	2.51	V	
		I _K =10mA	$-40^{\circ}\mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq +105^{\circ}\mathrm{C}$	2.48	2.50	2.52		
Reference Voltage Deviation over Full Temperature Range		°C to +105°C	-	5	24	mV		
Minimum Cathode Current for Regulation –		-	0.01	0.05	mA			
Dynamic Impedance I _K =1mA to 80mA, f<1kHz		-	0.2	0.5	Ω			
Op Amp 1 Section	(V _{CC} = 5V, V _O = 1.4V, T	$A = +25^{\circ}C$, unless o	therwise noted.)			·		
		T _A = +25°C		-	0.5	3		
Input Offset Voltage		T _A = -40°C to +105°C –		-	_	5	mV	
Input Offset Voltage	Temperature Drift	T _A = -40°C to +105°C		-	7	-	μV/°C	
Input Bias Current (I	nverting Input Only)	T _A = +25°C		-	20	150	nA	
Large Signal Voltage	e Gain	$V_{CC} = 15V, R_L = 2$	$k\Omega$, V _O = 1.4V to 11.4V	85	100	_	dB	
Power Supply Rejec	tion Ratio	$V_{CC} = 5V$ to 30V		70	90	-	dB	
<u> </u>	Source	$V_{CC} = 15V, V_{ID} = 100$	1V, V _O = 2V	20	40	-	mA	
Output Current	Sink	V _{CC} = 15V, V _{ID} = -	-1V, V _O = 2V	5	20	-		
Output Voltage Swin	ıg (High)	$V_{CC} = 30V, R_L = 10k\Omega, V_{ID} = 1V$		27	28	-	V	
Output Voltage Swing (Low)		$V_{CC} = 30V, R_L = 10k\Omega, V_{ID} = -1V$		-	17	100	mV	
Slew Rate		$V_{CC} = 18V, R_L = 2k\Omega, A_V = 1,$ $V_{IN} = 0.5V \text{ to } 2V, C_L = 100\text{pF}$		0.2	0.5	-	V/µs	
Unity Gain Bandwidth		$V_{CC} = 30V, R_L = 2k\Omega, C_L = 100pF$		0.7	1.0	-	MHz	
Op Amp 2 Section	(V _{CC} = 5V, V _O = 1.4V, T	$A = +25^{\circ}C$, unless c	otherwise noted.)					
Input Offset Voltage		T _A = +25°C		- 0.5 3 5		3		
		$T_A = -40^{\circ}C \text{ to } +10^{\circ}C$	5°C			- mV		
Input Offset Voltage Temperature Drift		$T_A = -40^{\circ}C \text{ to } +10^{\circ}C$	5°C	_	7	-	μV/°C	
Input Offset Current		T _A = +25°C		-	2	30	nA	
Input Bias Current		$T_A = +25^{\circ}C$		-	20	150	nA	
Input Voltage Range		V _{CC} = 0 to 36V		0	_	V _{CC} -1.5	V	
Common Mode Rejection Ratio		$T_A = +25^{\circ}C$, $V_{CM} = 0$ to 3.5V		70	85	_	dB	
Large Signal Voltage Gain		$V_{CC} = 15V, R_L = 2k\Omega, V_O = 1.4V$ to 11.4V		85	100	-	dB	
Power Supply Rejection Ratio		$V_{CC} = 5V$ to 30V		70	90	-	dB	
Output Current	Source	$V_{CC} = 15V, V_{ID} =$	1V, V ₀ = 2V	20	40	-	- mA	
	Sink	$V_{CC} = 15V, V_{ID} = -15V$	-1V, V _O = 2V	5	20	-		
Output Voltage Swing (High)		V_{CC} = 30V, R_L = 10k Ω , V_{ID} = 1V		27	28	-	V	
Output Voltage Swin	ıg (Low)	$V_{CC} = 30V, R_L = 10k\Omega, V_{ID} = -1V$		-	17	100	mV	
Slew Rate		$V_{CC} = 18V, R_L = 2k\Omega, A_V = 1,$ $V_{IN} = 0.5V$ to 2V, $C_L = 100pF$		0.2	0.5	_	V/µs	
Unity Gain Bandwidth		$V_{CC} = 30V, R_L = 2k\Omega, C_L = 100pF$		0.7	1.0	_	MHz	

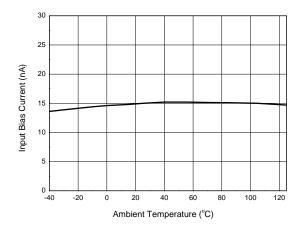
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Performance Characteristics



Reference Voltage vs. Ambient Temperature

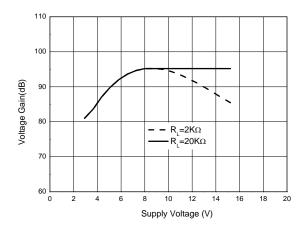
Input Bias Current vs. Ambient Temperature



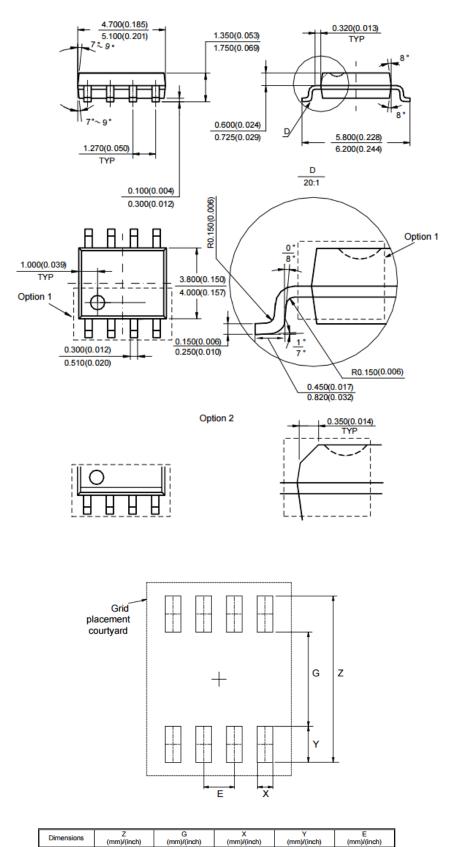
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Cathode Current vs. Cathode Voltage

Op Amp Voltage Gain



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以上后志以供参考 如盖带切联条各成人员。 例例 AINLUL	以	上信息仅供参考.	如需帮助联系客服人员。	谢谢	XINLUD
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0.650/0.026

1.500/0.059

1.270/0.050

3.900/0.154

6.900/0.272

Value

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